SPEC® CINT2006 Result

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Bull SAS

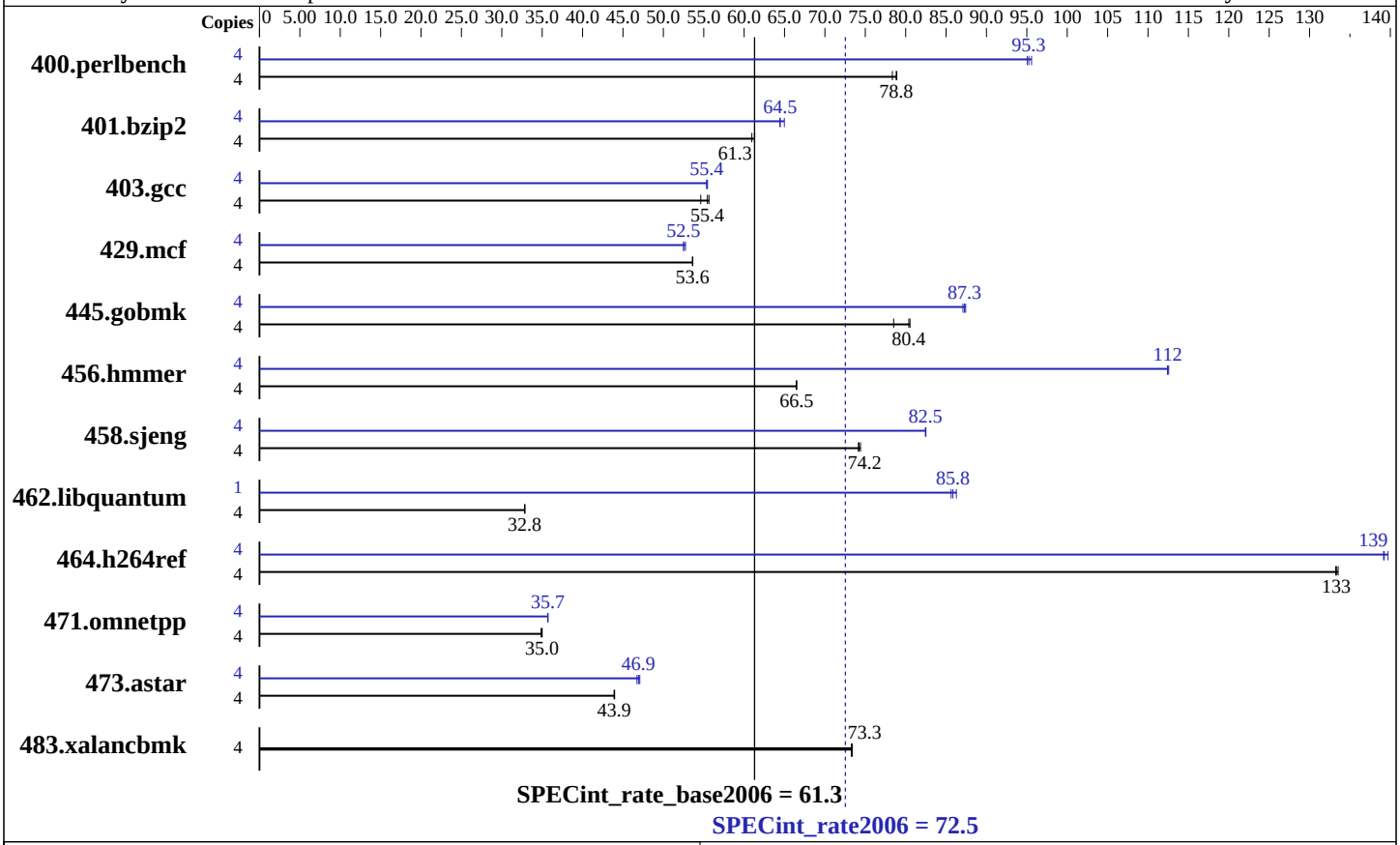
NovaScale T860 E1
(Intel Xeon X5460,3.16GHz)

SPECint®_rate2006 = 72.5

SPECint_rate_base2006 = 61.3

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: NEC Corporation

Test date: May-2008
Hardware Availability: Apr-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5460
CPU Characteristics:	3.16 GHz, 2x6 MB L2 shared, 1333 MHz bus
CPU MHz:	3167
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x73.2 GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**496**	**78.8**	499	78.3	495	78.9	4	409	95.6	411	95.0	**410**	**95.3**
401.bzip2	4	633	61.0	630	61.3	**630**	**61.3**	4	594	65.0	**599**	**64.5**	599	64.4
403.gcc	4	589	54.6	579	55.6	**581**	**55.4**	4	581	55.5	582	55.4	**582**	**55.4**
429.mcf	4	680	53.6	**680**	**53.6**	681	53.6	4	**695**	**52.5**	692	52.7	695	52.5
445.gobmk	4	534	78.5	**522**	**80.4**	521	80.5	4	482	87.1	**481**	**87.3**	480	87.4
456.hmmer	4	**561**	**66.5**	561	66.5	562	66.4	4	**332**	**112**	332	112	332	113
458.sjeng	4	**652**	**74.2**	650	74.4	653	74.1	4	587	82.5	587	82.4	**587**	**82.5**
462.libquantum	4	**2524**	**32.8**	2523	32.8	2524	32.8	1	242	85.6	**241**	**85.8**	240	86.3
464.h264ref	4	663	134	664	133	**664**	**133**	4	**636**	**139**	636	139	634	140
471.omnetpp	4	715	35.0	717	34.9	**715**	**35.0**	4	700	35.7	**700**	**35.7**	700	35.7
473.astar	4	**639**	**43.9**	639	44.0	640	43.9	4	596	47.1	**598**	**46.9**	601	46.7
483.xalancbmk	4	376	73.4	**377**	**73.3**	377	73.3	4	376	73.4	**377**	**73.3**	377	73.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
```

Platform Notes

```
Bios settings:
Hardware Prefetcher:          Enabled
Adjacent Cache Line Prefetch: Disabled
Intel SpeedStep Technology:   Disabled
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

 The NEC Express5800/120Lj(Intel Xeon X5460) and
 the Bull NovaScale T860 E1(Intel Xeon X5460,3.16GHz) models are electronically equivalent.
 The results have been measured on a NEC Express5800/120Lj(Intel Xeon X5460) model.
```

Base Compiler Invocation

C benchmarks:

```
icc
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.xml`

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