



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = 12.3

IBM BladeCenter HS12 (Intel Celeron 445)

SPECint_rate_base2006 = 10.9

CPU2006 license: 11

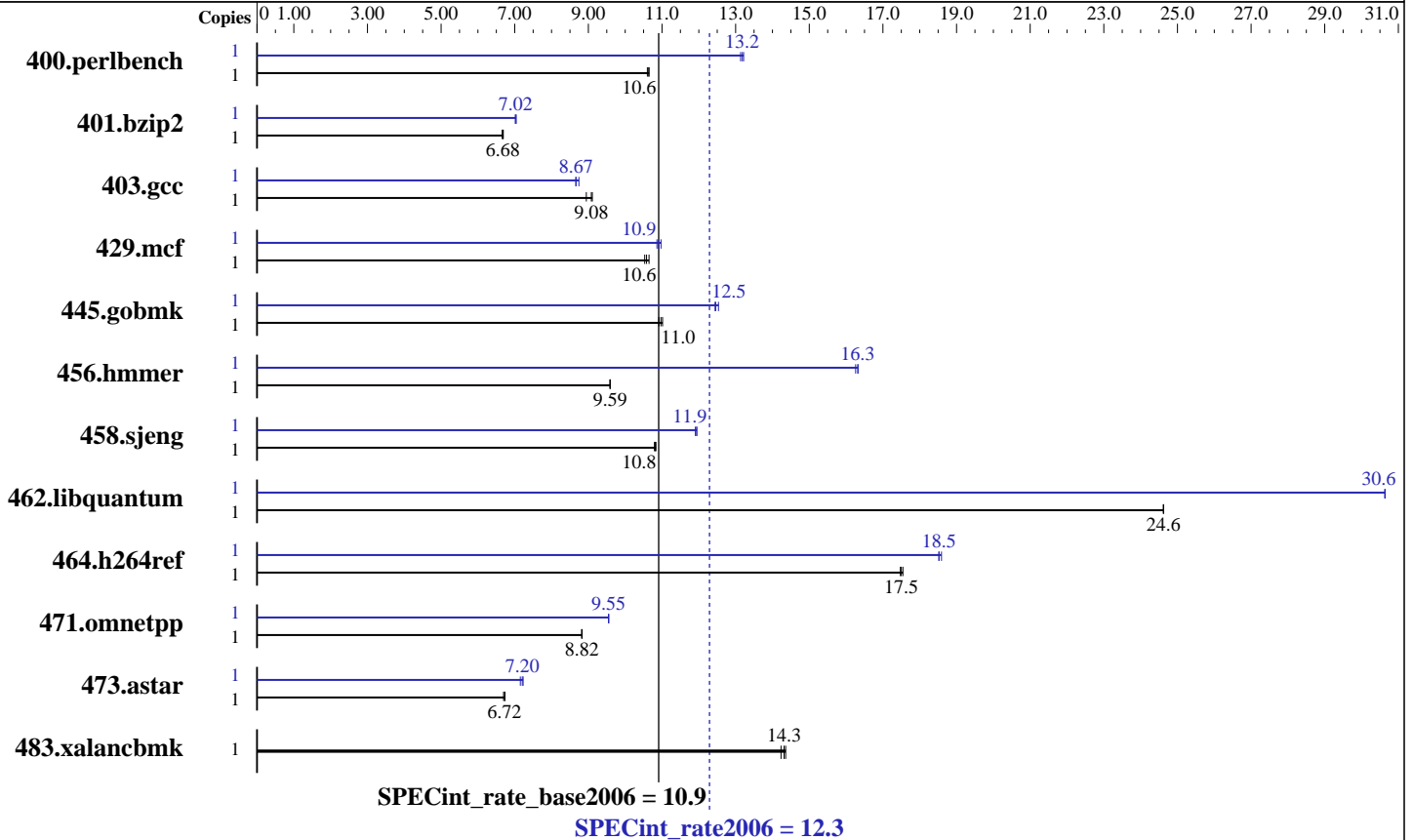
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: May-2008

Hardware Availability: May-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Celeron 445
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per chip
 Secondary Cache: 512 KB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR2-5300 ECC)
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1	921	10.6	<u>918</u>	<u>10.6</u>	918	10.6	1	739	13.2	<u>741</u>	<u>13.2</u>	744	13.1
401.bzip2	1	1449	6.66	1443	6.69	<u>1445</u>	<u>6.68</u>	1	1371	7.04	1375	7.02	<u>1374</u>	<u>7.02</u>
403.gcc	1	<u>887</u>	<u>9.08</u>	900	8.94	884	9.11	1	930	8.66	<u>929</u>	<u>8.67</u>	921	8.74
429.mcf	1	866	10.5	857	10.6	<u>862</u>	<u>10.6</u>	1	831	11.0	<u>836</u>	<u>10.9</u>	840	10.9
445.gobmk	1	952	11.0	961	10.9	<u>956</u>	<u>11.0</u>	1	837	12.5	<u>842</u>	<u>12.5</u>	843	12.4
456.hmmmer	1	973	9.59	<u>973</u>	<u>9.59</u>	973	9.59	1	<u>572</u>	<u>16.3</u>	572	16.3	574	16.3
458.sjeng	1	1117	10.8	1121	10.8	<u>1119</u>	<u>10.8</u>	1	1016	11.9	1013	11.9	<u>1013</u>	<u>11.9</u>
462.libquantum	1	841	24.6	842	24.6	<u>842</u>	<u>24.6</u>	1	<u>676</u>	<u>30.6</u>	676	30.6	676	30.6
464.h264ref	1	1261	17.5	<u>1265</u>	<u>17.5</u>	1266	17.5	1	1190	18.6	1195	18.5	<u>1195</u>	<u>18.5</u>
471.omnetpp	1	<u>708</u>	<u>8.82</u>	709	8.82	708	8.83	1	<u>654</u>	<u>9.55</u>	655	9.55	654	9.55
473.astar	1	1042	6.73	<u>1044</u>	<u>6.72</u>	1048	6.70	1	982	7.15	971	7.23	<u>974</u>	<u>7.20</u>
483.xalancbmk	1	<u>482</u>	<u>14.3</u>	481	14.4	485	14.2	1	<u>482</u>	<u>14.3</u>	481	14.4	485	14.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Enabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.xml>

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