SPEC® CINT2006 Result

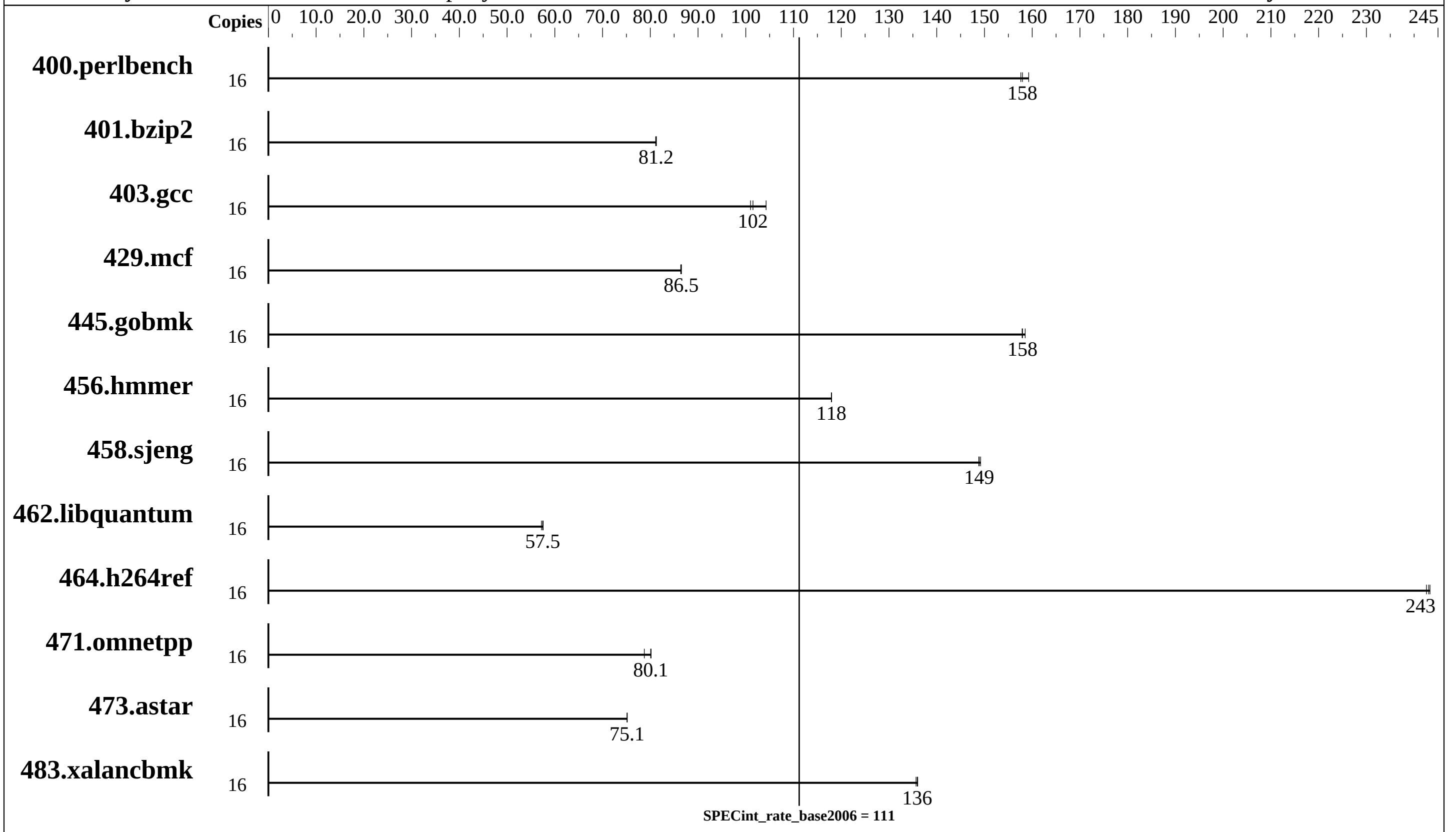
Hewlett-Packard Company

ProLiant BL680c
(1.60 GHz, Intel Xeon E7310)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 111

CPU2006 license:	3	**Test date:**	May-2008
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Jan-2008
Tested by:	Hewlett-Packard Company	**Software Availability:**	Nov-2007



Hardware

CPU Name:	Intel Xeon E7310
CPU Characteristics:	1.60 GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	1,2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	64 GB (16x4 GB PC2-5300F CL5)
Disk Subsystem:	1x72 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	No
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.1 binutils-2.17.50

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	982	159	**990**	**158**	992	158							
401.bzip2	16	1903	81.1	**1901**	**81.2**	1900	81.3							
403.gcc	16	1235	104	1275	101	**1269**	**102**							
429.mcf	16	1689	86.4	**1687**	**86.5**	1687	86.5							
445.gobmk	16	**1062**	**158**	1059	159	1063	158							
456.hmmer	16	1266	118	1265	118	**1266**	**118**							
458.sjeng	16	1301	149	1298	149	**1300**	**149**							
462.libquantum	16	5794	57.2	**5769**	**57.5**	5760	57.6							
464.h264ref	16	**1457**	**243**	1460	243	1455	243							
471.omnetpp	16	1270	78.7	**1249**	**80.1**	1247	80.2							
473.astar	16	**1495**	**75.1**	1495	75.1	1494	75.2							
483.xalancbmk	16	814	136	812	136	**812**	**136**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch Disabled
  Hardware Prefetcher Disabled
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/cpu2006/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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