SPEC® CINT2006 Result

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Itautec

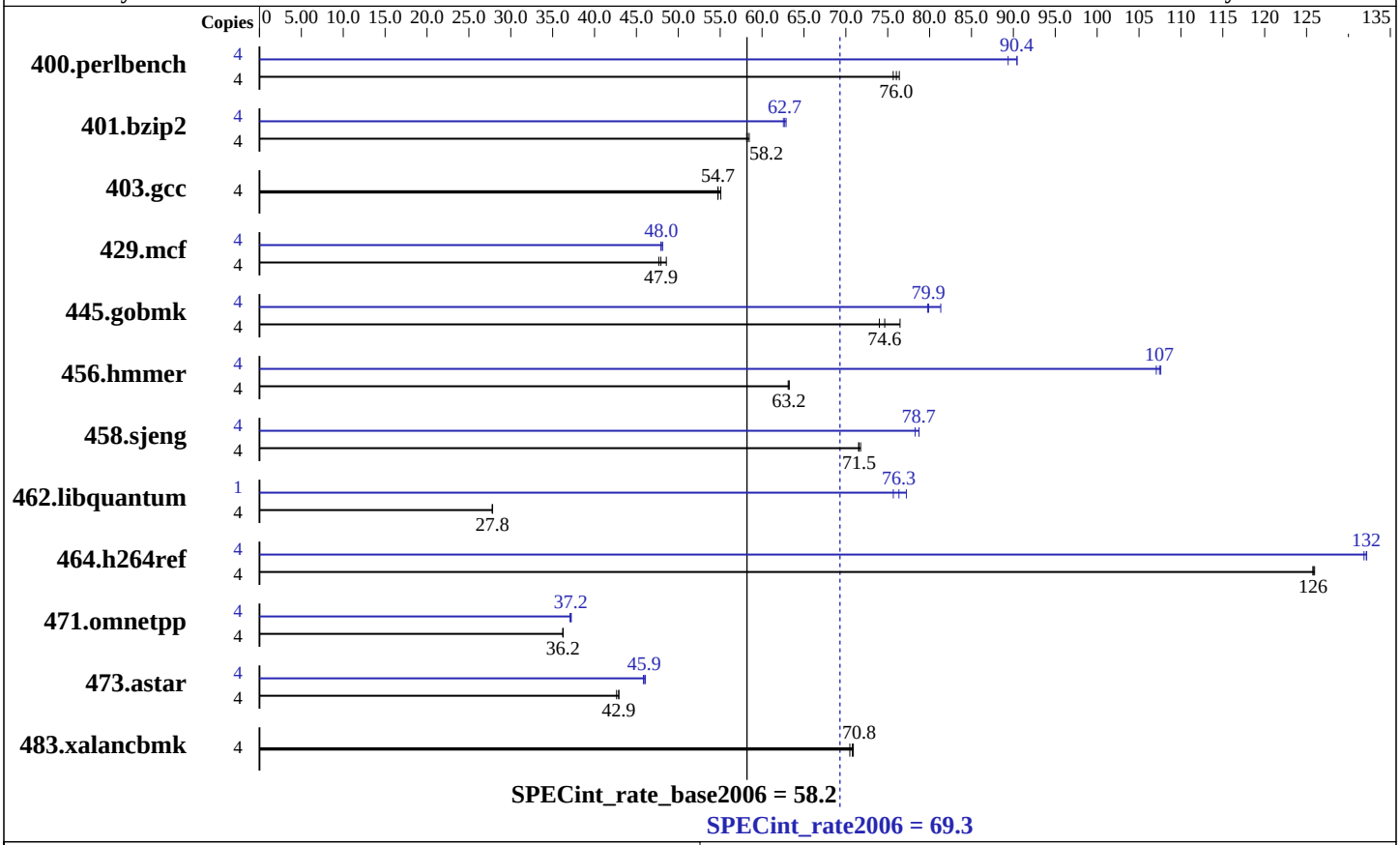
Servidor Itautec LX211 (Intel Xeon X5460)

SPECint®_rate2006 = 69.3

SPECint_rate_base2006 = 58.2

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: May-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008



Hardware

CPU Name:	Intel Xeon X5460
CPU Characteristics:	
CPU MHz:	3160
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	1 x SCSI, 73GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run Level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.10.50 MicroQuill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**514**	**76.0**	512	76.4	517	75.7	4	432	90.4	437	89.4	**432**	**90.4**
401.bzip2	4	660	58.5	664	58.2	**663**	**58.2**	4	**616**	**62.7**	617	62.5	614	62.9
403.gcc	4	588	54.7	585	55.1	**588**	**54.7**	4	588	54.7	585	55.1	**588**	**54.7**
429.mcf	4	751	48.6	**761**	**47.9**	765	47.7	4	761	47.9	758	48.1	**760**	**48.0**
445.gobmk	4	567	74.0	549	76.5	**562**	**74.6**	4	516	81.3	526	79.8	**525**	**79.9**
456.hmmer	4	591	63.1	**590**	**63.2**	590	63.2	4	349	107	347	108	**347**	**107**
458.sjeng	4	**676**	**71.5**	677	71.5	674	71.8	4	618	78.3	**615**	**78.7**	615	78.7
462.libquantum	4	2984	27.8	2978	27.8	**2979**	**27.8**	1	**271**	**76.3**	274	75.7	268	77.2
464.h264ref	4	704	126	703	126	**704**	**126**	4	671	132	**670**	**132**	670	132
471.omnetpp	4	**690**	**36.2**	690	36.2	690	36.2	4	672	37.2	674	37.1	**673**	**37.2**
473.astar	4	659	42.6	**655**	**42.9**	654	42.9	4	609	46.1	613	45.8	**612**	**45.9**
483.xalancbmk	4	389	70.9	392	70.5	**390**	**70.8**	4	389	70.9	392	70.5	**390**	**70.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.
```

Platform Notes

```
Hardware Prefetch Enabled
```

Base Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.xml`

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