SPEC® CINT2006 Result
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Itautec

SPECint®_rate2006 =   127

Servidor Itautec LX211 (Intel Xeon X5450)

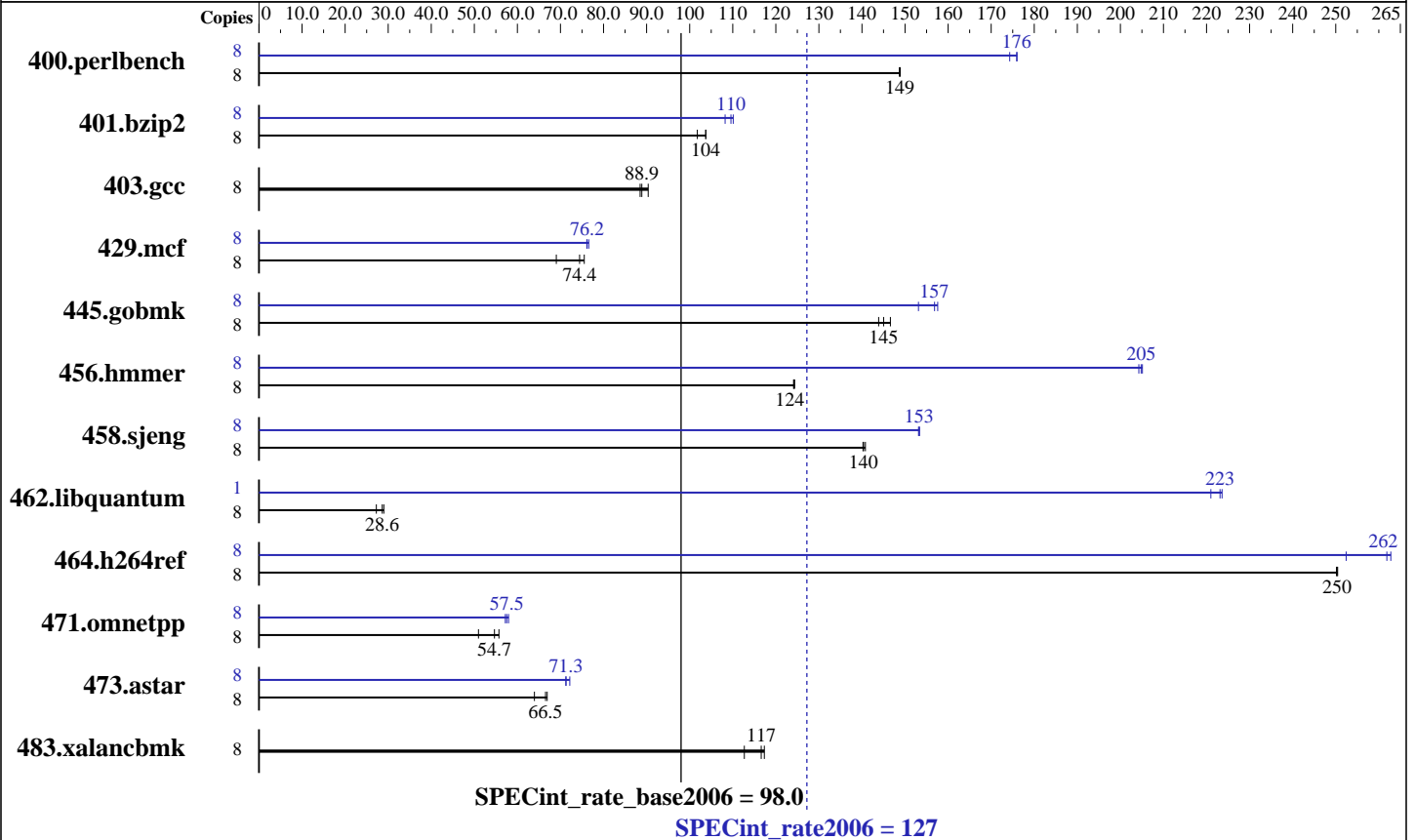SPECint_rate_base2006 = 98.0

CPU2006 license: 9001　　　　　　　　　　　　　　　　**Test date:** May-2008
Test sponsor: Itautec　　　　　　　　　　　　**Hardware Availability:** Dec-2007
Tested by: Itautec　　　　　　　　　　　　　**Software Availability:** Jan-2008

Benchmark	Copies	Base	Peak
400.perlbench	8	149	176
401.bzip2	8	104	110
403.gcc	8	88.9	
429.mcf	8	74.4	76.2
445.gobmk	8	145	157
456.hmmer	8	124	205
458.sjeng	8	140	153
462.libquantum	1 / 8	28.6	223
464.h264ref	8	250	262
471.omnetpp	8	54.7	57.5
473.astar	8	66.5	71.3
483.xalancbmk	8	117	

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Hardware

CPU Name: Intel Xeon X5450
CPU Characteristics:
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem: 1 x SCSI, 73GB, 10000 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
Auto Parallel: Yes
File System: ReiserFS
System State: Run Level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.10.50
MicroQuill SmartHeap V8.1

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	525	149	**525**	**149**	526	149	8	448	174	**444**	**176**	444	176
401.bzip2	8	759	102	**744**	**104**	744	104	8	**704**	**110**	713	108	701	110
403.gcc	8	728	88.5	**724**	**88.9**	713	90.4	8	728	88.5	**724**	**88.9**	713	90.4
429.mcf	8	1057	69.0	**980**	**74.4**	966	75.5	8	953	76.6	958	76.1	**958**	**76.2**
445.gobmk	8	**579**	**145**	583	144	572	147	8	533	158	**535**	**157**	548	153
456.hmmer	8	601	124	600	124	**601**	**124**	8	364	205	365	204	**364**	**205**
458.sjeng	8	690	140	**690**	**140**	687	141	8	632	153	631	153	**632**	**153**
462.libquantum	8	6083	27.2	**5793**	**28.6**	5718	29.0	1	93.8	221	92.7	224	**92.9**	**223**
464.h264ref	8	**707**	**250**	708	250	707	250	8	**676**	**262**	701	252	674	263
471.omnetpp	8	981	51.0	**915**	**54.7**	897	55.7	8	863	57.9	**869**	**57.5**	874	57.2
473.astar	8	878	64.0	**844**	**66.5**	839	66.9	8	778	72.2	**788**	**71.3**	788	71.3
483.xalancbmk	8	490	113	**473**	**117**	470	117	8	490	113	**473**	**117**	470	117

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch Enabled
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.012/bin/icc
           -L/opt/intel/cce/10.1.012/lib
           -I/opt/intel/cce/10.1.012/include
```

```
456.hmmer: /opt/intel/cce/10.1.012/bin/icc
           -L/opt/intel/cce/10.1.012/lib
           -I/opt/intel/cce/10.1.012/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast` `-ansi-alias`
`-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast` `-prefetch`

403.gcc: basepeak = yes

429.mcf: `-fast` `-prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT` `-O2` `-ipo`
`-no-prec-div` `-ansi-alias`

456.hmmer: `-fast` `-unroll2` `-ansi-alias` `-opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast` `-unroll4`

462.libquantum: `-fast` `-unroll4` `-Ob0` `-prefetch`
`-opt-streaming-stores always` `-vec-guard-write`
`-opt-malloc-options=3` `-parallel` `-par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast` `-unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT` `-O3` `-ipo`
`-no-prec-div` `-ansi-alias` `-opt-ra-region-strategy=block`
`-Wl,-z,muldefs` `-L/opt/sh/SmartHeap_8.1/lib` `-lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT` `-O3` `-ipo`
`-no-prec-div` `-ansi-alias` `-opt-ra-region-strategy=routine`
`-Wl,-z,muldefs` `-L/opt/sh/SmartHeap_8.1/lib` `-lsmartheap`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.xml

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