SPEC® CINT2006 Result

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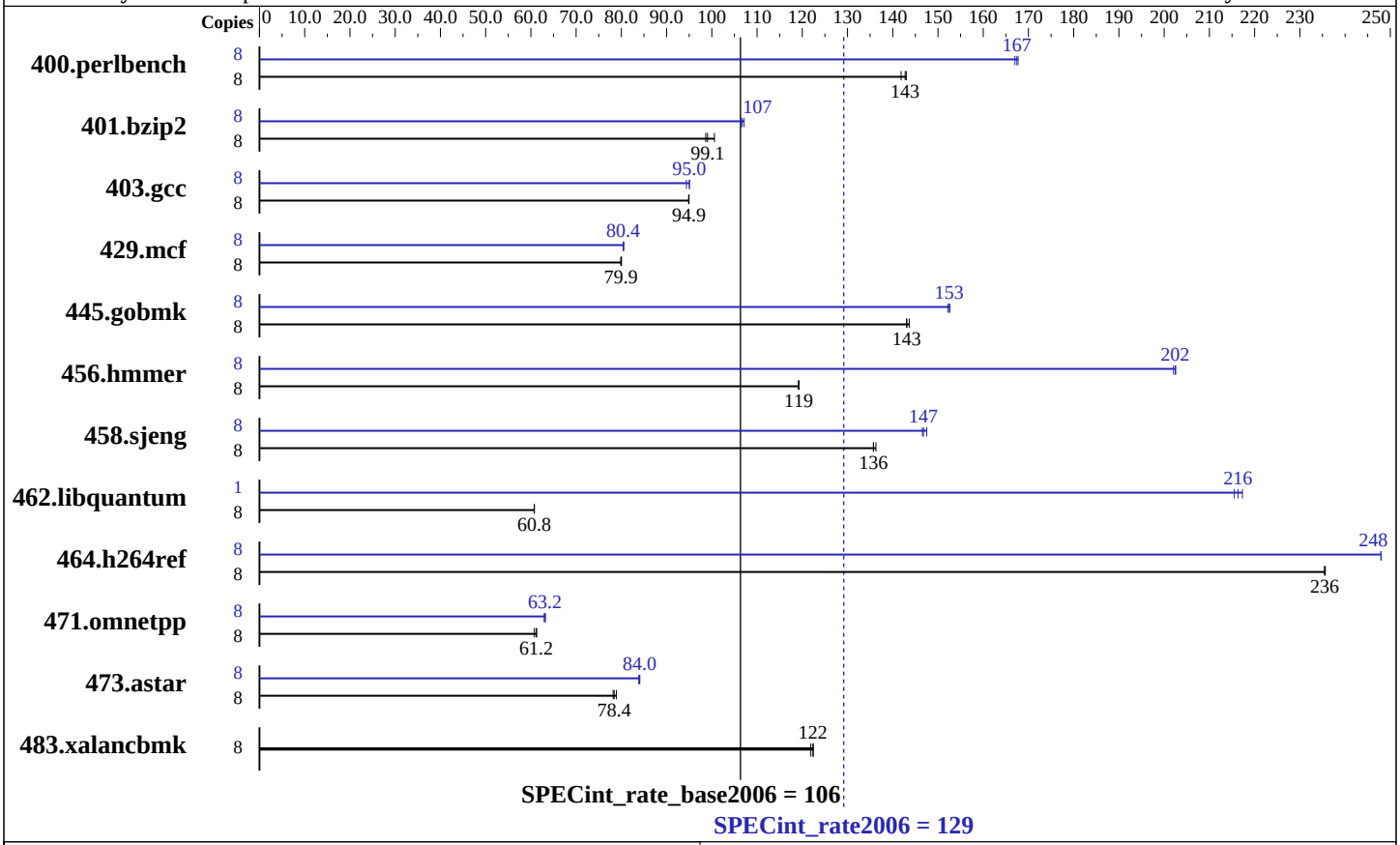
Supermicro Motherboard X7DWT

SPECint®_rate2006 = 129

SPECint_rate_base2006 = 106

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	Quad Core, 2.83 GHz, 1333 MHz System Bus
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 X 2 GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem:	SAMSUNG HD501LJ 500 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	64-Bit SUSE Linux Enterprise Server 10 SP1
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	551	142	547	143	**548**	**143**	8	466	168	468	167	**467**	**167**
401.bzip2	8	**779**	**99.1**	783	98.7	768	101	8	721	107	**723**	**107**	725	107
403.gcc	8	**678**	**94.9**	678	95.0	679	94.9	8	682	94.4	**678**	**95.0**	678	95.0
429.mcf	8	**913**	**79.9**	913	79.9	912	80.0	8	906	80.6	907	80.4	**907**	**80.4**
445.gobmk	8	587	143	584	144	**586**	**143**	8	551	152	550	153	**550**	**153**
456.hmmer	8	626	119	**626**	**119**	626	119	8	**369**	**202**	368	203	369	202
458.sjeng	8	713	136	**713**	**136**	710	136	8	**659**	**147**	656	148	660	147
462.libquantum	8	**2729**	**60.8**	2729	60.7	2728	60.8	1	95.3	217	**95.8**	**216**	96.1	216
464.h264ref	8	**752**	**236**	751	236	752	235	8	**714**	**248**	714	248	714	248
471.omnetpp	8	816	61.3	**817**	**61.2**	823	60.8	8	794	62.9	**792**	**63.2**	791	63.2
473.astar	8	**716**	**78.4**	712	78.9	718	78.2	8	670	83.8	**668**	**84.0**	668	84.1
483.xalancbmk	8	453	122	451	122	**451**	**122**	8	453	122	451	122	**451**	**122**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, which are compiled in 64-bit mode
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2:
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

456.hmmer:
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.xml`

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