SPEC® CINT2006 Result

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Itautec

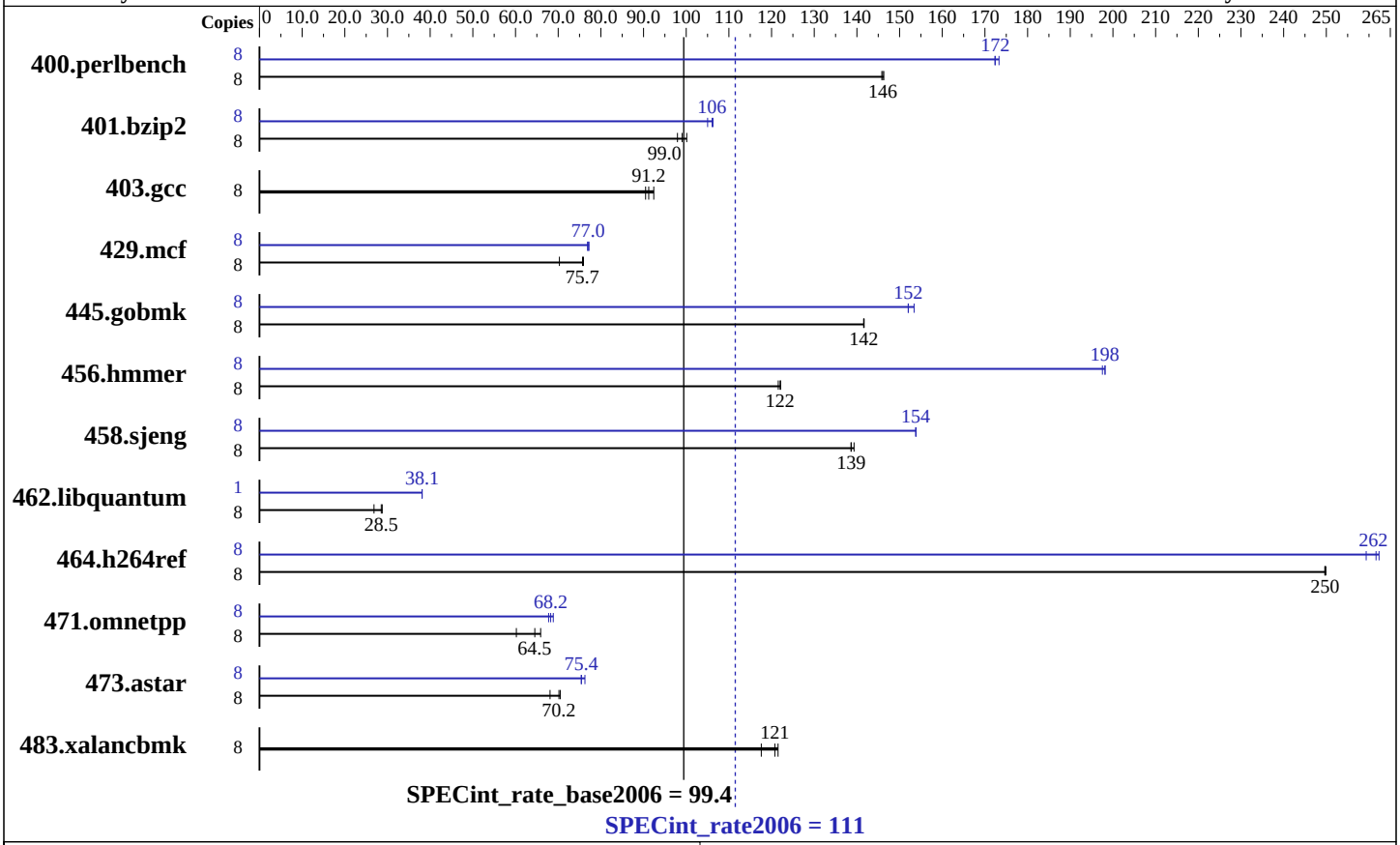
Servidor Itautec LX211 (Intel Xeon X5450)

SPECint®_rate2006 = 111

SPECint_rate_base2006 = 99.4

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: May-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008



Hardware

CPU Name:	Intel Xeon X5450
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	1 x SCSI, 73GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run Level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.10.50 MicroQuill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**535**	**146**	534	146	536	146	8	454	172	451	173	**453**	**172**
401.bzip2	8	788	97.9	**780**	**99.0**	771	100	8	735	105	**728**	**106**	726	106
403.gcc	8	712	90.5	**706**	**91.2**	697	92.4	8	712	90.5	**706**	**91.2**	697	92.4
429.mcf	8	1038	70.3	**964**	**75.7**	961	75.9	8	945	77.2	**947**	**77.0**	949	76.9
445.gobmk	8	593	142	592	142	**593**	**142**	8	547	153	552	152	**552**	**152**
456.hmmer	8	614	122	**612**	**122**	611	122	8	378	198	377	198	**377**	**198**
458.sjeng	8	698	139	**698**	**139**	695	139	8	629	154	629	154	**629**	**154**
462.libquantum	8	6176	26.8	**5809**	**28.5**	5763	28.8	1	**544**	**38.1**	544	38.1	544	38.1
464.h264ref	8	709	250	**709**	**250**	708	250	8	683	259	675	262	**677**	**262**
471.omnetpp	8	831	60.2	**775**	**64.5**	759	65.9	8	726	68.9	**733**	**68.2**	738	67.8
473.astar	8	825	68.1	**800**	**70.2**	797	70.5	8	736	76.3	745	75.4	**744**	**75.4**
483.xalancbmk	8	469	118	**457**	**121**	454	122	8	469	118	**457**	**121**	454	122

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.xml`

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