SPEC® CINT2006 Result

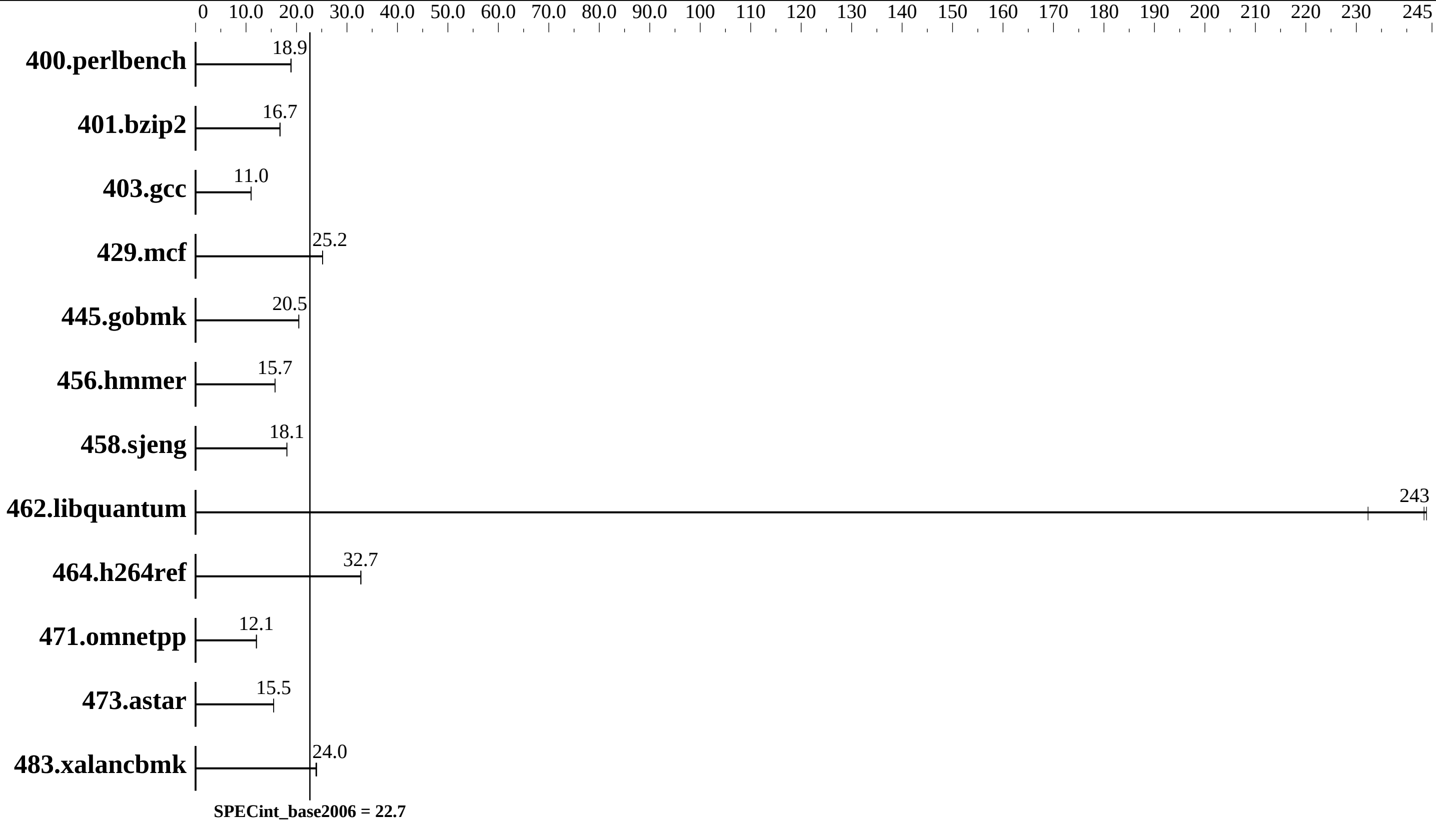
Apple, Inc.
3.0 GHz 8-Core Xserve

SPECint®2006 = Not Run

SPECint_base2006 = 22.7

CPU2006 license: 77
Test sponsor: Apple, Inc.
Tested by: Apple, Inc.

Test date: Apr-2008
Hardware Availability: Jan-2008
Software Availability: Feb-2008



Hardware

CPU Name:	Intel Xeon E5472
CPU Characteristics:	3.0 GHz, 12 MB L2, 1600 MHz bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2-800 FBDIMM)
Disk Subsystem:	1x80 GB Seagate SATA, 7200PM
Other Hardware:	None

Software

Operating System:	Mac OS X Server 10.5.2, Build 9C31
Compiler:	Intel C++ Compiler for Mac OS X Version 10.1 Build 20080212 (012)
Auto Parallel:	Yes
File System:	Journaled HFS+
System State:	Console mode
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	516	18.9	518	18.9	**516**	**18.9**						
401.bzip2	576	16.8	578	16.7	**576**	**16.7**						
403.gcc	**730**	**11.0**	732	11.0	730	11.0						
429.mcf	362	25.2	**362**	**25.2**	363	25.2						
445.gobmk	513	20.4	512	20.5	**513**	**20.5**						
456.hmmer	593	15.7	593	15.7	**593**	**15.7**						
458.sjeng	668	18.1	669	18.1	**669**	**18.1**						
462.libquantum	89.2	232	84.9	244	**85.1**	**243**						
464.h264ref	675	32.8	**676**	**32.7**	676	32.7						
471.omnetpp	517	12.1	520	12.0	**519**	**12.1**						
473.astar	**453**	**15.5**	454	15.5	453	15.5						
483.xalancbmk	288	24.0	**288**	**24.0**	289	23.8						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Binaries compiled using Mac OS X 10.4.11, Build 8S2167
OMP_NUM_THREADS set to number of cores
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

403.gcc: `-DSPEC_CPU_MACOSX`
462.libquantum: `-DSPEC_CPU_MACOSX`
483.xalancbmk: `-DSPEC_CPU_MACOSX`

Base Optimization Flags

C benchmarks:
`-static-intel -fast -parallel -par-runtime-control`

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
`-static-intel -fast -parallel`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/macosx-iccifort-v10.1-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/macosx-iccifort-v10.1-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 16:40:24 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 May 2008.