SPEC® CINT2006 Result

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NEC Corporation

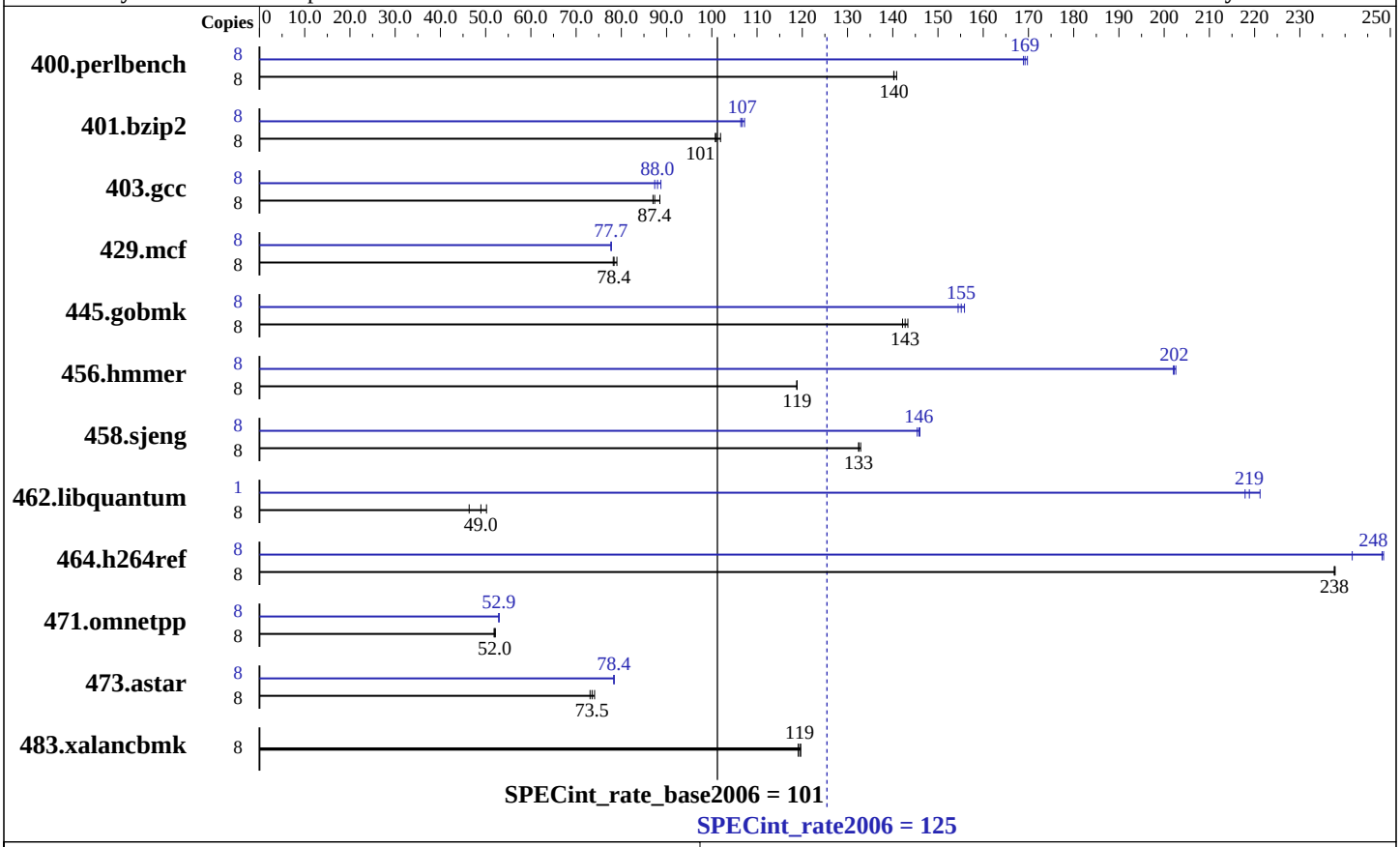
Express5800/120Rj-2
(Intel Xeon E5440)

SPECint®_rate2006 = 125

SPECint_rate_base2006 = 101

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Apr-2008
Hardware Availability: Dec-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	2.83 GHz, 2x6 MB L2 shared, 1333 MHz bus
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x73.2 GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	555	141	557	140	**557**	**140**	8	**462**	**169**	463	169	460	170
401.bzip2	8	766	101	**765**	**101**	757	102	8	**723**	**107**	726	106	720	107
403.gcc	8	740	87.0	728	88.5	**737**	**87.4**	8	737	87.4	**732**	**88.0**	726	88.7
429.mcf	8	933	78.2	**931**	**78.4**	923	79.0	8	**938**	**77.7**	939	77.7	938	77.8
445.gobmk	8	585	143	**588**	**143**	590	142	8	543	154	**541**	**155**	538	156
456.hmmer	8	628	119	628	119	**628**	**119**	8	**369**	**202**	368	203	369	202
458.sjeng	8	**731**	**133**	728	133	731	132	8	663	146	**664**	**146**	666	145
462.libquantum	8	3576	46.4	**3386**	**49.0**	3303	50.2	1	**94.7**	**219**	95.1	218	93.7	221
464.h264ref	8	744	238	**745**	**238**	745	238	8	733	242	712	249	**713**	**248**
471.omnetpp	8	964	51.9	**962**	**52.0**	959	52.1	8	944	53.0	**945**	**52.9**	945	52.9
473.astar	8	768	73.1	758	74.1	**764**	**73.5**	8	716	78.4	**717**	**78.4**	717	78.3
483.xalancbmk	8	464	119	**462**	**119**	461	120	8	464	119	**462**	**119**	461	120

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
Bios settings:
Intel SpeedStep Technology:   Disabled
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

 The NEC Express5800/120Rh-1(Intel Xeon E5440) and
 the NEC Express5800/120Rj-2(Intel Xeon E5440) models are electronically equivalent.
 The results have been measured on a NEC Express5800/120Rj-2(Intel Xeon E5440) model.
```

Base Compiler Invocation

C benchmarks:

```
icc
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090714.xml`

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