SPEC® CINT2006 Result

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Fujitsu Siemens Computers

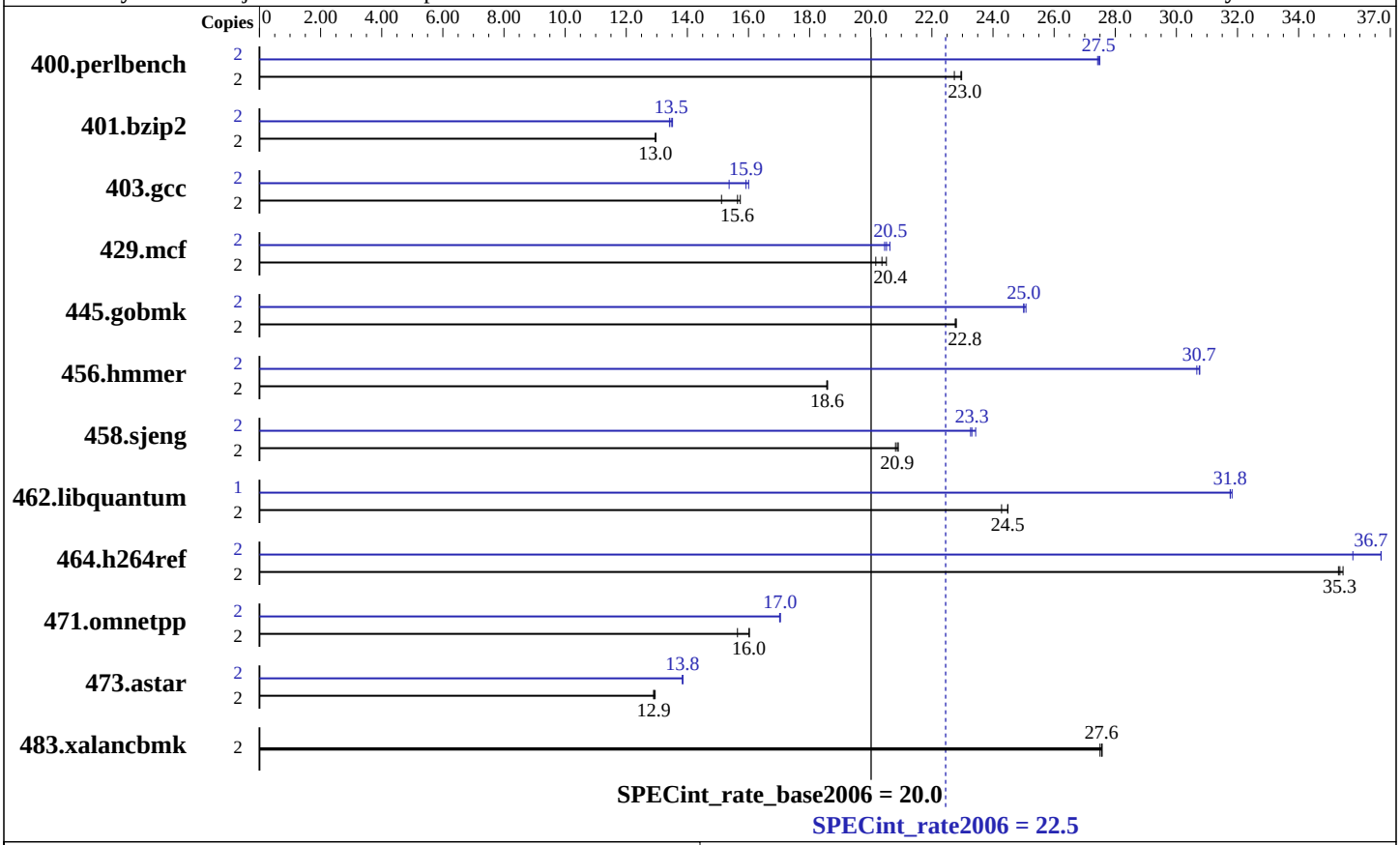
PRIMERGY RX100 S5, Intel Pentium Dual Core E2160, 1.80 GHz

SPECint®_rate2006 = 22.5

SPECint_rate_base2006 = 20.0

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Apr-2008
Hardware Availability: Apr-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Pentium Dual Core E2160
CPU Characteristics:	800 MHz system bus
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB PC2-6400E, 2 rank, CL 6-6-6, ECC)
Disk Subsystem:	1x SATA, 500 GB, 7200 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
Auto Parallel:	Yes
File System:	ext2
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**851**	**23.0**	851	23.0	859	22.7	2	**712**	**27.5**	713	27.4	711	27.5
401.bzip2	2	**1489**	**13.0**	1488	13.0	1490	13.0	2	1439	13.4	1428	13.5	**1433**	**13.5**
403.gcc	2	1023	15.7	**1029**	**15.6**	1065	15.1	2	**1011**	**15.9**	1048	15.4	1006	16.0
429.mcf	2	**896**	**20.4**	905	20.2	889	20.5	2	892	20.5	884	20.6	**889**	**20.5**
445.gobmk	2	920	22.8	922	22.8	**921**	**22.8**	2	837	25.1	839	25.0	**839**	**25.0**
456.hmmer	2	**1004**	**18.6**	1004	18.6	1005	18.6	2	607	30.8	**607**	**30.7**	608	30.7
458.sjeng	2	**1160**	**20.9**	1163	20.8	1158	20.9	2	1040	23.3	1033	23.4	**1038**	**23.3**
462.libquantum	2	1707	24.3	**1693**	**24.5**	1692	24.5	1	651	31.8	**652**	**31.8**	652	31.8
464.h264ref	2	1248	35.5	**1252**	**35.3**	1254	35.3	2	**1206**	**36.7**	1237	35.8	1206	36.7
471.omnetpp	2	799	15.6	780	16.0	**781**	**16.0**	2	734	17.0	734	17.0	**734**	**17.0**
473.astar	2	1084	12.9	1089	12.9	**1088**	**12.9**	2	1014	13.8	**1014**	**13.8**	1015	13.8
483.xalancbmk	2	501	27.6	**501**	**27.6**	502	27.5	2	501	27.6	**501**	**27.6**	502	27.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
```

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml`

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