



# SPEC® CINT2006 Result

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## Itautec

## SPECint®\_rate2006 = 116

### Servidor Itautec MX201 (Intel Xeon E5430)

## SPECint\_rate\_base2006 = 103

CPU2006 license: 9001

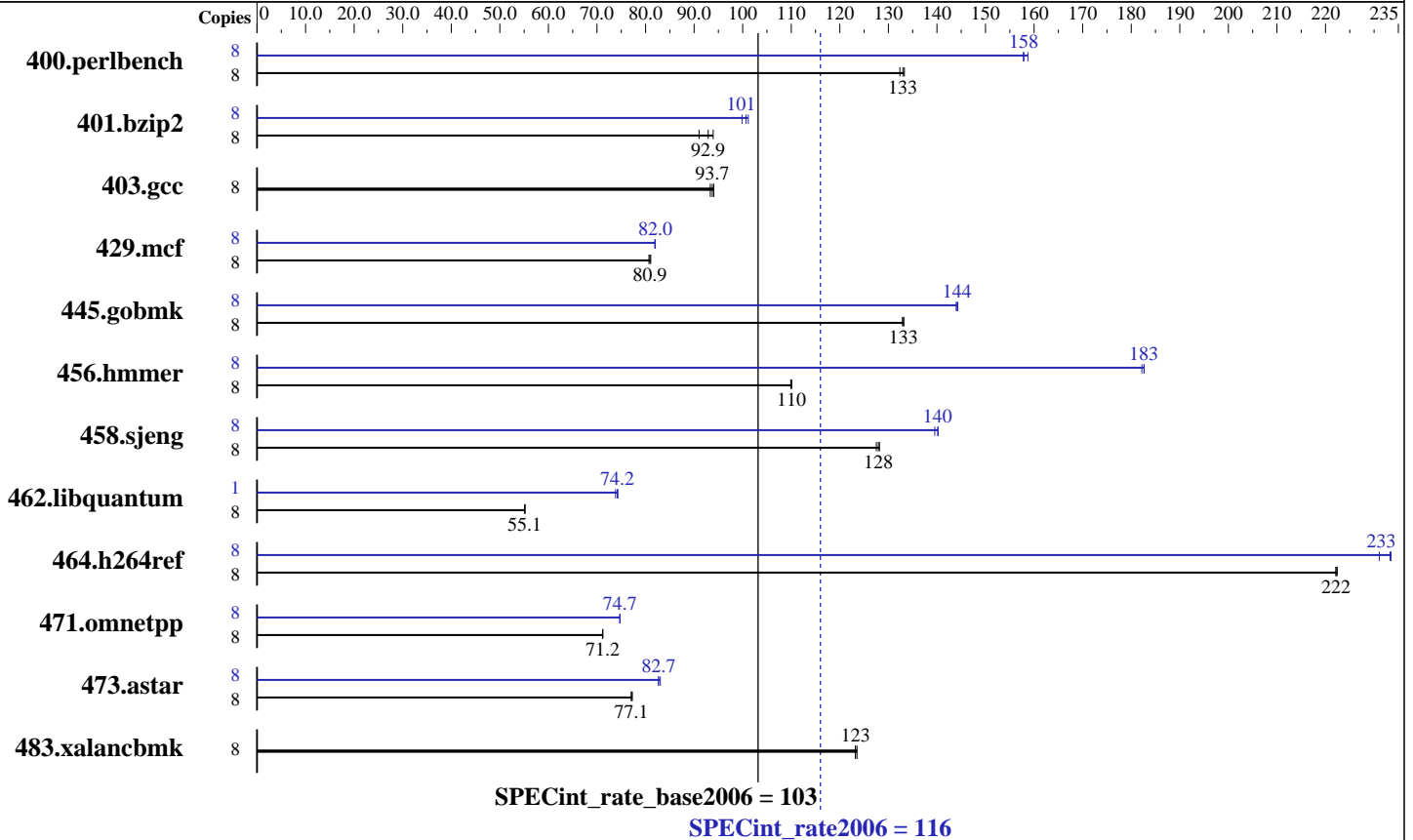
Test date: Apr-2008

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Jan-2008



### Hardware

CPU Name: Intel Xeon E5430  
 CPU Characteristics:  
 CPU MHz: 2660  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	590	132	<b>588</b>	<b>133</b>	586	133	8	492	159	<b>495</b>	<b>158</b>	495	158
401.bzip2	8	822	93.9	<b>831</b>	<b>92.9</b>	848	91.0	8	763	101	<b>767</b>	<b>101</b>	773	99.9
403.gcc	8	<b>687</b>	<b>93.7</b>	685	94.0	690	93.3	8	<b>687</b>	<b>93.7</b>	685	94.0	690	93.3
429.mcf	8	<b>902</b>	<b>80.9</b>	904	80.7	900	81.1	8	<b>890</b>	<b>82.0</b>	891	81.9	890	82.0
445.gobmk	8	630	133	632	133	<b>630</b>	<b>133</b>	8	<b>582</b>	<b>144</b>	583	144	582	144
456.hammer	8	678	110	<b>678</b>	<b>110</b>	679	110	8	<b>409</b>	<b>183</b>	410	182	409	183
458.sjeng	8	759	128	755	128	<b>757</b>	<b>128</b>	8	694	140	690	140	<b>691</b>	<b>140</b>
462.libquantum	8	<b>3006</b>	<b>55.1</b>	3006	55.1	3004	55.2	1	<b>279</b>	<b>74.2</b>	279	74.3	281	73.8
464.h264ref	8	<b>797</b>	<b>222</b>	797	222	796	222	8	<b>759</b>	<b>233</b>	758	233	766	231
471.omnetpp	8	<b>703</b>	<b>71.2</b>	702	71.2	703	71.1	8	<b>669</b>	<b>74.7</b>	669	74.7	669	74.7
473.astar	8	727	77.3	<b>729</b>	<b>77.1</b>	729	77.1	8	676	83.0	679	82.7	<b>679</b>	<b>82.7</b>
483.xalancbmk	8	448	123	447	124	<b>448</b>	<b>123</b>	8	448	123	447	124	<b>448</b>	<b>123</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Disabled

## General Notes

This result was measured on the Servidor Itautec MX201.  
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
456.hmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
403.gcc: basepeak = yes  
429.mcf: -fast -prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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