



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 37.2

Servidor Itaotec MX201 (Intel Xeon 5160)

SPECint\_rate\_base2006 = 31.7

CPU2006 license: 9001

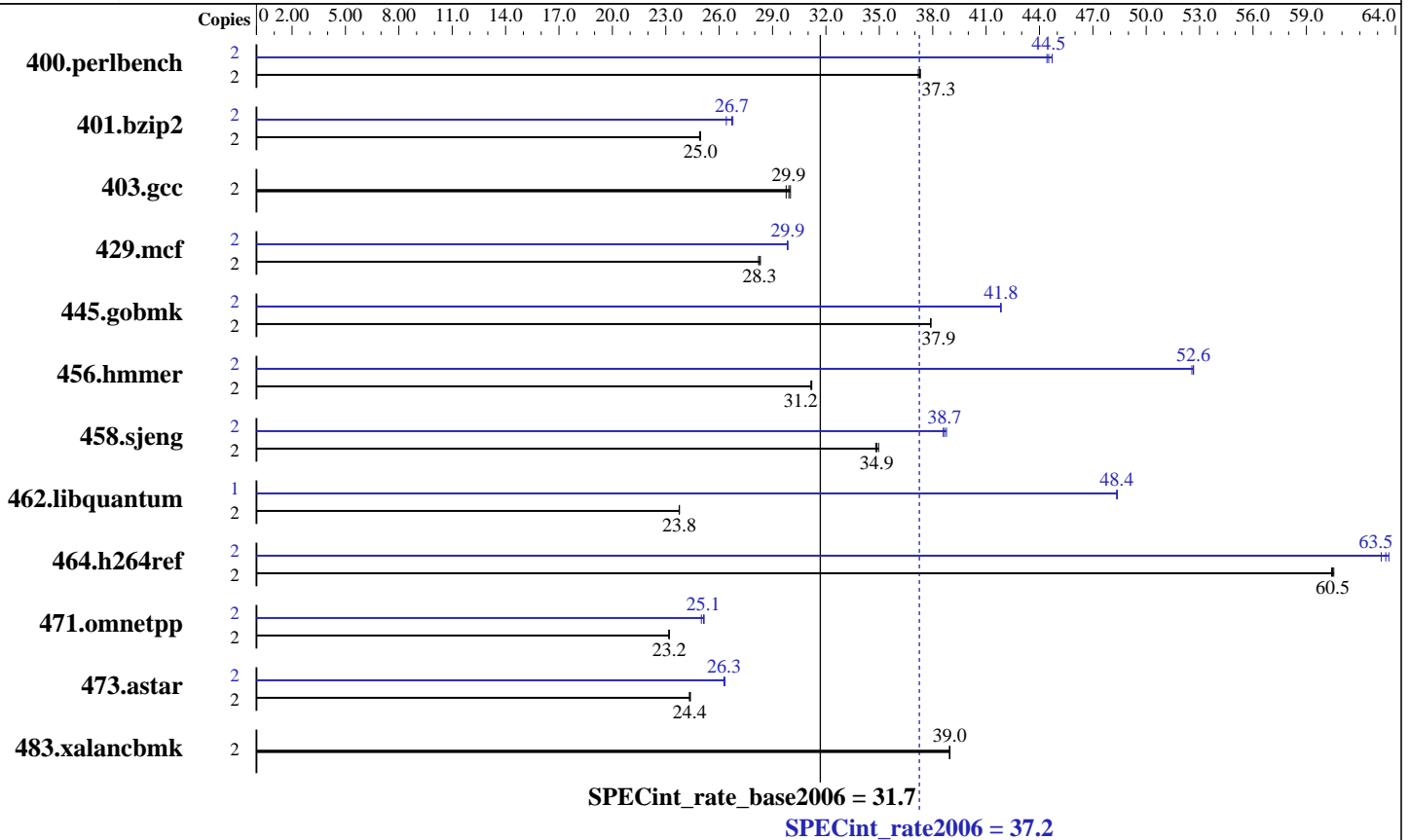
Test date: Apr-2008

Test sponsor: Itaotec

Hardware Availability: Jul-2006

Tested by: Itaotec

Software Availability: Jan-2008



## Hardware

CPU Name: Intel Xeon 5160  
 CPU Characteristics:  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	524	37.3	525	37.2	<b>524</b>	<b>37.3</b>	2	<b>439</b>	<b>44.5</b>	440	44.4	437	44.7
401.bzip2	2	773	25.0	<b>774</b>	<b>25.0</b>	775	24.9	2	721	26.8	731	26.4	<b>723</b>	<b>26.7</b>
403.gcc	2	541	29.8	537	30.0	<b>538</b>	<b>29.9</b>	2	541	29.8	537	30.0	<b>538</b>	<b>29.9</b>
429.mcf	2	644	28.3	646	28.2	<b>645</b>	<b>28.3</b>	2	611	29.8	<b>611</b>	<b>29.9</b>	611	29.9
445.gobmk	2	554	37.9	554	37.9	<b>554</b>	<b>37.9</b>	2	<b>502</b>	<b>41.8</b>	502	41.8	501	41.8
456.hammer	2	598	31.2	599	31.2	<b>598</b>	<b>31.2</b>	2	<b>354</b>	<b>52.6</b>	354	52.7	355	52.6
458.sjeng	2	695	34.8	<b>694</b>	<b>34.9</b>	692	35.0	2	624	38.8	627	38.6	<b>626</b>	<b>38.7</b>
462.libquantum	2	1744	23.8	1744	23.8	<b>1744</b>	<b>23.8</b>	1	429	48.3	428	48.4	<b>428</b>	<b>48.4</b>
464.h264ref	2	733	60.4	731	60.5	<b>732</b>	<b>60.5</b>	2	700	63.2	<b>697</b>	<b>63.5</b>	696	63.6
471.omnetpp	2	<b>539</b>	<b>23.2</b>	540	23.2	539	23.2	2	<b>498</b>	<b>25.1</b>	497	25.2	500	25.0
473.astar	2	577	24.3	576	24.4	<b>577</b>	<b>24.4</b>	2	<b>534</b>	<b>26.3</b>	534	26.3	533	26.3
483.xalancbmk	2	354	39.0	354	38.9	<b>354</b>	<b>39.0</b>	2	354	39.0	354	38.9	<b>354</b>	<b>39.0</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Disabled

## General Notes

This result was measured on the Servidor Itautec MX221.  
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

Continued on next page



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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

456.hmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
403.gcc: basepeak = yes  
429.mcf: -fast -prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.  
Report generated on Tue Jul 22 16:51:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 29 April 2008.