SPEC® CINT2006 Result

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Itautec

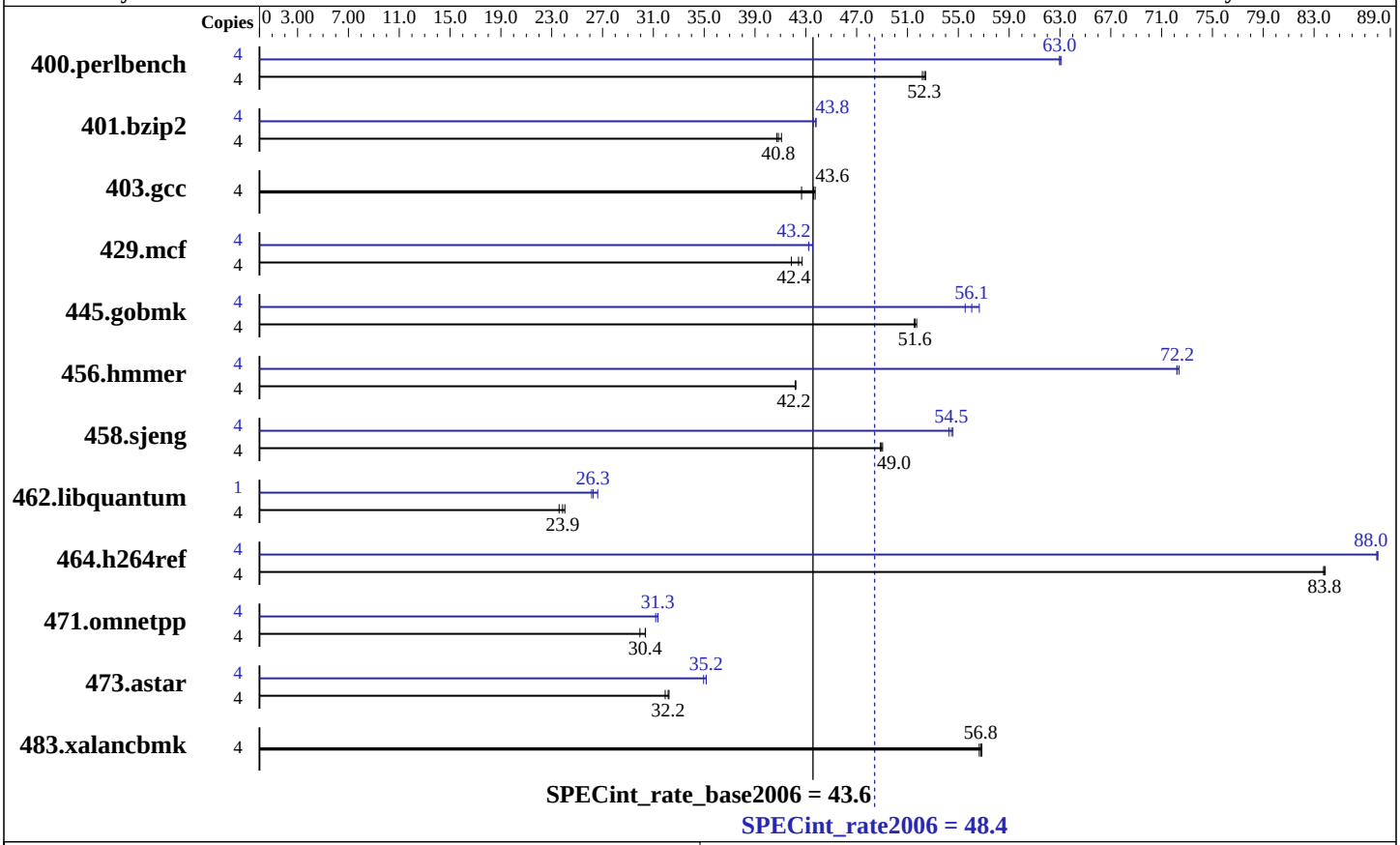
Servidor Itautec LX211 (Intel Xeon E5405)

SPECint®_rate2006 = 48.4

SPECint_rate_base2006 = 43.6

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Apr-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008



Hardware

CPU Name:	Intel Xeon E5405
CPU Characteristics:	
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4 * 1 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	1 x SCSI, 73GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run Level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.10.50 MicroQuill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	745	52.4	749	52.2	**747**	**52.3**	4	**620**	**63.0**	621	63.0	619	63.1
401.bzip2	4	940	41.1	**945**	**40.8**	948	40.7	4	881	43.8	886	43.6	**882**	**43.8**
403.gcc	4	755	42.7	736	43.7	**738**	**43.6**	4	755	42.7	736	43.7	**738**	**43.6**
429.mcf	4	872	41.9	854	42.7	**860**	**42.4**	4	838	43.5	**844**	**43.2**	844	43.2
445.gobmk	4	814	51.5	811	51.7	**813**	**51.6**	4	755	55.5	741	56.7	**748**	**56.1**
456.hmmer	4	884	42.2	**885**	**42.2**	885	42.2	4	517	72.2	516	72.4	**517**	**72.2**
458.sjeng	4	991	48.9	**989**	**49.0**	987	49.0	4	887	54.6	**889**	**54.5**	892	54.3
462.libquantum	4	3515	23.6	**3474**	**23.9**	3448	24.0	1	778	26.6	**789**	**26.3**	793	26.1
464.h264ref	4	1055	83.9	1057	83.7	**1056**	**83.8**	4	**1006**	**88.0**	1007	87.9	1006	88.0
471.omnetpp	4	835	29.9	823	30.4	**823**	**30.4**	4	**798**	**31.3**	797	31.4	802	31.2
473.astar	4	879	31.9	871	32.2	**873**	**32.2**	4	799	35.2	803	35.0	**799**	**35.2**
483.xalancbmk	4	**486**	**56.8**	485	56.9	487	56.6	4	**486**	**56.8**	485	56.9	487	56.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch Enabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/opt/sh/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.1.012/bin/icc`
`-L/opt/intel/cce/10.1.012/lib`
`-I/opt/intel/cce/10.1.012/include`

456.hmmer: `/opt/intel/cce/10.1.012/bin/icc`
`-L/opt/intel/cce/10.1.012/lib`
`-I/opt/intel/cce/10.1.012/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml`

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