SPEC® CINT2006 Result

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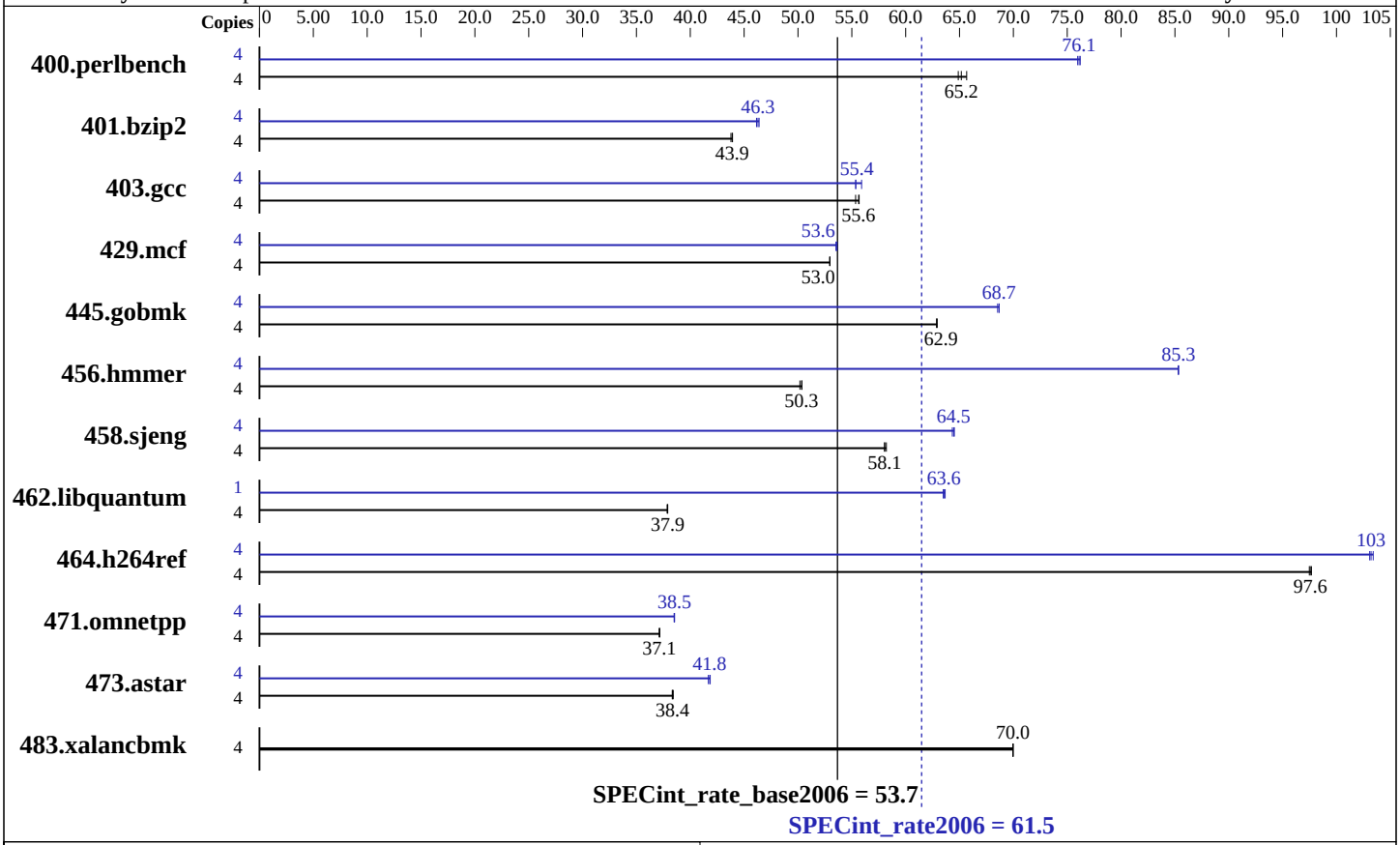
Supermicro Motherboard C2SBX

SPECint®_rate2006 = 61.5

SPECint_rate_base2006 = 53.7

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Feb-2008
Hardware Availability: Sep-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X3220
CPU Characteristics:	2.40 GHz 1066 MHz System Bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4 x 1 GB PC3-8500U-7-00-BP, DDR3-1066)
Disk Subsystem:	Western Digital WD800JD 80 GB SATA 7200RPM
Other Hardware:	None

Software

Operating System:	64-Bit Suse Linux Enterprise Server 10 w/SP1
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap library V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	595	65.7	602	64.9	**600**	**65.2**	4	514	76.0	513	76.2	**514**	**76.1**
401.bzip2	4	882	43.8	**879**	**43.9**	879	43.9	4	**834**	**46.3**	836	46.2	832	46.4
403.gcc	4	578	55.7	**579**	**55.6**	582	55.4	4	582	55.4	576	55.9	**582**	**55.4**
429.mcf	4	**689**	**53.0**	689	53.0	689	53.0	4	680	53.6	**681**	**53.6**	681	53.5
445.gobmk	4	667	62.9	**667**	**62.9**	667	62.9	4	**611**	**68.7**	612	68.5	611	68.7
456.hmmer	4	741	50.4	744	50.2	**741**	**50.3**	4	**437**	**85.3**	438	85.3	437	85.3
458.sjeng	4	834	58.0	**834**	**58.1**	831	58.2	4	750	64.5	752	64.3	**751**	**64.5**
462.libquantum	4	**2187**	**37.9**	2190	37.8	2187	37.9	1	325	63.7	**326**	**63.6**	326	63.5
464.h264ref	4	908	97.5	**907**	**97.6**	906	97.7	4	**858**	**103**	856	103	859	103
471.omnetpp	4	674	37.1	**673**	**37.1**	673	37.2	4	649	38.5	**649**	**38.5**	649	38.5
473.astar	4	733	38.3	**732**	**38.4**	731	38.4	4	671	41.8	**672**	**41.8**	674	41.7
483.xalancbmk	4	**394**	**70.0**	394	70.0	394	70.0	4	**394**	**70.0**	394	70.0	394	70.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.xml`

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