



SPEC® CINT2006 Result

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Maxdata AG

(Test Sponsor: Maxdata)

FAVORIT 5000 I
(2.2 GHz, Intel Core 2 Duo processor
E4500)

SPECint®_rate2006 = 23.0

SPECint_rate_base2006 = 22.0

CPU2006 license: 3168

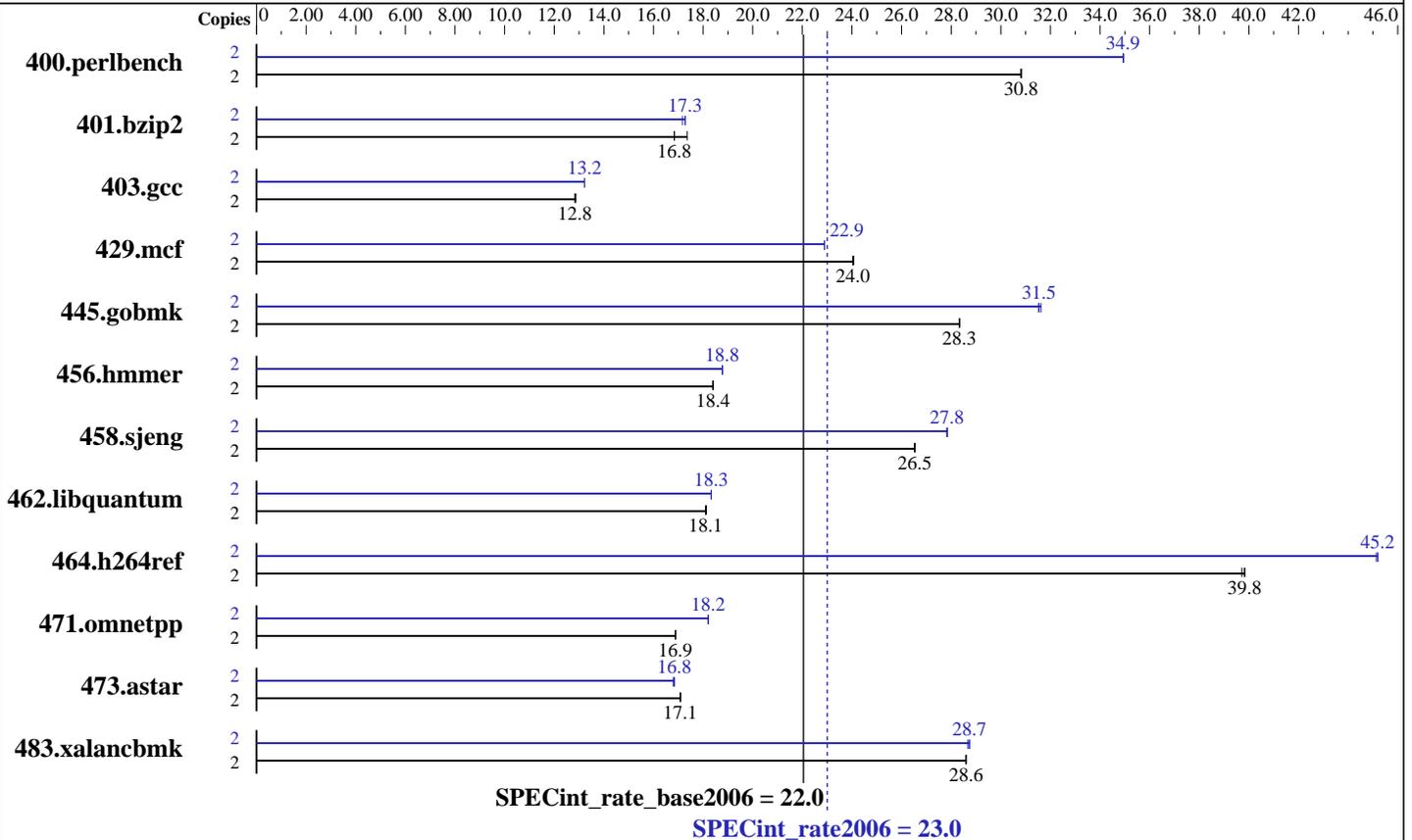
Test sponsor: Maxdata

Tested by: Maxdata

Test date: Mar-2008

Hardware Availability: Mar-2008

Software Availability: Mar-2008



Hardware

CPU Name: Intel Core 2 Duo E4500
 CPU Characteristics: 2.2 GHz, 800 MHz bus
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2 1GB DDR2 667 CL5-5-5)
 Disk Subsystem: Seagate ST3160815AS 160GB NCQ Serial ATA (7200 RPM, 8MB cache)
 Other Hardware: None

Software

Operating System: Windows XP Professional SP2
 Compiler: Intel C++ and Fortran Compiler 9.1 for IA32 Build 20060816Z
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: None



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	634	30.8	634	30.8	634	30.8	2	559	34.9	559	35.0	559	34.9
401.bzip2	2	1112	17.4	1146	16.8	1146	16.8	2	1124	17.2	1117	17.3	1117	17.3
403.gcc	2	1254	12.8	1251	12.9	1254	12.8	2	1218	13.2	1218	13.2	1218	13.2
429.mcf	2	759	24.0	759	24.0	758	24.1	2	797	22.9	797	22.9	797	22.9
445.gobmk	2	741	28.3	740	28.3	741	28.3	2	664	31.6	665	31.5	665	31.5
456.hammer	2	1014	18.4	1014	18.4	1014	18.4	2	994	18.8	994	18.8	994	18.8
458.sjeng	2	912	26.5	912	26.5	913	26.5	2	869	27.9	870	27.8	870	27.8
462.libquantum	2	2289	18.1	2290	18.1	2286	18.1	2	2261	18.3	2261	18.3	2261	18.3
464.h264ref	2	1111	39.8	1111	39.8	1114	39.7	2	979	45.2	979	45.2	981	45.1
471.omnetpp	2	740	16.9	740	16.9	740	16.9	2	686	18.2	686	18.2	687	18.2
473.astar	2	822	17.1	821	17.1	822	17.1	2	834	16.8	836	16.8	834	16.8
483.xalancbmk	2	483	28.6	482	28.6	483	28.6	2	480	28.7	481	28.7	480	28.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Memory timings set manually to DDR2-800 5-5-5-15 in the bios

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.xml>



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Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 16:45:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 25 April 2008.