



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint®_rate2006 = 61.3

Servidor Itautec MX221 (Intel Xeon E5410)

SPECint_rate_base2006 = 53.2

CPU2006 license: 9001

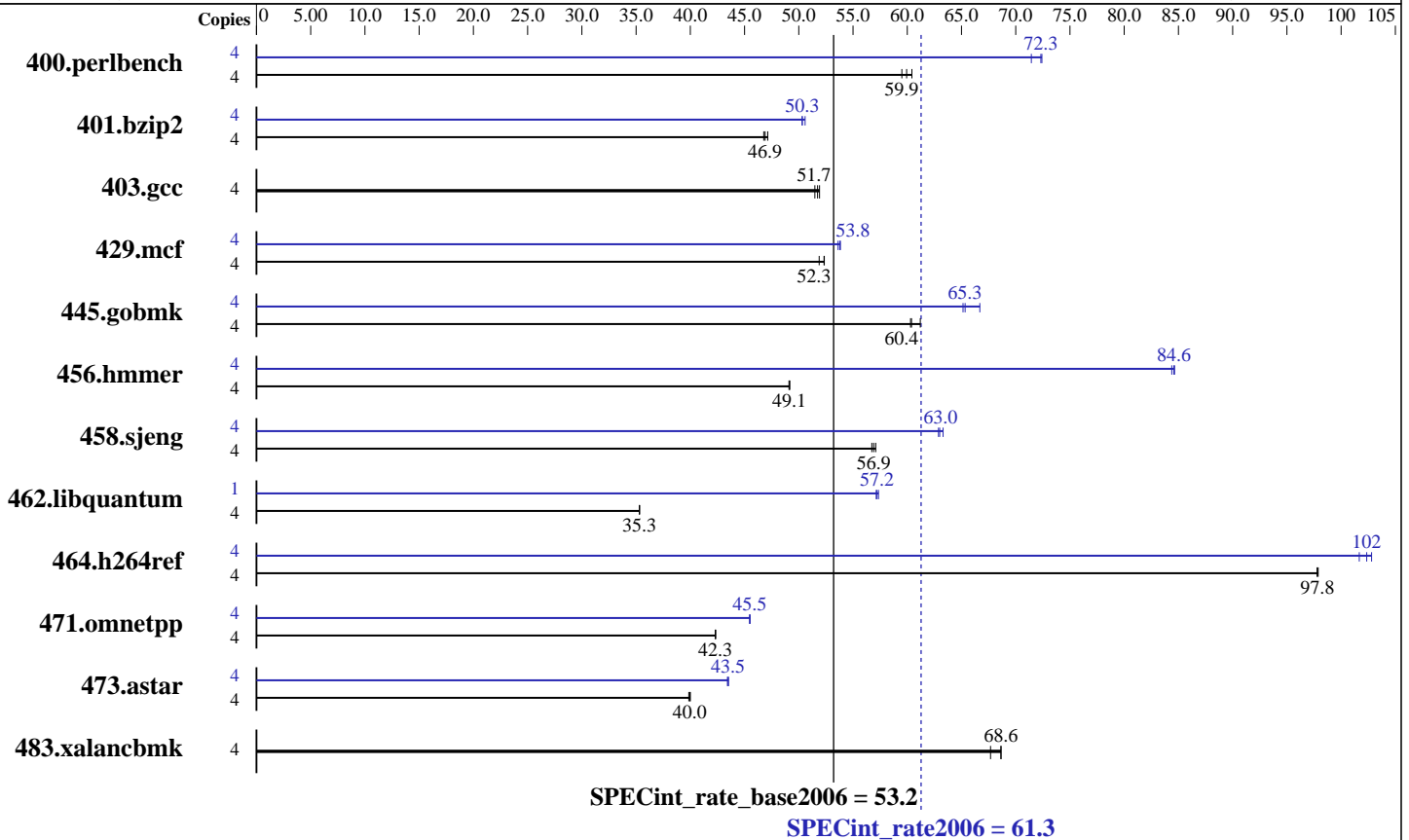
Test sponsor: Itautec

Tested by: Itautec

Test date: Mar-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics:
 CPU MHz: 2330
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.10.50, MicroQuill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 61.3

Servidor Itaotec MX221 (Intel Xeon E5410)

SPECint_rate_base2006 = 53.2

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Mar-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	4	657	59.5	647	60.4	652	59.9	4	540	72.3	540	72.4	547	71.4		
401.bzip2	4	823	46.9	825	46.8	819	47.1	4	767	50.3	767	50.3	763	50.6		
403.gcc	4	625	51.5	620	51.9	622	51.7	4	625	51.5	620	51.9	622	51.7		
429.mcf	4	703	51.9	697	52.3	697	52.4	4	678	53.8	680	53.6	678	53.8		
445.gobmk	4	695	60.4	696	60.3	686	61.2	4	642	65.3	629	66.7	644	65.1		
456.hammer	4	760	49.1	759	49.2	759	49.1	4	442	84.4	441	84.6	441	84.6		
458.sjeng	4	853	56.7	851	56.9	848	57.1	4	768	63.0	765	63.3	770	62.9		
462.libquantum	4	2347	35.3	2348	35.3	2347	35.3	1	361	57.3	363	57.1	362	57.2		
464.h264ref	4	905	97.8	904	97.9	905	97.8	4	871	102	865	102	861	103		
471.omnetpp	4	591	42.3	590	42.3	591	42.3	4	549	45.5	550	45.5	550	45.5		
473.astar	4	702	40.0	704	39.9	703	40.0	4	647	43.4	646	43.5	645	43.5		
483.xalancbmk	4	402	68.6	408	67.7	402	68.6	4	402	68.6	408	67.7	402	68.6		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.

Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

General Notes

This result was measured on the Servidor Itaotec MX201.
The Servidor Itaotec MX221 and the Servidor Itaotec MX201 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 61.3

Servidor Itaotec MX221 (Intel Xeon E5410)

SPECint_rate_base2006 = 53.2

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Mar-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 61.3

Servidor Itaotec MX221 (Intel Xeon E5410)

SPECint_rate_base2006 = 53.2

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Mar-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
403.gcc: basepeak = yes
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 61.3

Servidor Itautec MX221 (Intel Xeon E5410)

SPECint_rate_base2006 = 53.2

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Mar-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 18:24:27 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 15 April 2008.