SPEC® CFP2006 Result

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Itautec

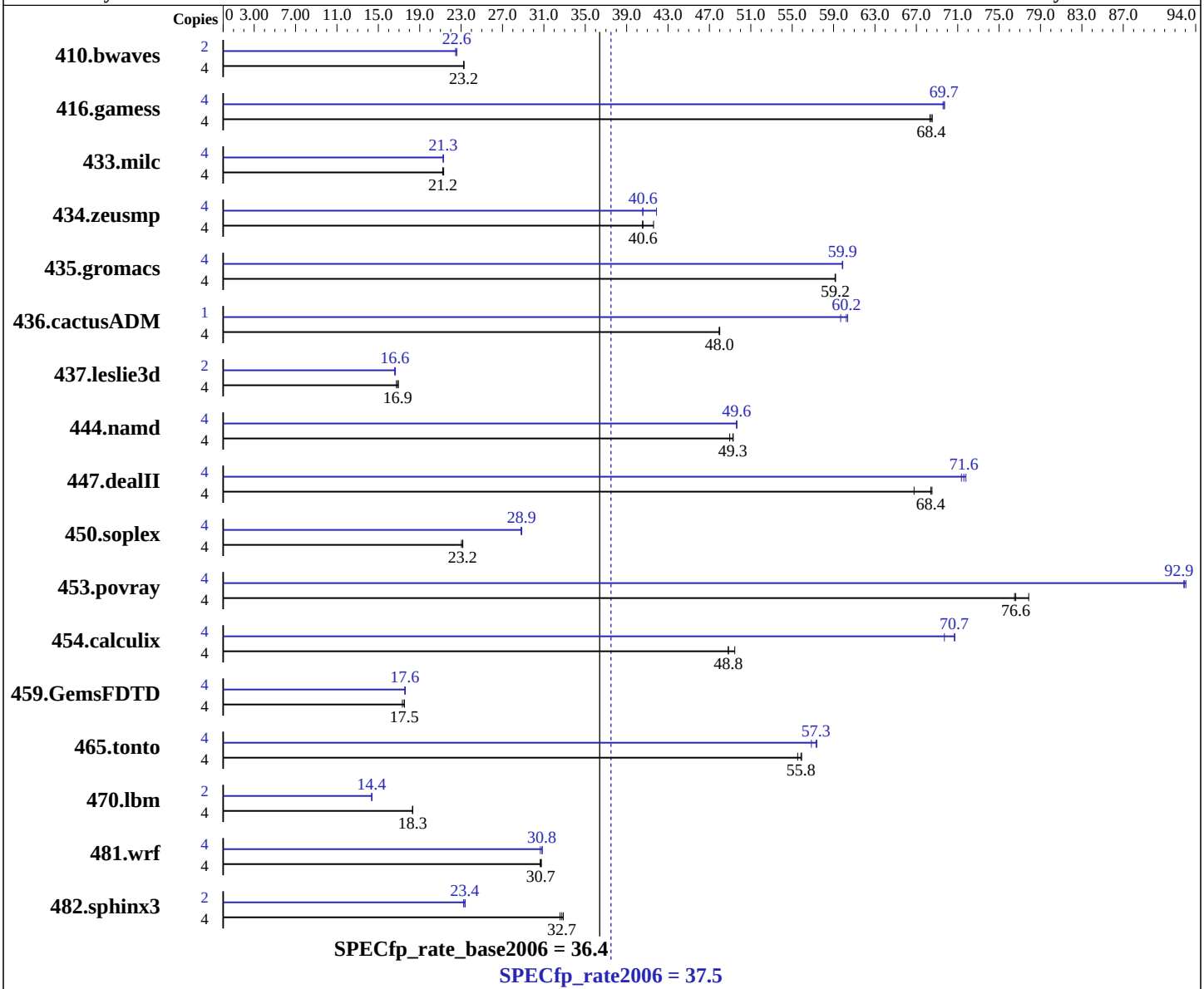
Servidor Itautec MX201 (Intel Xeon E5410)

SPECfp®_rate2006 = 37.5

SPECfp_rate_base2006 = 36.4

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Mar-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	
CPU MHz:	2330
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ and Fortran Compiler 10.1 for Linux Build 20080112 Package ID: l_cc_p_10.1.012, l_fc_p_10.1.012
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run Level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.10.50

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	**2339**	**23.2**	2334	23.3	2339	23.2	2	1204	22.6	1210	22.5	**1204**	**22.6**
416.gamess	4	1143	68.5	1147	68.3	**1144**	**68.4**	4	1123	69.7	1126	69.6	**1124**	**69.7**
433.milc	4	1730	21.2	1723	21.3	**1729**	**21.2**	4	**1726**	**21.3**	1725	21.3	1727	21.3
434.zeusmp	4	875	41.6	898	40.5	**897**	**40.6**	4	**897**	**40.6**	869	41.9	898	40.6
435.gromacs	4	**483**	**59.2**	483	59.1	483	59.2	4	477	59.9	**477**	**59.9**	477	59.9
436.cactusADM	4	996	48.0	997	47.9	**996**	**48.0**	1	**198**	**60.2**	198	60.4	200	59.7
437.leslie3d	4	**2230**	**16.9**	2220	16.9	2245	16.7	2	1133	16.6	**1133**	**16.6**	1129	16.7
444.namd	4	**651**	**49.3**	651	49.3	655	49.0	4	646	49.6	646	49.7	**646**	**49.6**
447.dealII	4	**669**	**68.4**	685	66.8	668	68.5	4	637	71.8	**639**	**71.6**	641	71.4
450.soplex	4	1440	23.2	**1441**	**23.2**	1448	23.0	4	1156	28.9	1159	28.8	**1156**	**28.9**
453.povray	4	**278**	**76.6**	278	76.5	273	77.9	4	229	93.1	229	92.9	**229**	**92.9**
454.calculix	4	676	48.8	667	49.5	**676**	**48.8**	4	467	70.7	**467**	**70.7**	473	69.7
459.GemsFDTD	4	**2424**	**17.5**	2450	17.3	2423	17.5	4	2410	17.6	2418	17.6	**2415**	**17.6**
465.tonto	4	704	55.9	**705**	**55.8**	709	55.5	4	**687**	**57.3**	686	57.4	692	56.9
470.lbm	4	**3000**	**18.3**	3000	18.3	3001	18.3	2	1914	14.4	1913	14.4	**1913**	**14.4**
481.wrf	4	1458	30.6	1453	30.8	**1455**	**30.7**	4	1448	30.8	**1450**	**30.8**	1457	30.7
482.sphinx3	4	2394	32.6	2371	32.9	**2381**	**32.7**	2	1666	23.4	1678	23.2	**1669**	**23.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch Disabled
```

General Notes

```
This result was measured on the Servidor Itautec MX201.
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-fast`

Fortran benchmarks:
`-fast`

Benchmarks using both Fortran and C:
`-fast`

Peak Compiler Invocation

C benchmarks (except as noted below):
`/opt/intel/cc/10.1.012/bin/icc -L/opt/intel/cc/10.1.012/lib -I/opt/intel/cc/10.1.012/include`

433.milc: `icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/10.1.012/bin/icpc -L/opt/intel/cc/10.1.012/lib -I/opt/intel/cc/10.1.012/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/fc/10.1.012/bin/ifort -L/opt/intel/fc/10.1.012/lib -I/opt/intel/fc/10.1.012/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -prefetch

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Peak Optimization Flags (Continued)

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -prefetch -parallel -auto-ilp32`

454.calculix: `-fast -unroll-aggressive -auto-ilp32`

481.wrf: `-fast -auto-ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.20090713.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.20090713.00.xml`

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For other inquiries, please contact webmaster@spec.org.

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