



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

### SPECint®\_rate2006 = 29.4

PRIMERGY RX100 S5, Intel Core 2 Duo E4600,  
2.40 GHz

### SPECint\_rate\_base2006 = 26.1

CPU2006 license: 22

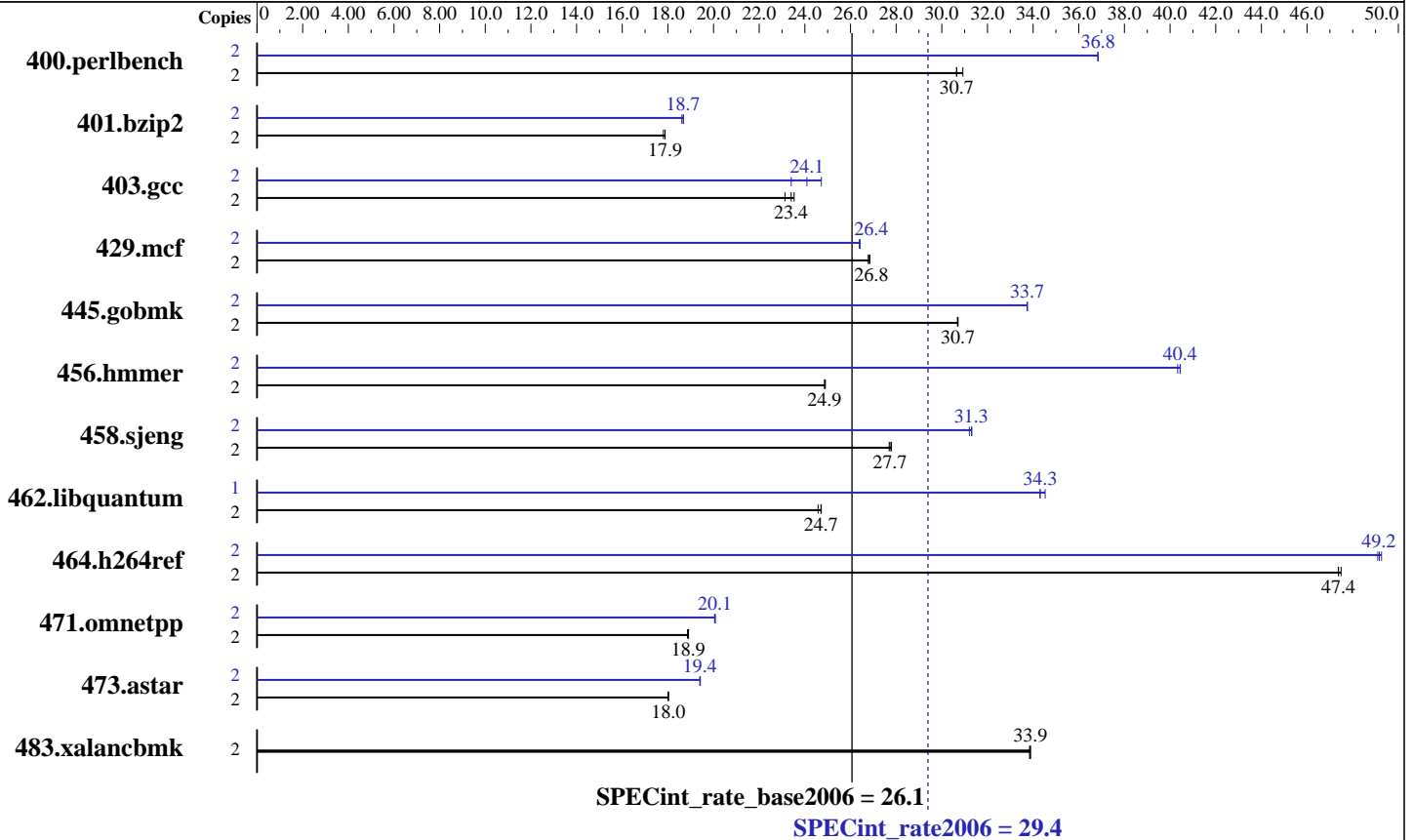
Test date: Mar-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Apr-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Core 2 Duo E4600  
 CPU Characteristics: 800 MHz system bus  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 2 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL 6-6-6, ECC)  
 Disk Subsystem: 1x SATA, 500 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1,  
Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64,  
Version 10.1, Build 20070913  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library, Version 8.1  
binutils-2.17.50.0.5-0.1.x86\_64



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<b>637</b>	<b>30.7</b>	638	30.6	632	30.9	2	<b>530</b>	<b>36.8</b>	530	36.8	530	36.8
401.bzip2	2	1080	17.9	1084	17.8	<b>1080</b>	<b>17.9</b>	2	1037	18.6	<b>1034</b>	<b>18.7</b>	1033	18.7
403.gcc	2	696	23.1	684	23.5	<b>688</b>	<b>23.4</b>	2	651	24.7	<b>668</b>	<b>24.1</b>	688	23.4
429.mcf	2	<b>680</b>	<b>26.8</b>	679	26.9	681	26.8	2	<b>691</b>	<b>26.4</b>	691	26.4	691	26.4
445.gobmk	2	<b>683</b>	<b>30.7</b>	683	30.7	684	30.7	2	622	33.7	622	33.7	<b>622</b>	<b>33.7</b>
456.hammer	2	749	24.9	751	24.9	<b>750</b>	<b>24.9</b>	2	461	40.5	463	40.3	<b>461</b>	<b>40.4</b>
458.sjeng	2	871	27.8	<b>873</b>	<b>27.7</b>	874	27.7	2	775	31.2	<b>773</b>	<b>31.3</b>	773	31.3
462.libquantum	2	1686	24.6	<b>1678</b>	<b>24.7</b>	1676	24.7	1	600	34.5	<b>604</b>	<b>34.3</b>	604	34.3
464.h264ref	2	<b>934</b>	<b>47.4</b>	932	47.5	934	47.4	2	898	49.3	<b>900</b>	<b>49.2</b>	901	49.1
471.omnetpp	2	<b>662</b>	<b>18.9</b>	663	18.9	661	18.9	2	623	20.1	<b>623</b>	<b>20.1</b>	624	20.0
473.astar	2	779	18.0	780	18.0	<b>779</b>	<b>18.0</b>	2	724	19.4	<b>724</b>	<b>19.4</b>	723	19.4
483.xalancbmk	2	407	33.9	408	33.8	<b>407</b>	<b>33.9</b>	2	407	33.9	408	33.8	<b>407</b>	<b>33.9</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores (default)

## Platform Notes

BIOS configuration:  
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable

## General Notes

All binaries were built with 32-bit Intel compiler except:  
401.bzip2 and 456.hammer in peak were built with 64-bit Intel  
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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