### IBM Corporation

**IBM System x3400 (Intel Xeon E5420)**

**SPEC® CFP2006 Result**

**SPECfp®_rate2006 = Not Run**

**SPECfp_rate_base2006 = 63.1**

**CPU2006 license:** 11  
**Test sponsor:** IBM Corporation  
**Tested by:** IBM Corporation  
**Test date:** Mar-2008  
**Hardware Availability:** Jan-2008  
**Software Availability:** Nov-2007  

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**Hardware**

- **CPU Name:** Intel Xeon E5420  
- **CPU Characteristics:** 1333MHz system bus  
- **CPU MHz:** 2500  
- **FPU:** Integrated  
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 12 MB I+D on chip per chip, 6 MB shared / 2 cores

**Software**

- **Operating System:** SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp  
- **Compiler:** Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008  
- **Auto Parallel:** No  
- **File System:** ReiserFS  
- **System State:** Multi-user, run level 3  
- **Base Pointers:** 64-bit

**COPYS**

- **SPECfp_rate_base2006 = 63.1**
IBM Corporation

IBM System x3400 (Intel Xeon E5420)

SPECfp_rate2006 = Not Run
SPECfp_rate_base2006 = 63.1

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Mar-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: Binutils 2.17.50.0.15

Results Table

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes
All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
taskset utility used to bind CPU(s) to processes
This result is measured on an IBM System x3500 Server. Note
that the IBM System x3500 and IBM System x3400 are electrically
equivalent.

Base Compiler Invocation

C benchmarks:
icc

Continued on next page
IBM Corporation  
IBM System x3400 (Intel Xeon E5420)  

SPEC CFP2006 Result

SPECfp_rate2006 = Not Run
SPECfp_rate_base2006 = 63.1

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Mar-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamec: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leshe3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64 -nofor_main
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast
IBM Corporation

IBM System x3400 (Intel Xeon E5420)  

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The flags file that was used to format this result can be browsed at  

You can also download the XML flags source by saving the following link:  

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.  
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