



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®2006 = 20.5

IBM System x3500 (Intel Xeon E5420)

SPECfp_base2006 = 17.3

CPU2006 license: 11

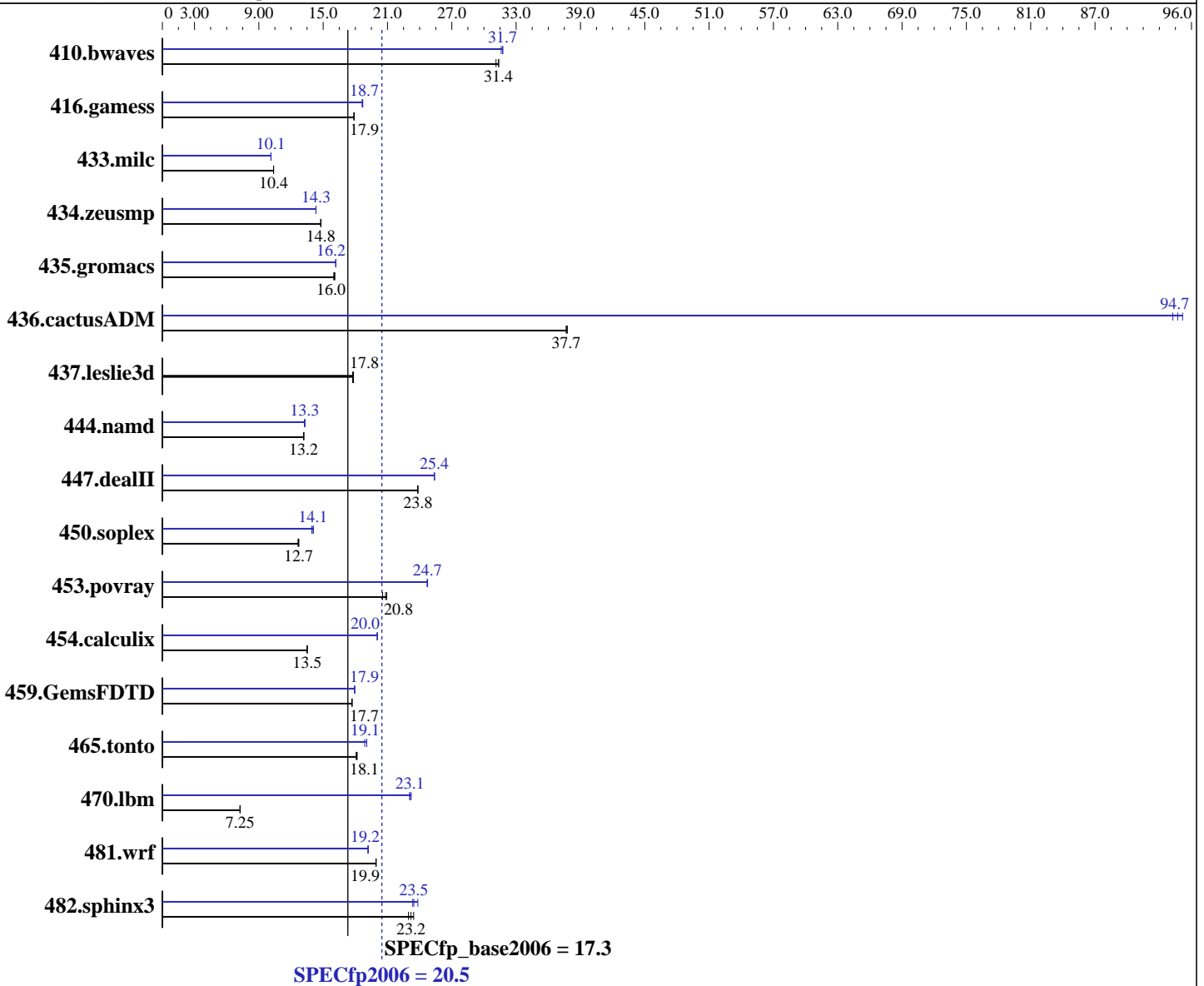
Test date: Mar-2008

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5420
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 64-bit

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L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	437	31.1	433	31.4	433	31.4	428	31.7	430	31.6	428	31.8
416.gamess	1094	17.9	1096	17.9	1096	17.9	1050	18.7	1050	18.6	1047	18.7
433.milc	885	10.4	885	10.4	884	10.4	905	10.1	906	10.1	907	10.1
434.zeusmp	615	14.8	616	14.8	615	14.8	635	14.3	635	14.3	635	14.3
435.gromacs	444	16.1	445	16.0	447	16.0	441	16.2	442	16.2	441	16.2
436.cactusADM	316	37.8	317	37.7	317	37.7	127	94.3	126	94.7	126	95.2
437.leslie3d	527	17.8	529	17.8	529	17.8	527	17.8	529	17.8	529	17.8
444.namd	609	13.2	608	13.2	608	13.2	605	13.3	604	13.3	603	13.3
447.dealII	480	23.8	481	23.8	480	23.8	451	25.4	451	25.4	451	25.4
450.soplex	655	12.7	656	12.7	659	12.7	592	14.1	593	14.1	599	13.9
453.povray	254	20.9	256	20.8	259	20.5	215	24.7	215	24.7	215	24.7
454.calculix	611	13.5	610	13.5	611	13.5	412	20.0	411	20.1	411	20.0
459.GemsFDTD	600	17.7	600	17.7	600	17.7	592	17.9	591	17.9	591	17.9
465.tonto	541	18.2	543	18.1	544	18.1	521	18.9	516	19.1	516	19.1
470.lbm	1898	7.24	1893	7.26	1894	7.25	592	23.2	595	23.1	596	23.1
481.wrf	561	19.9	561	19.9	560	20.0	582	19.2	582	19.2	582	19.2
482.sphinx3	831	23.4	849	23.0	840	23.2	818	23.8	831	23.5	835	23.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 64-bit mode except 450.soplex, 470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode
 Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Enabled
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to physical,0
 KMP_STACKSIZE set to 200M
 This result is measured on an IBM System x3500 Server. Note that the IBM System x3500 and IBM System x3400 are electrically equivalent.



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast -parallel

C++ benchmarks:

-fast -parallel

Fortran benchmarks:

-fast -parallel

Benchmarks using both Fortran and C:

-fast -parallel



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Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

```
433.milc: icc
```

C++ benchmarks (except as noted below):

```
icpc
```

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icc ifort
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32
```

```
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-req- -prefetch -opt-malloc-options=3
```

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Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealIII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.xml>



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