# SPEC® CINT2006 Result

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## Bull SAS

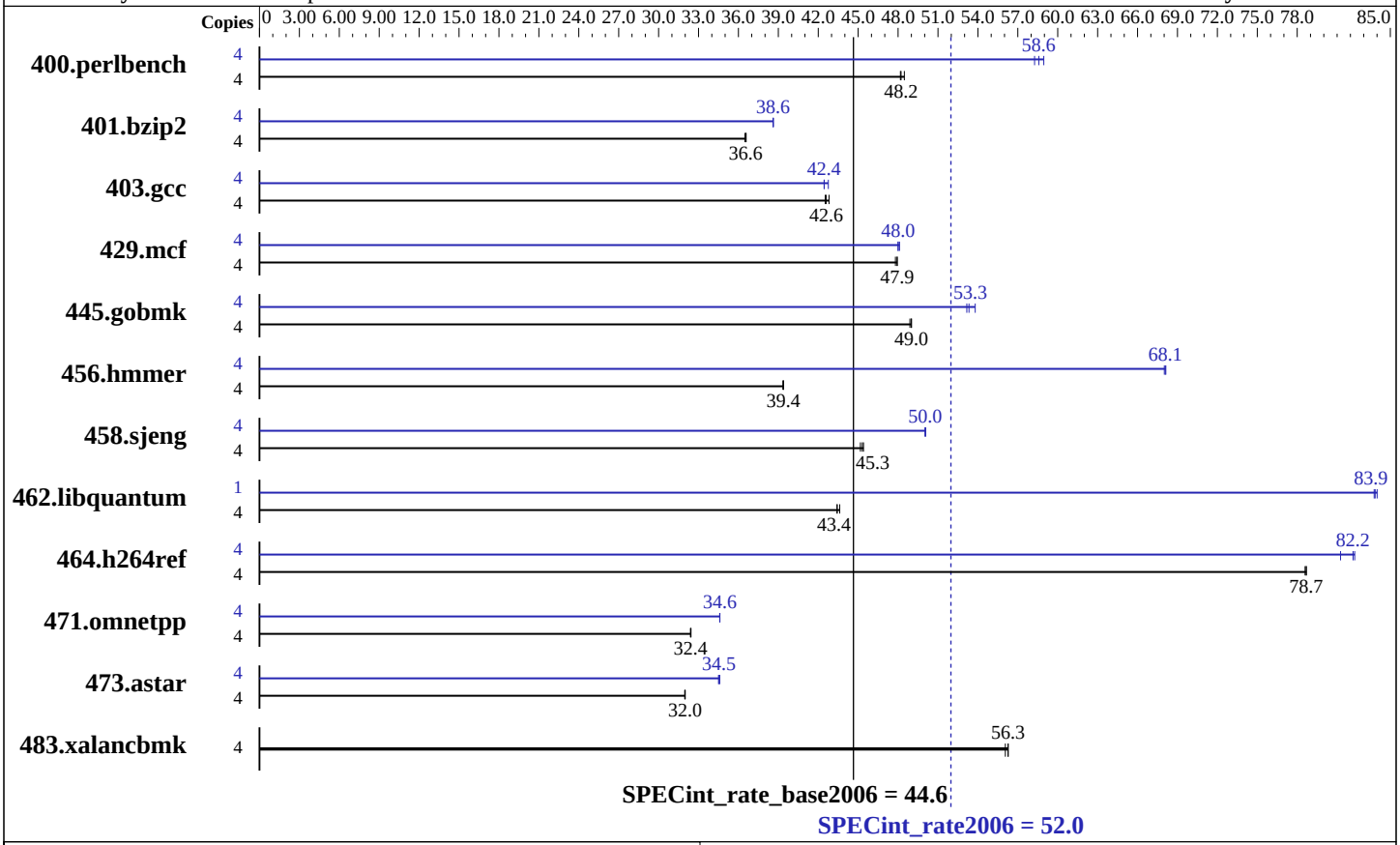
NovaScale R460 E1
(Intel Xeon E5205,1.86GHz)

SPECint®_rate2006 = 52.0

SPECint_rate_base2006 = 44.6

**CPU2006 license:** 20
**Test sponsor:** Bull SAS
**Tested by:** NEC Corporation

**Test date:** Feb-2008
**Hardware Availability:** Jan-2008
**Software Availability:** Nov-2007




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5205 |
| CPU Characteristics: | 1.86 GHz, 6 MB L2, 1066 MHz bus |
| CPU MHz: | 1861 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 2 chips, 2 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 6 MB I+D on chip per chip |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC) |
| Disk Subsystem: | 1x73.2 GB SAS, 15000RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp |
| Compiler: | Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008 |
| Auto Parallel: | Yes |
| File System: | ext2 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | 806 | 48.5 | 811 | 48.2 | **810** | **48.2** | 4 | 671 | 58.3 | **667** | **58.6** | 663 | 59.0 |
| 401.bzip2 | 4 | **1056** | **36.6** | 1055 | 36.6 | 1058 | 36.5 | 4 | **1000** | **38.6** | 999 | 38.6 | 1000 | 38.6 |
| 403.gcc | 4 | **756** | **42.6** | 752 | 42.8 | 757 | 42.5 | 4 | 759 | 42.4 | 753 | 42.8 | **759** | **42.4** |
| 429.mcf | 4 | **761** | **47.9** | 763 | 47.8 | 761 | 47.9 | 4 | 760 | 48.0 | 758 | 48.1 | **760** | **48.0** |
| 445.gobmk | 4 | 856 | 49.0 | **856** | **49.0** | 858 | 48.9 | 4 | **787** | **53.3** | 780 | 53.8 | 789 | 53.2 |
| 456.hmmer | 4 | 949 | 39.3 | **948** | **39.4** | 948 | 39.4 | 4 | **548** | **68.1** | 548 | 68.1 | 549 | 68.0 |
| 458.sjeng | 4 | **1068** | **45.3** | 1066 | 45.4 | 1072 | 45.2 | 4 | 967 | 50.1 | **967** | **50.0** | 967 | 50.0 |
| 462.libquantum | 4 | **1909** | **43.4** | 1900 | 43.6 | 1909 | 43.4 | 1 | 247 | 84.0 | 247 | 83.8 | **247** | **83.9** |
| 464.h264ref | 4 | 1125 | 78.7 | 1126 | 78.6 | **1125** | **78.7** | 4 | **1077** | **82.2** | 1075 | 82.3 | 1089 | 81.3 |
| 471.omnetpp | 4 | 771 | 32.4 | 772 | 32.4 | **771** | **32.4** | 4 | **723** | **34.6** | 722 | 34.6 | 723 | 34.6 |
| 473.astar | 4 | **878** | **32.0** | 877 | 32.0 | 878 | 32.0 | 4 | 812 | 34.6 | **813** | **34.5** | 814 | 34.5 |
| 483.xalancbmk | 4 | 490 | 56.3 | 492 | 56.1 | **490** | **56.3** | 4 | 490 | 56.3 | 492 | 56.1 | **490** | **56.3** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
 OMP_NUM_THREADS set to number of cores
```

## Platform Notes

```
Bios settings:
Intel SpeedStep Technology: Disabled
```

## General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

 The NEC Express5800/120Rh-1(Intel Xeon Prosessor E5205),
 the NEC Express5800/120Rj-2(Intel Xeon Prosessor E5205),
 the Bull NovaScale R440 E1 (Intel Xeon E5205,1.86GHz) and
 the Bull NovaScale R460 E1 (Intel Xeon E5205,1.86GHz) models are electronically equivalent.
 The results have been measured on a NEC Express5800/120Rj-2(Intel Xeon Prosessor E5205) model.
```

## Base Compiler Invocation

C benchmarks:
icc

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

# Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.01.xml`


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