SPEC® CINT2006 Result

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Acer Incorporated

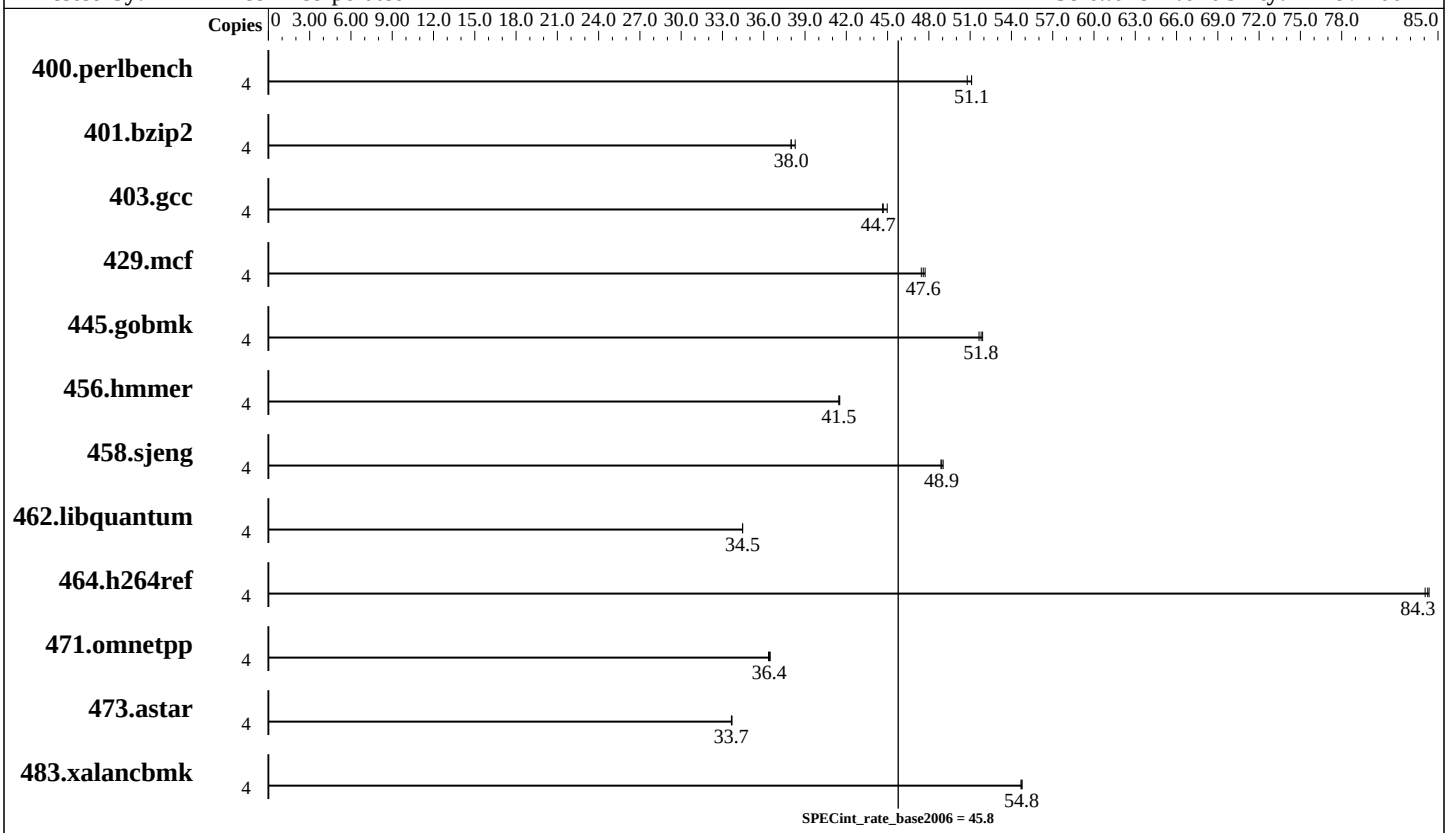
Acer Altos R520 (Intel Xeon E5405)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 45.8

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5405
CPU Characteristics:	1333MHz system bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem:	1 x 73GB 10K RPM SAS HDD
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 SP1 (x86_64) Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, level 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**765**	**51.1**	769	50.8	765	51.1							
401.bzip2	4	1008	38.3	**1016**	**38.0**	1016	38.0							
403.gcc	4	716	45.0	**721**	**44.7**	721	44.6							
429.mcf	4	764	47.7	**767**	**47.6**	769	47.4							
445.gobmk	4	812	51.6	808	51.9	**810**	**51.8**							
456.hmmer	4	**900**	**41.5**	900	41.5	899	41.5							
458.sjeng	4	**989**	**48.9**	987	49.0	990	48.9							
462.libquantum	4	**2405**	**34.5**	2405	34.5	2405	34.5							
464.h264ref	4	1049	84.4	**1051**	**84.3**	1053	84.1							
471.omnetpp	4	688	36.3	**687**	**36.4**	686	36.5							
473.astar	4	**834**	**33.7**	833	33.7	834	33.7							
483.xalancbmk	4	**504**	**54.8**	504	54.8	505	54.7							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
BIOS Settings
  Hardware Prefetcher set to OFF

  Adjacent Cache Line Prefetch set to OFF

All results but peak 462.libquantum used taskset to bind
processes to cores.
"ulimit -s unlimited" was set for stacksize unlimited
OMP_NUM_THREADS set to number of cores
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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