SPEC® CINT2006 Result

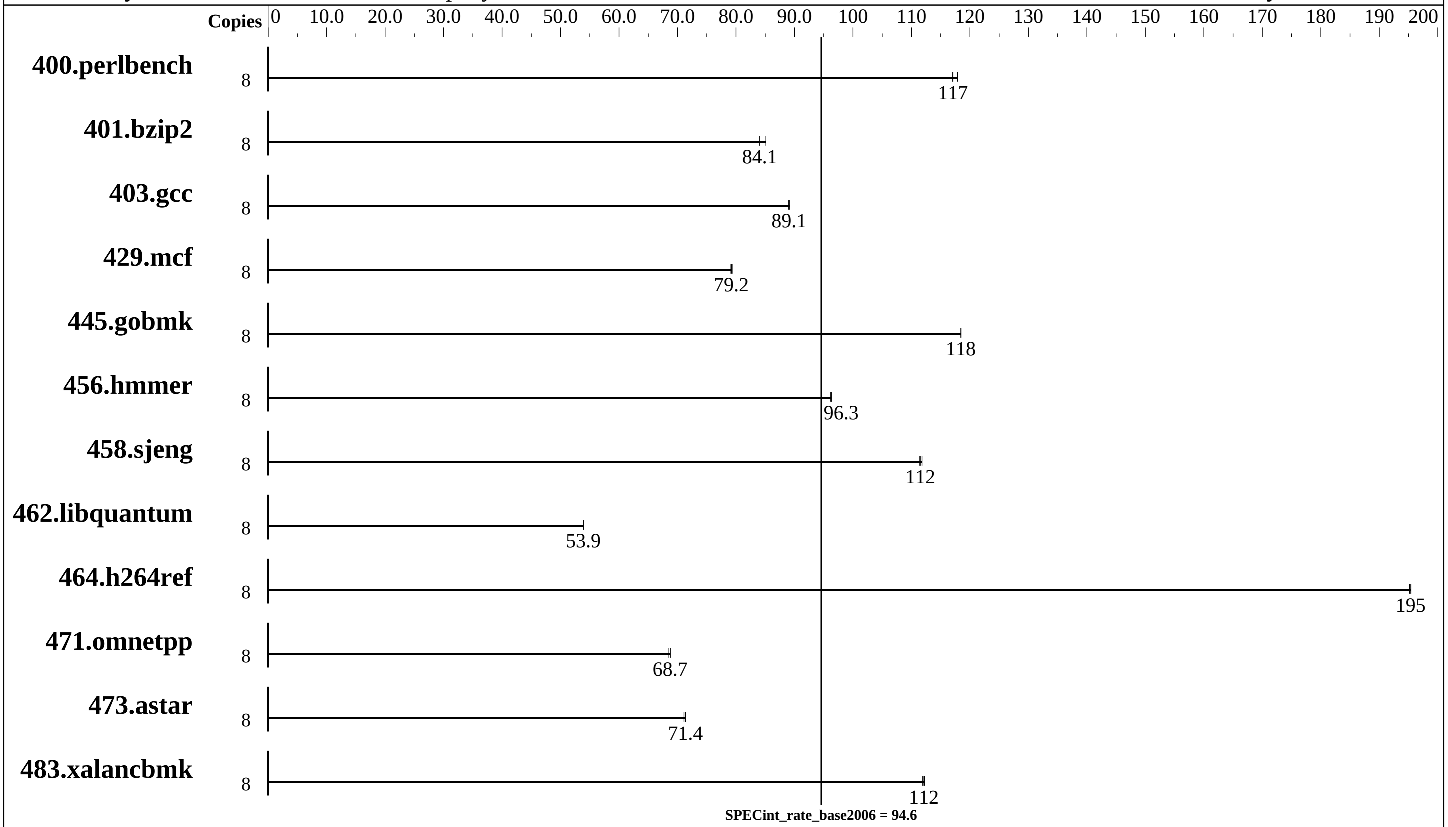
Hewlett-Packard Company

ProLiant BL460c
(2.33 GHz, Intel Xeon E5410)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 94.6

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company
Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	2.33 GHz, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem:	1x72 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	No
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.1 binutils-2.17.50

SPEC CINT2006 Result

Hewlett-Packard Company

ProLiant BL460c
(2.33 GHz, Intel Xeon E5410)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 94.6

CPU2006 license: 3 | **Test date:** Mar-2008
Test sponsor: Hewlett-Packard Company | **Hardware Availability:** Nov-2007
Tested by: Hewlett-Packard Company | **Software Availability:** Nov-2007

Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	663	118	**667**	**117**	668	117							
401.bzip2	8	919	84.0	**918**	**84.1**	907	85.1							
403.gcc	8	**723**	**89.1**	722	89.2	724	89.0							
429.mcf	8	**921**	**79.2**	922	79.1	919	79.4							
445.gobmk	8	709	118	708	118	**708**	**118**							
456.hmmer	8	776	96.2	775	96.3	**775**	**96.3**							
458.sjeng	8	869	111	866	112	**868**	**112**							
462.libquantum	8	3076	53.9	3077	53.9	**3076**	**53.9**							
464.h264ref	8	**906**	**195**	906	195	907	195							
471.omnetpp	8	**727**	**68.7**	727	68.8	730	68.5							
473.astar	8	787	71.4	790	71.1	**787**	**71.4**							
483.xalancbmk	8	492	112	493	112	**492**	**112**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch Disabled
  Hardware Prefetcher Disabled
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Hewlett-Packard Company

ProLiant BL460c
(2.33 GHz, Intel Xeon E5410)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 94.6

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company
Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/cpu2006/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 17:56:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 April 2008.