# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

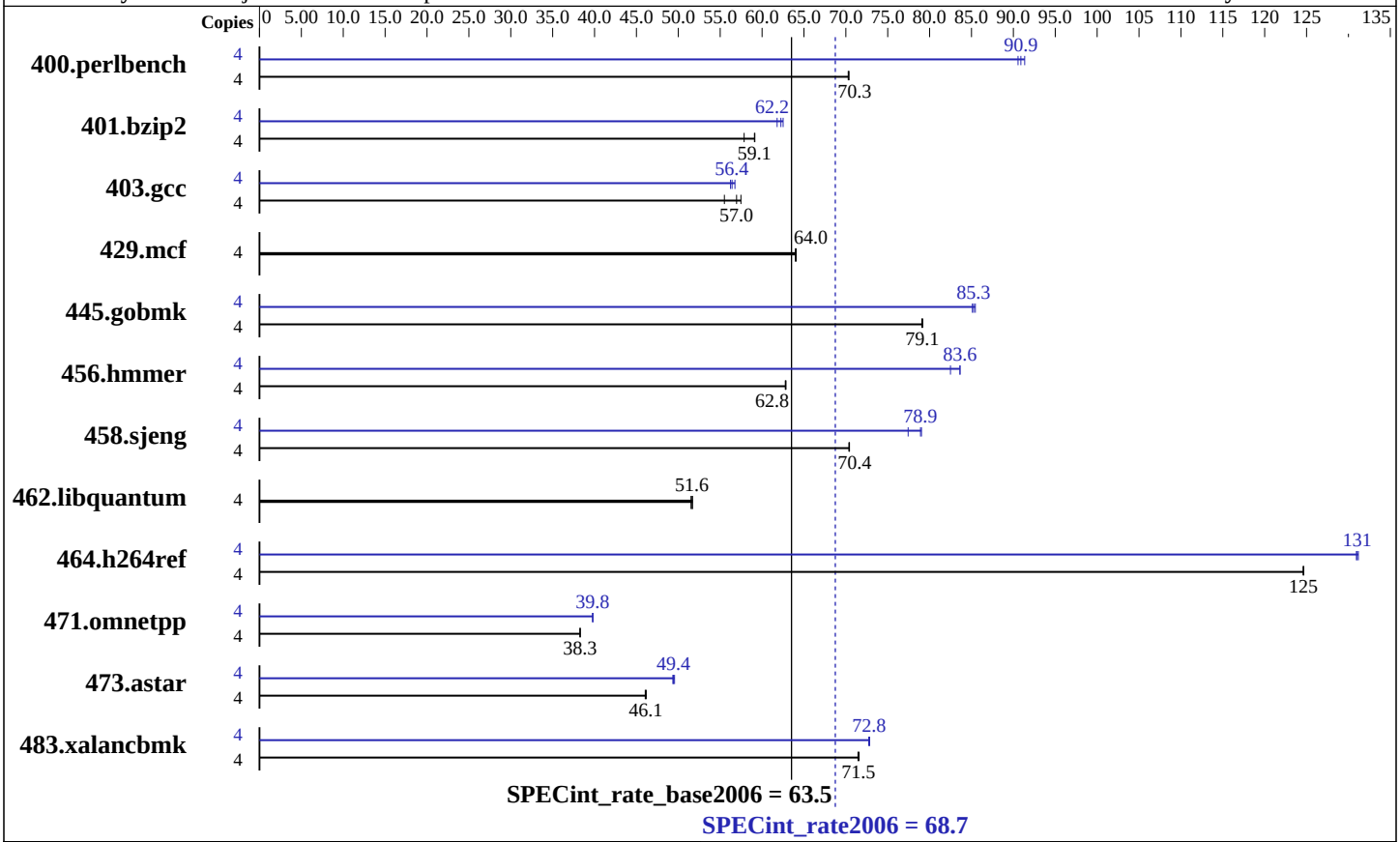
## CELSIUS M460, Intel Core 2 Extreme QX9650

SPECint®_rate2006 = 68.7

SPECint_rate_base2006 = 63.5

**CPU2006 license:** 22
**Test sponsor:** Fujitsu Siemens Computers
**Tested by:** Fujitsu Siemens Computers

**Test date:** Feb-2008
**Hardware Availability:** Mar-2008
**Software Availability:** Nov-2007




### Hardware

| | |
|---|---|
| CPU Name: | Intel Core 2 Extreme QX9650 |
| CPU Characteristics: | |
| CPU MHz: | 3000 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 12 MB I+D on chip per chip, 6 MB shared / 2 cores |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 4 GB (4x1 GB PC2-6400 CL6 SDRAM) |
| Disk Subsystem: | 1 x 400 GB SATA 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows Vista64 Ultimate |
| Compiler: | Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20070913<br>Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20070913<br>Microsoft Visual Studio 2005 with SP1 (for libraries) |
| Auto Parallel: | No |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | SmartHeap Library Version 8.1 from http://www.microquill.com/ |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | 556 | 70.3 | **556** | **70.3** | 555 | 70.4 | 4 | 428 | 91.4 | **430** | **90.9** | 432 | 90.6 |
| 401.bzip2 | 4 | 667 | 57.9 | 653 | 59.1 | **653** | **59.1** | 4 | 625 | 61.8 | 617 | 62.5 | **620** | **62.2** |
| 403.gcc | 4 | 560 | 57.5 | 580 | 55.5 | **565** | **57.0** | 4 | 573 | 56.2 | **571** | **56.4** | 567 | 56.8 |
| 429.mcf | 4 | 570 | 64.0 | **570** | **64.0** | 569 | 64.1 | 4 | 570 | 64.0 | **570** | **64.0** | 569 | 64.1 |
| 445.gobmk | 4 | 531 | 79.1 | **530** | **79.1** | 530 | 79.2 | 4 | 493 | 85.1 | **492** | **85.3** | 491 | 85.5 |
| 456.hmmer | 4 | 595 | 62.8 | **594** | **62.8** | 594 | 62.8 | 4 | 452 | 82.5 | 446 | 83.6 | **446** | **83.6** |
| 458.sjeng | 4 | 687 | 70.4 | 688 | 70.4 | **687** | **70.4** | 4 | 625 | 77.4 | 612 | 79.0 | **613** | **78.9** |
| 462.libquantum | 4 | 1609 | 51.5 | **1605** | **51.6** | 1603 | 51.7 | 4 | 1609 | 51.5 | **1605** | **51.6** | 1603 | 51.7 |
| 464.h264ref | 4 | 710 | 125 | 710 | 125 | **710** | **125** | 4 | **675** | **131** | 676 | 131 | 675 | 131 |
| 471.omnetpp | 4 | **653** | **38.3** | 653 | 38.3 | 653 | 38.3 | 4 | **628** | **39.8** | 628 | 39.8 | 629 | 39.8 |
| 473.astar | 4 | 610 | 46.1 | **609** | **46.1** | 608 | 46.2 | 4 | 567 | 49.5 | **568** | **49.4** | 569 | 49.3 |
| 483.xalancbmk | 4 | 386 | 71.4 | 386 | 71.6 | **386** | **71.5** | 4 | 379 | 72.8 | 379 | 72.7 | **379** | **72.8** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

```
BIOS default settings have been used.
```

## General Notes

```
 All binaries were built with 32-bit Intel compiler except:
 401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
 compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com/
```

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

## Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

## Base Optimization Flags

C benchmarks:
-fast -Qvec-guard-write -F512000000

C++ benchmarks:
-fast -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icl -Qvc8 -Qc99

401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

456.hmmer: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

C++ benchmarks:
icl -Qvc8

## Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64

Continued on next page

## Peak Portability Flags (Continued)

403.gcc: -DSPEC_CPU_WIN32
456.hmmer: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qprefetch -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch -F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias -F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive -F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 -F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

# Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.xml`


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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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