



# SPEC<sup>®</sup> CINT2006 Result

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## Itautec

## SPECint<sup>®</sup>\_rate2006 = Not Run

### Servidor Itautec MX201 (Intel Xeon X5460)

## SPECint\_rate\_base2006 = 114

CPU2006 license: 9001

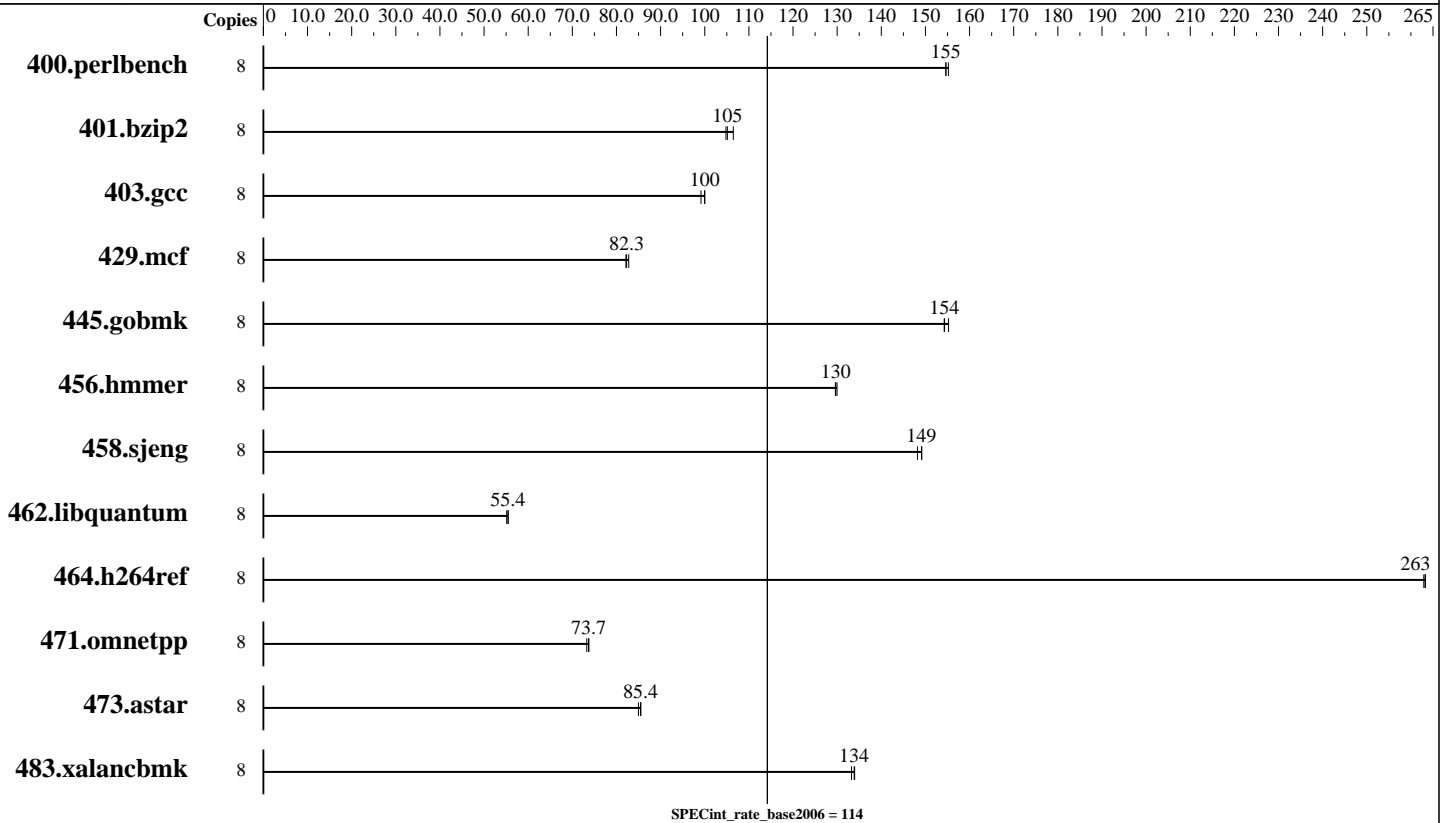
Test sponsor: Itautec

Tested by: Itautec

Test date: Mar-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



### Hardware

CPU Name: Intel Xeon X5460  
 CPU Characteristics:  
 CPU MHz: 3160  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 15000RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	505	155	504	155	<u>505</u>	<u>155</u>							
401.bzip2	8	725	106	737	105	<u>734</u>	<u>105</u>							
403.gcc	8	<b>644</b>	<b>100</b>	644	100	650	99.2							
429.mcf	8	881	82.8	<b>887</b>	<b>82.3</b>	888	82.1							
445.gobmk	8	541	155	<b>544</b>	<b>154</b>	544	154							
456.hammer	8	574	130	576	130	<b>576</b>	<b>130</b>							
458.sjeng	8	649	149	<b>649</b>	<b>149</b>	653	148							
462.libquantum	8	<b>2990</b>	<b>55.4</b>	2989	55.5	3007	55.1							
464.h264ref	8	<b>673</b>	<b>263</b>	672	263	673	263							
471.omnetpp	8	678	73.7	<b>679</b>	<b>73.7</b>	682	73.3							
473.astar	8	<b>657</b>	<b>85.4</b>	657	85.5	661	85.0							
483.xalancbmk	8	<b>412</b>	<b>134</b>	412	134	414	133							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Disabled

## General Notes

This result was measured on the Servidor Itaotec MX201.  
The Servidor Itaotec MX201 and the Servidor Itaotec MX221 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-FP-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-FP-intel64-linux-flags.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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