



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant BL260c G5
(3.0 GHz, Intel Xeon E5450)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 109

CPU2006 license: 3

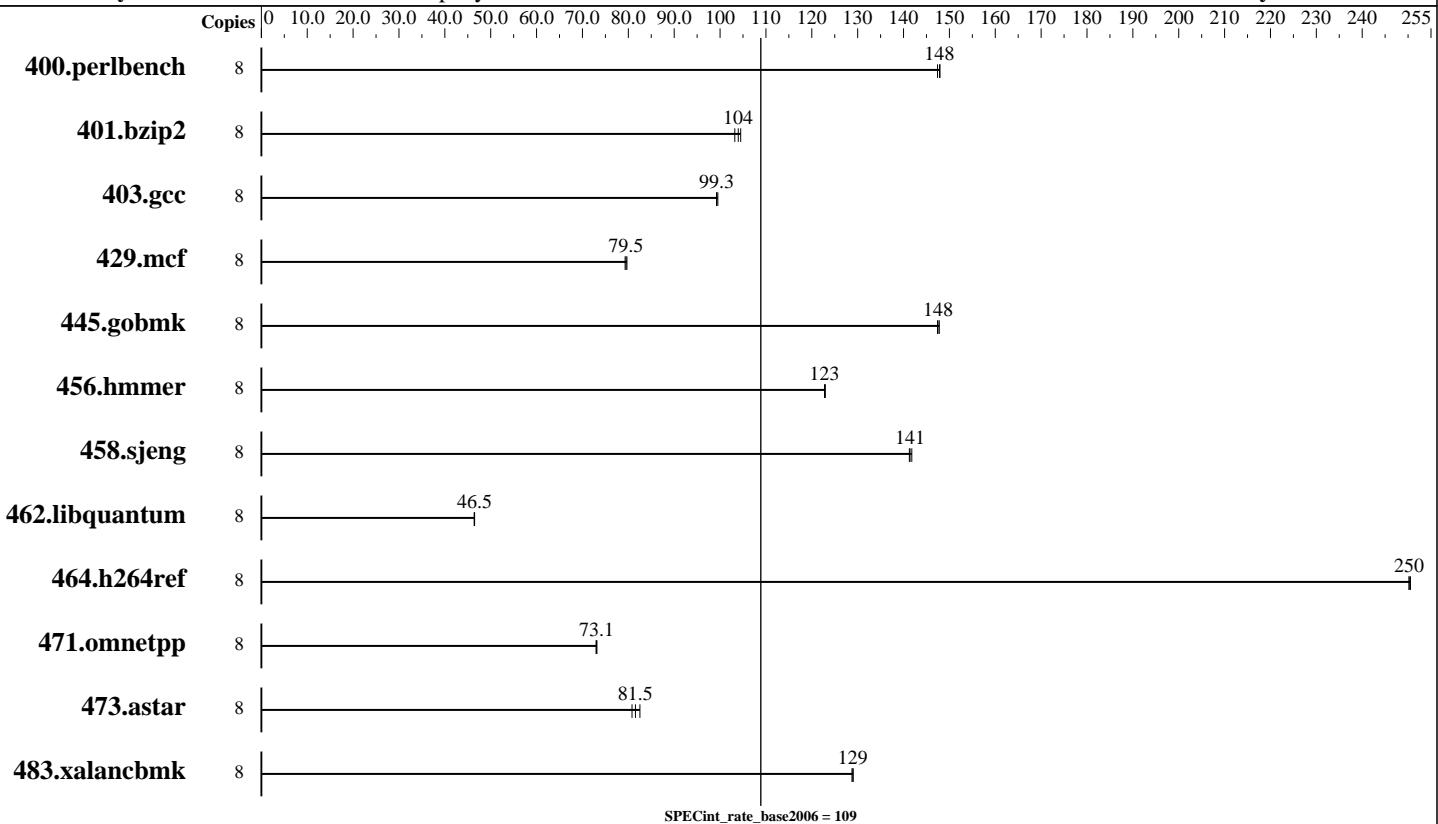
Test date: Mar-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5450
CPU Characteristics: 3.0 GHz, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (4x4 GB PC2-5300P CL5)
Disk Subsystem: 1x72 GB 15 K SAS
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: No
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	530	147	528	148	529	148							
401.bzip2	8	748	103	739	104	743	104							
403.gcc	8	647	99.5	649	99.2	649	99.3							
429.mcf	8	915	79.7	918	79.5	920	79.3							
445.gobmk	8	569	148	568	148	569	147							
456.hmmer	8	608	123	607	123	608	123							
458.sjeng	8	684	141	685	141	683	142							
462.libquantum	8	3573	46.4	3567	46.5	3566	46.5							
464.h264ref	8	707	250	707	251	708	250							
471.omnetpp	8	685	73.0	684	73.1	684	73.1							
473.astar	8	689	81.5	695	80.8	680	82.5							
483.xalancbmk	8	428	129	428	129	429	129							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:

Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled
Hardware Prefetcher Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 17:50:18 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 3 April 2008.