



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX300 S4, Intel Xeon E5430, 2.66 GHz

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 102

CPU2006 license: 22

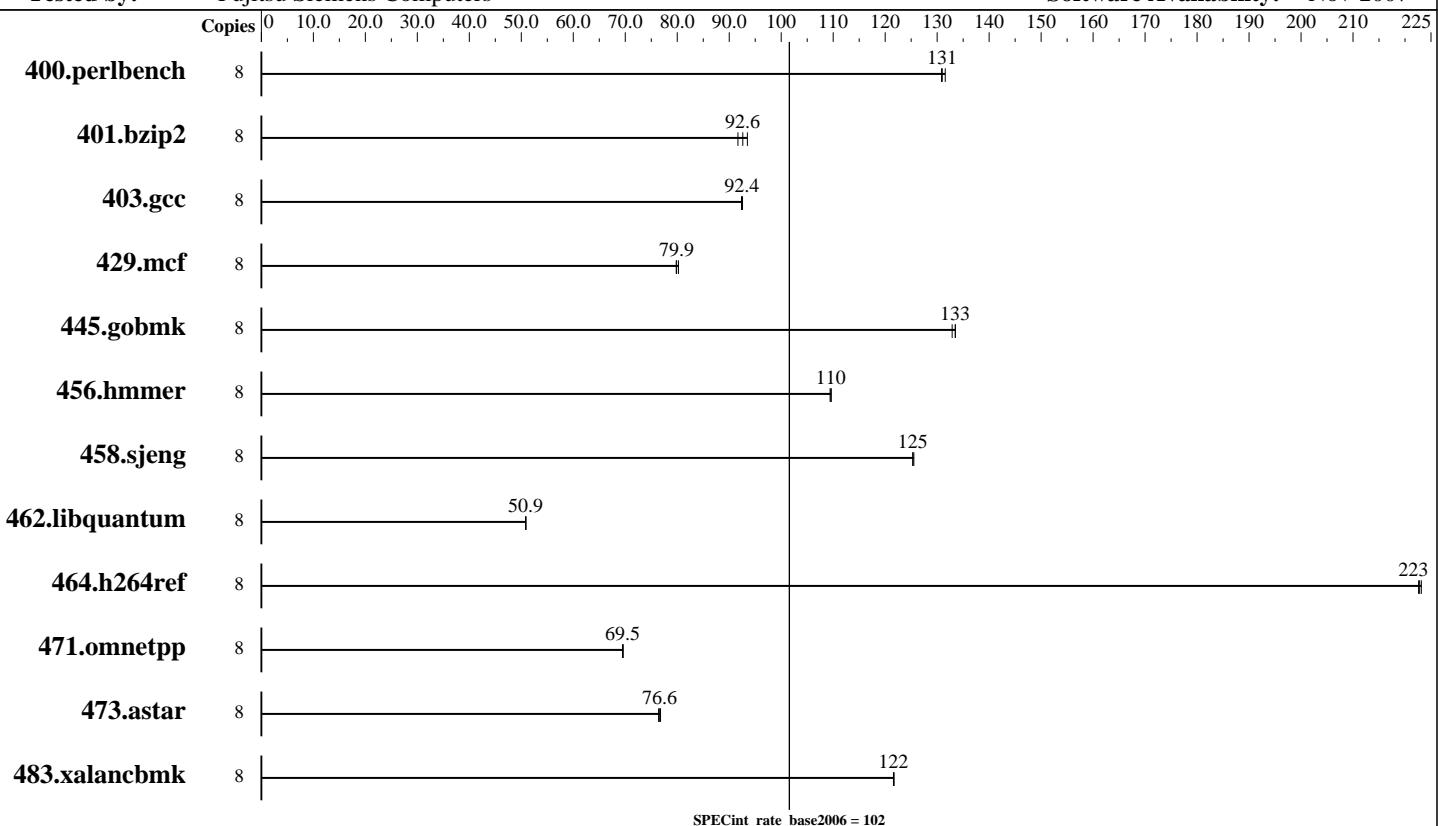
Test date: Mar-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Dec-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
Disk Subsystem: 1x SAS, 73 GB, 15000 rpm
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
Auto Parallel: No
File System: ext2
System State: Multi-User Run Level 3
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	597	131	594	132	597	131							
401.bzip2	8	834	92.6	842	91.7	826	93.5							
403.gcc	8	696	92.6	697	92.4	697	92.4							
429.mcf	8	909	80.2	913	79.9	914	79.8							
445.gobmk	8	629	134	629	133	632	133							
456.hmmer	8	682	109	681	110	681	110							
458.sjeng	8	771	126	772	125	772	125							
462.libquantum	8	3259	50.9	3258	50.9	3259	50.9							
464.h264ref	8	795	223	795	223	793	223							
471.omnetpp	8	719	69.5	718	69.6	720	69.5							
473.astar	8	733	76.6	735	76.4	731	76.8							
483.xalancbmk	8	454	122	454	122	454	122							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
 icc

C++ benchmarks:
 icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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