# SPEC® CINT2006 Result

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## Acer Incorporated

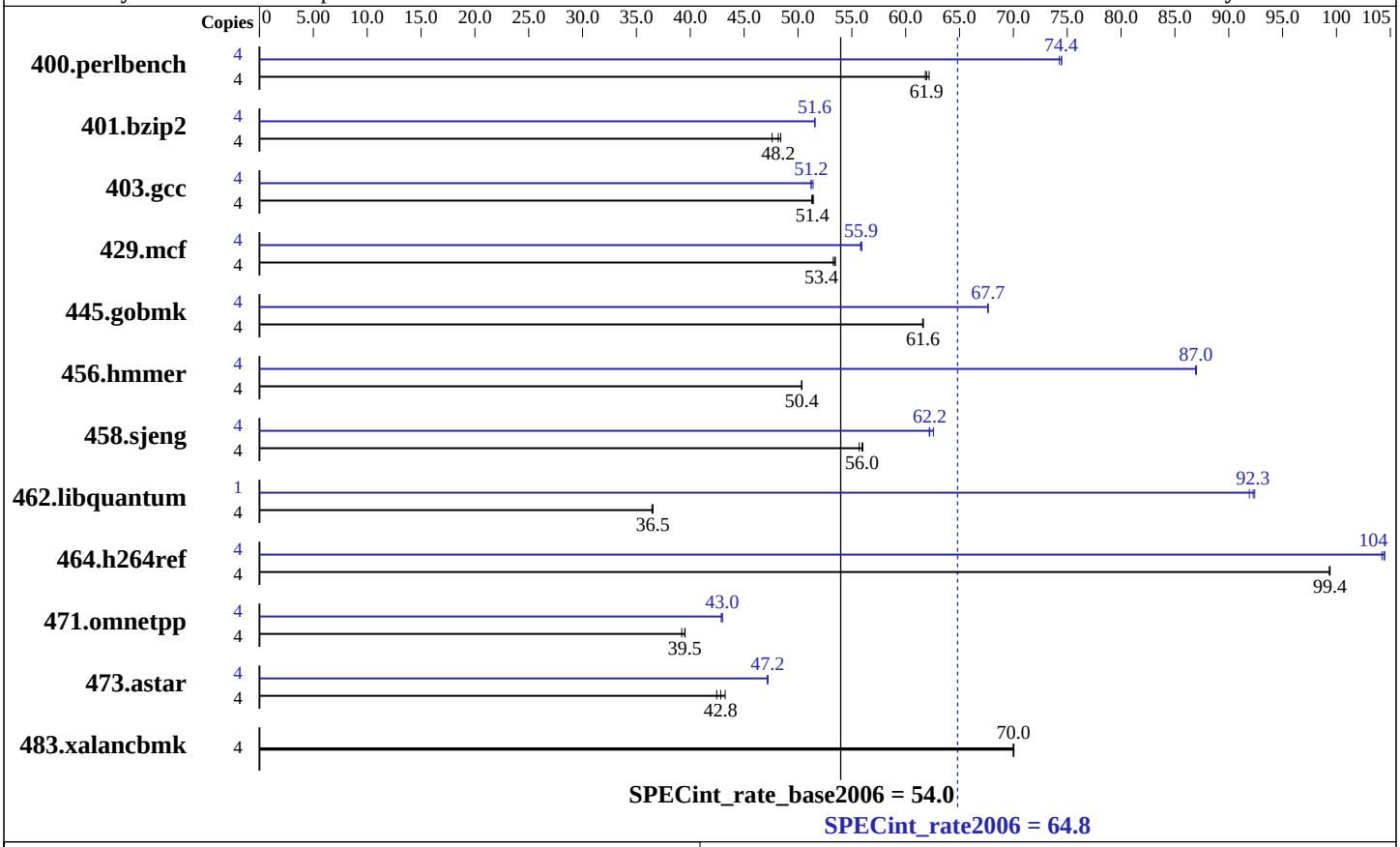
## Acer Altos R920 (Intel Xeon E7210)

SPECint®_rate2006 = 64.8

SPECint_rate_base2006 = 54.0

**CPU2006 license:** 97
**Test sponsor:** Acer Incorporated
**Tested by:** Acer Incorporated

**Test date:** Nov-2007
**Hardware Availability:** Nov-2007
**Software Availability:** Nov-2007




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E7210 |
| CPU Characteristics: | 1066MHz system bus |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 2 chips, 2 cores/chip |
| CPU(s) orderable: | 1, 2, 4 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 4 MB I+D on chip per core |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 16 GB (16 x 1048MB ECC FB-DIMM DDR2-667 CL5-5-5) |
| Disk Subsystem: | 1 x 73GB 10000PRM SAS HDD |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SuSE Linux Enterprise Server 10 SP1 (x86_64)<br>Kernel 2.6.16.46-0.12-smp |
| Compiler: | Intel C++ Compiler 10.1 for Linux<br>Build 20070913 Package ID: l_cc_p_10.1.008 |
| Auto Parallel: | Yes |
| File System: | ReiserFS |
| System State: | Multi-user, level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1<br>Binutils 2.17.50.0.15 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | **631** | **61.9** | 629 | 62.2 | 632 | 61.8 | 4 | 525 | 74.5 | **525** | **74.4** | 526 | 74.3 |
| 401.bzip2 | 4 | 811 | 47.6 | 797 | 48.4 | **801** | **48.2** | 4 | 749 | 51.6 | 748 | 51.6 | **749** | **51.6** |
| 403.gcc | 4 | **627** | **51.4** | 626 | 51.4 | 628 | 51.3 | 4 | **628** | **51.2** | 629 | 51.2 | 626 | 51.4 |
| 429.mcf | 4 | 685 | 53.3 | **683** | **53.4** | 682 | 53.5 | 4 | **653** | **55.9** | 654 | 55.8 | 652 | 55.9 |
| 445.gobmk | 4 | 681 | 61.6 | 681 | 61.6 | **681** | **61.6** | 4 | 620 | 67.7 | **620** | **67.7** | 621 | 67.6 |
| 456.hmmer | 4 | **741** | **50.4** | 741 | 50.4 | 741 | 50.3 | 4 | 429 | 87.0 | 429 | 86.9 | **429** | **87.0** |
| 458.sjeng | 4 | 869 | 55.7 | 864 | 56.0 | **865** | **56.0** | 4 | **778** | **62.2** | 778 | 62.2 | 773 | 62.6 |
| 462.libquantum | 4 | 2275 | 36.4 | **2269** | **36.5** | 2268 | 36.5 | 1 | **225** | **92.3** | 224 | 92.4 | 225 | 91.9 |
| 464.h264ref | 4 | **891** | **99.4** | 891 | 99.4 | 891 | 99.3 | 4 | 847 | 104 | 849 | 104 | **848** | **104** |
| 471.omnetpp | 4 | 633 | 39.5 | **633** | **39.5** | 638 | 39.2 | 4 | 583 | 42.9 | 582 | 43.0 | **582** | **43.0** |
| 473.astar | 4 | 649 | 43.2 | 661 | 42.5 | **656** | **42.8** | 4 | 596 | 47.1 | 595 | 47.2 | **595** | **47.2** |
| 483.xalancbmk | 4 | 394 | 70.1 | 394 | 70.0 | **394** | **70.0** | 4 | 394 | 70.1 | 394 | 70.0 | **394** | **70.0** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode

BIOS Settings
  Hardware Prefetcher set to OFF

  Adjacent Cache Line Prefetch set to OFF

All results but peak 462.libquantum used taskset to bind
processes to cores.
"ulimit -s unlimited" was set for stacksize unlimited
OMP_NUM_THREADS set to number of cores
```

## Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

# Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml`


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