SPEC® CINT2006 Result
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Acer Incorporated

Acer Altos R920 (Intel Xeon E7330)

SPECint®_rate2006 =   91.5

SPECint_rate_base2006 = 82.4

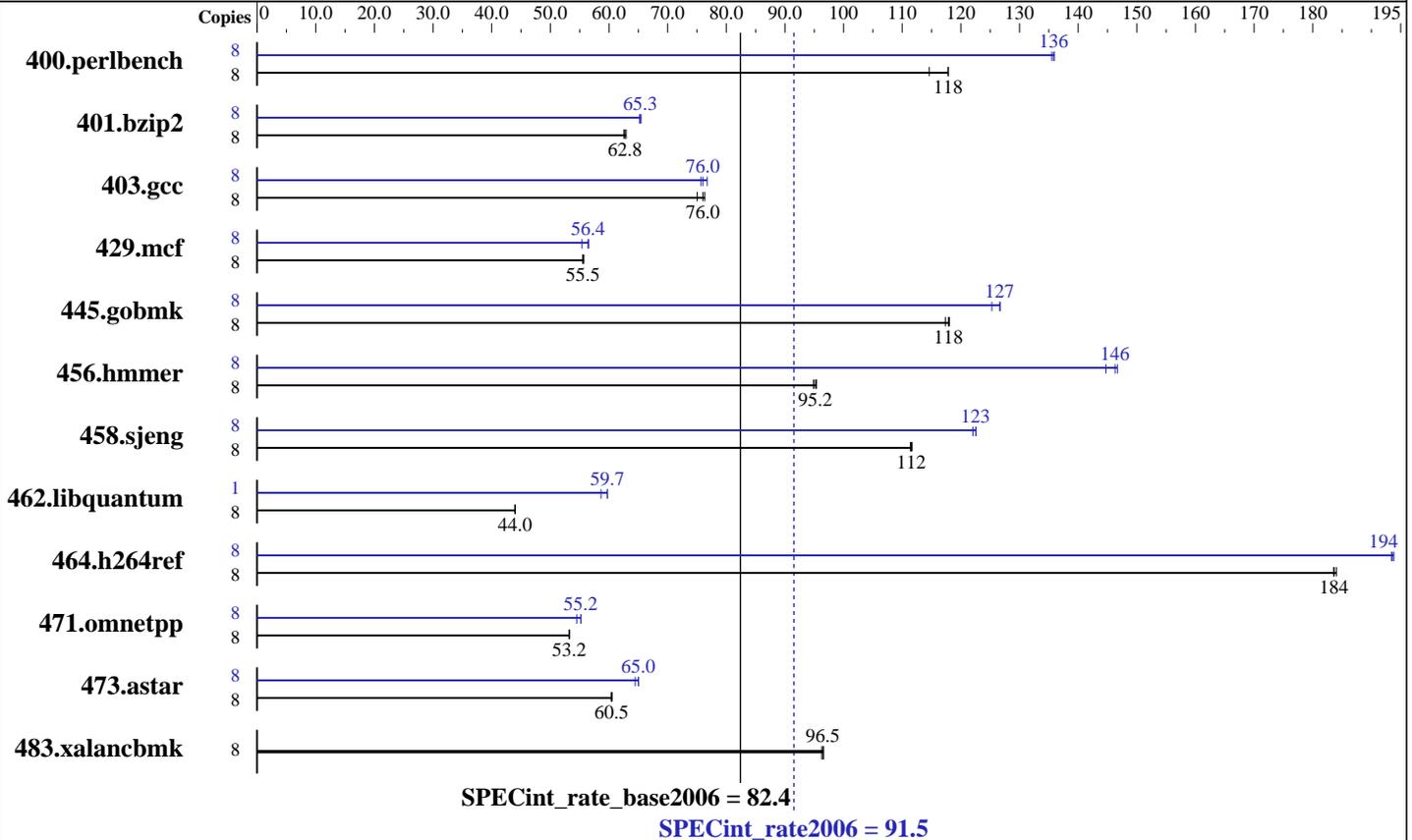**CPU2006 license:** 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Copies		
400.perlbench	8	136
	8	118
401.bzip2	8	65.3
	8	62.8
403.gcc	8	76.0
	8	76.0
429.mcf	8	56.4
	8	55.5
445.gobmk	8	127
	8	118
456.hmmer	8	146
	8	95.2
458.sjeng	8	123
	8	112
462.libquantum	1	59.7
	8	44.0
464.h264ref	8	194
	8	184
471.omnetpp	8	55.2
	8	53.2
473.astar	8	65.0
	8	60.5
483.xalancbmk	8	96.5

SPECint_rate_base2006 = 82.4

SPECint_rate2006 = 91.5

Hardware

CPU Name: Intel Xeon E7330
CPU Characteristics: 1066MHz system bus
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1, 2, 4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (16 x 1048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73GB 10000PRM SAS HDD
Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 (x86_64)
Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1
Build 20070725
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 8.1
Binutils 2.17.50.0.15

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	682	115	**663**	**118**	663	118	8	**575**	**136**	575	136	577	135
401.bzip2	8	1234	62.6	1227	62.9	**1230**	**62.8**	8	**1182**	**65.3**	1179	65.5	1183	65.2
403.gcc	8	844	76.3	**847**	**76.0**	858	75.1	8	**847**	**76.0**	851	75.7	839	76.8
429.mcf	8	1309	55.7	**1313**	**55.5**	1315	55.5	8	1317	55.4	**1293**	**56.4**	1290	56.6
445.gobmk	8	**712**	**118**	711	118	715	117	8	**663**	**127**	662	127	670	125
456.hmmer	8	782	95.4	**784**	**95.2**	787	94.9	8	516	145	509	147	**510**	**146**
458.sjeng	8	**868**	**112**	867	112	869	111	8	790	123	793	122	**790**	**123**
462.libquantum	8	3765	44.0	3772	43.9	**3765**	**44.0**	1	353	58.6	347	59.8	**347**	**59.7**
464.h264ref	8	965	184	**964**	**184**	962	184	8	**914**	**194**	913	194	915	193
471.omnetpp	8	**939**	**53.2**	939	53.3	940	53.2	8	917	54.5	905	55.2	**906**	**55.2**
473.astar	8	**929**	**60.5**	930	60.4	927	60.6	8	871	64.5	863	65.1	**864**	**65.0**
483.xalancbmk	8	**572**	**96.5**	571	96.6	573	96.3	8	**572**	**96.5**	571	96.6	573	96.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
BIOS Settings
  Hardware Prefetcher set to OFF

  Adjacent Cache Line Prefetch set to OFF

All results but peak 462.libquantum used taskset to bind
processes to cores.
"ulimit -s unlimited" was set for stacksize unlimited
OMP_NUM_THREADS set to number of cores
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Continued on next page

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Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.html

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You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.xml

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 18:11:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 April 2008.