SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

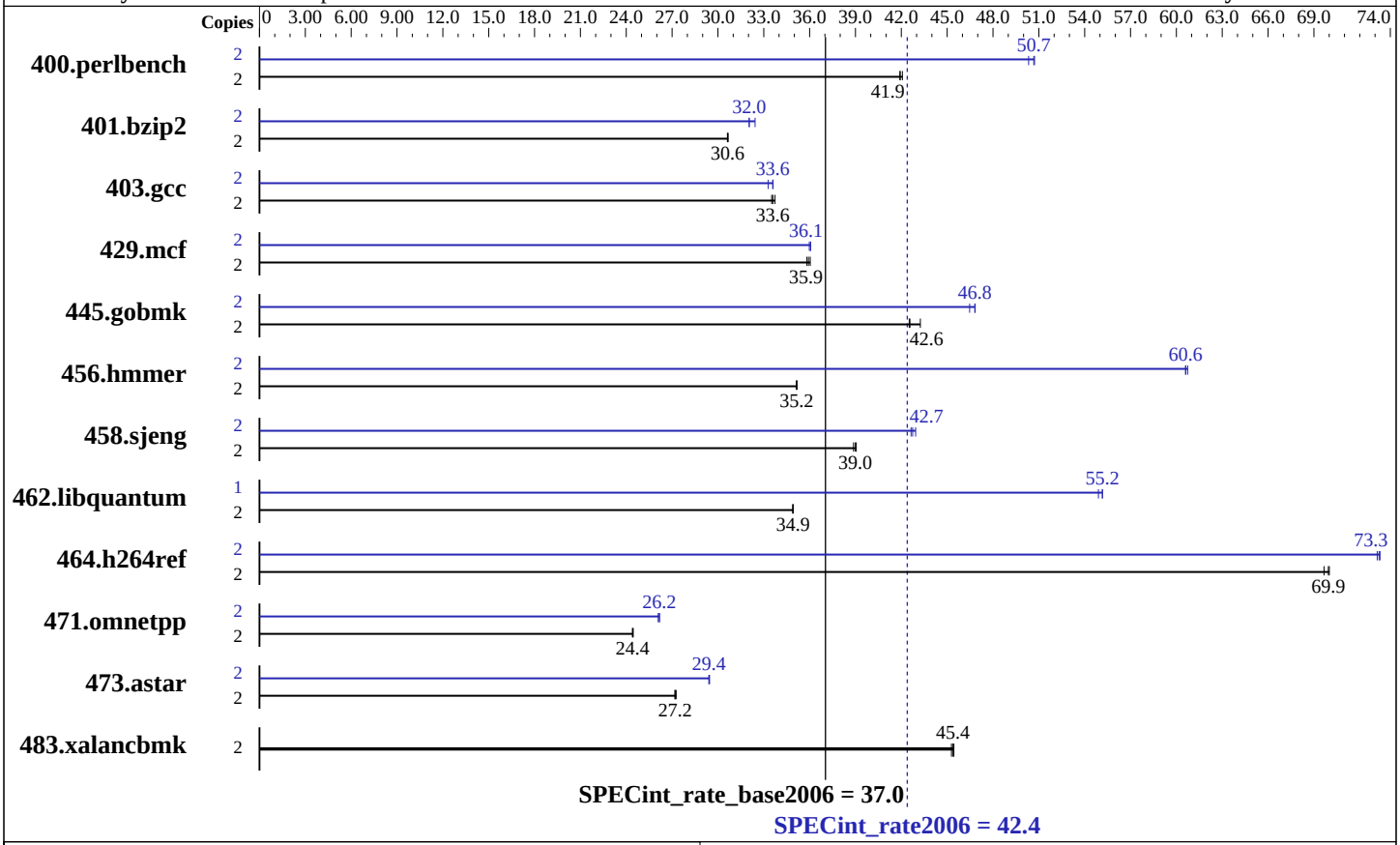
Acer Altos R720 (Intel Xeon X5260)

SPECint®_rate2006 = 42.4

SPECint_rate_base2006 = 37.0

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	1333MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem:	1 x 73GB 10000PRM SAS HDD
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 SP1 (x86_64) Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Acer Altos R720 (Intel Xeon X5260)

SPECint_rate2006 = 42.4

SPECint_rate_base2006 = 37.0

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	464	42.1	**466**	**41.9**	466	41.9	2	385	50.7	388	50.3	**386**	**50.7**
401.bzip2	2	629	30.7	**630**	**30.6**	630	30.6	2	**602**	**32.0**	595	32.4	602	32.0
403.gcc	2	480	33.5	**479**	**33.6**	477	33.7	2	479	33.6	484	33.3	**479**	**33.6**
429.mcf	2	506	36.0	509	35.8	**508**	**35.9**	2	**506**	**36.1**	506	36.1	507	36.0
445.gobmk	2	**493**	**42.6**	493	42.5	485	43.2	2	**448**	**46.8**	451	46.5	448	46.8
456.hmmer	2	**531**	**35.2**	530	35.2	531	35.1	2	308	60.6	307	60.7	**308**	**60.6**
458.sjeng	2	**621**	**39.0**	620	39.1	623	38.9	2	568	42.6	**566**	**42.7**	564	42.9
462.libquantum	2	1187	34.9	1188	34.9	**1187**	**34.9**	1	376	55.2	377	54.9	**376**	**55.2**
464.h264ref	2	635	69.7	632	70.0	**633**	**69.9**	2	**604**	**73.3**	604	73.3	605	73.1
471.omnetpp	2	511	24.5	512	24.4	**512**	**24.4**	2	479	26.1	**478**	**26.2**	478	26.2
473.astar	2	515	27.3	**516**	**27.2**	516	27.2	2	**477**	**29.4**	477	29.5	477	29.4
483.xalancbmk	2	305	45.3	**304**	**45.4**	304	45.4	2	305	45.3	**304**	**45.4**	304	45.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
BIOS Settings
  Hardware Prefetcher set to ON

  Adjacent Cache Line Prefetch set to ON

All results but peak 462.libquantum used taskset to bind
processes to cores.
"ulimit -s unlimited" was set for stacksize unlimited
OMP_NUM_THREADS set to number of cores
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 18:09:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 April 2008.