SPEC® CINT2006 Result

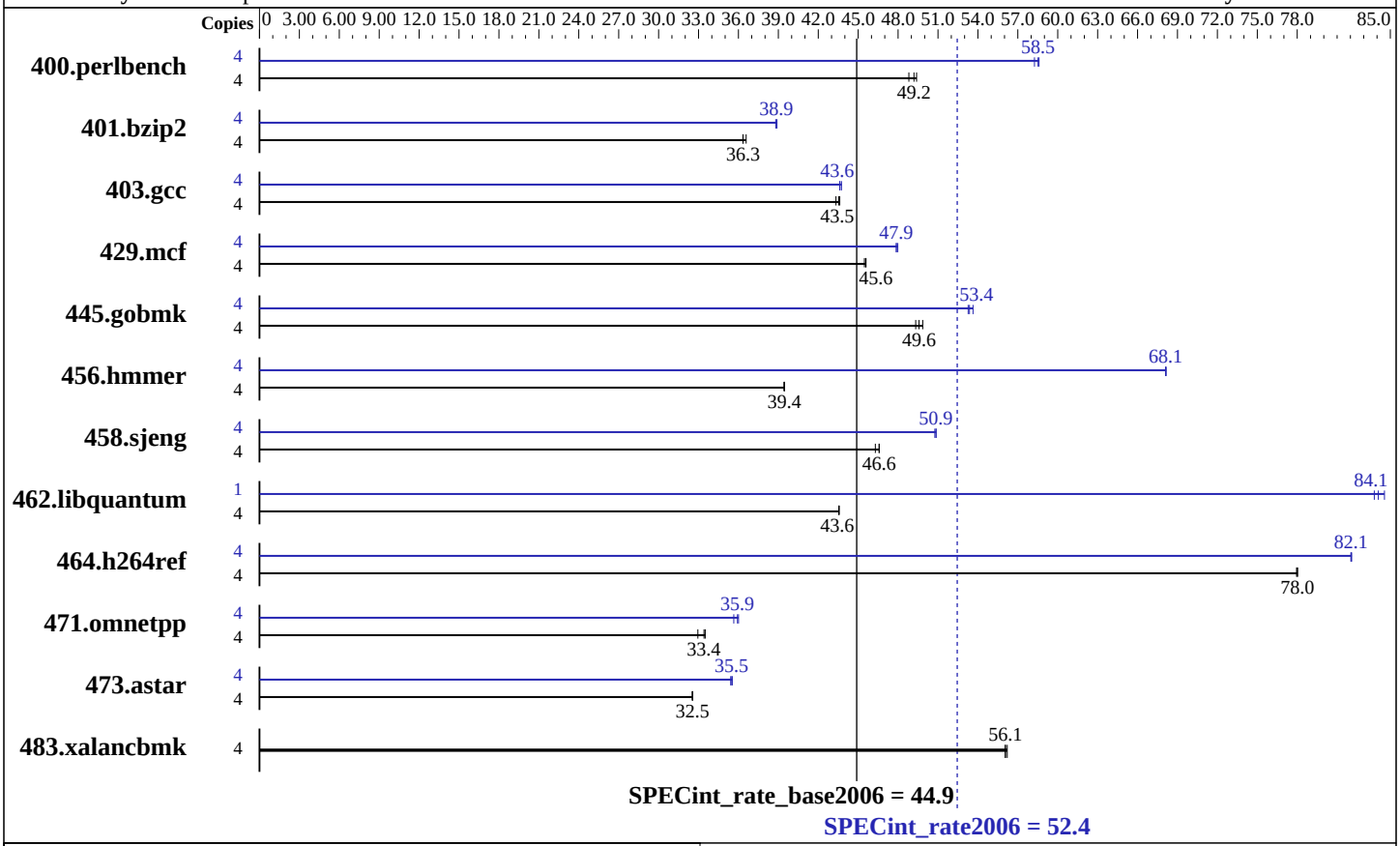
Supermicro Motherboard X7DBN

SPECint®_rate2006 = 52.4

SPECint_rate_base2006 = 44.9

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5205
CPU Characteristics:	Dual Core, 1.86GHz
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 X 1GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem:	Western DigitalD WD1600YS-01SHB1 160GB SATA II, 7200RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, kernel 2.6.16.46-0.12-default
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**794**	**49.2**	801	48.8	791	49.4	4	667	58.6	671	58.3	**668**	**58.5**
401.bzip2	4	1062	36.3	**1062**	**36.3**	1055	36.6	4	995	38.8	**994**	**38.9**	992	38.9
403.gcc	4	738	43.6	**740**	**43.5**	743	43.3	4	736	43.7	**738**	**43.6**	738	43.6
429.mcf	4	802	45.5	800	45.6	**801**	**45.6**	4	**762**	**47.9**	760	48.0	762	47.9
445.gobmk	4	842	49.9	**846**	**49.6**	851	49.3	4	788	53.3	782	53.6	**786**	**53.4**
456.hmmer	4	946	39.5	**946**	**39.4**	947	39.4	4	548	68.2	548	68.1	**548**	**68.1**
458.sjeng	4	**1039**	**46.6**	1039	46.6	1046	46.3	4	952	50.9	953	50.8	**952**	**50.9**
462.libquantum	4	1902	43.6	**1903**	**43.6**	1903	43.6	1	**246**	**84.1**	245	84.6	247	83.8
464.h264ref	4	1135	78.0	1136	78.0	**1135**	**78.0**	4	1079	82.0	1078	82.1	**1079**	**82.1**
471.omnetpp	4	**748**	**33.4**	746	33.5	759	33.0	4	**696**	**35.9**	694	36.0	701	35.7
473.astar	4	863	32.5	**863**	**32.5**	862	32.6	4	790	35.5	**791**	**35.5**	793	35.4
483.xalancbmk	4	**492**	**56.1**	492	56.1	491	56.2	4	**492**	**56.1**	492	56.1	491	56.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with CSE-825TQ-560LPV case,
To ensure system stability, a 550W (minimum) ATX power supply
[4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBN.cfm
The system bus runs at 1066 MHz
The "taskset" command was used to bind benchmark copies to cores
except for 462.libquantum peak
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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