SPEC® CINT2006 Result

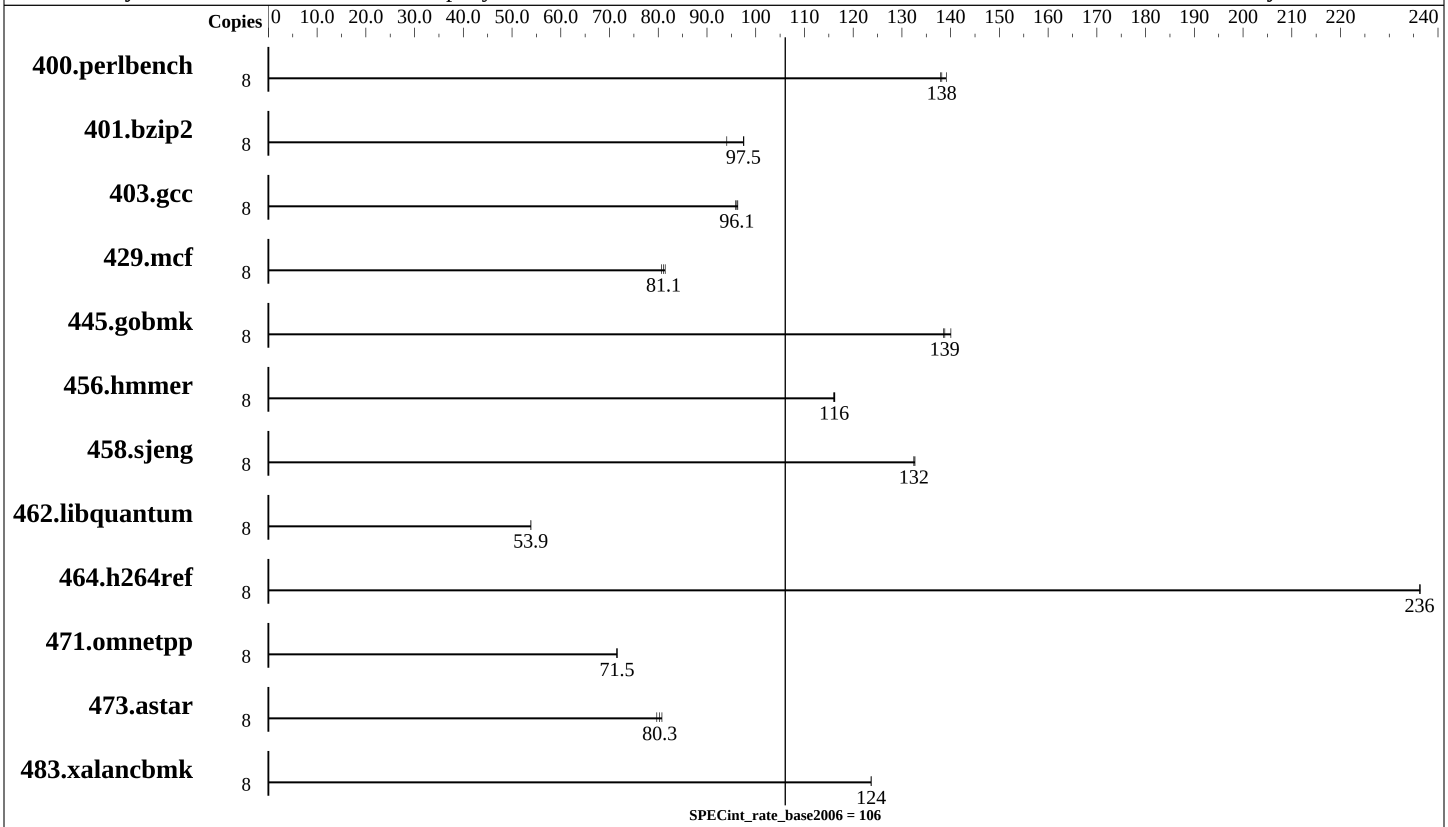
Hewlett-Packard Company

ProLiant BL460c
(2.83 GHz, Intel Xeon E5440)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 106

CPU2006 license: 3		**Test date:**	Feb-2008
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Nov-2007
Tested by:	Hewlett-Packard Company	**Software Availability:**	Nov-2007



Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	2.83 GHz, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem:	1x72 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	No
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.1 binutils-2.17.50

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	8	562	139	**566**	**138**	567	138							
401.bzip2	8	**792**	**97.5**	821	94.1	791	97.6							
403.gcc	8	669	96.3	672	95.9	**670**	**96.1**							
429.mcf	8	896	81.4	905	80.6	**900**	**81.1**							
445.gobmk	8	599	140	**605**	**139**	606	139							
456.hmmer	8	642	116	644	116	**643**	**116**							
458.sjeng	8	**731**	**132**	731	132	730	133							
462.libquantum	8	3078	53.9	3079	53.8	**3078**	**53.9**							
464.h264ref	8	749	236	749	236	**749**	**236**							
471.omnetpp	8	698	71.6	**699**	**71.5**	700	71.4							
473.astar	8	695	80.8	705	79.7	**699**	**80.3**							
483.xalancbmk	8	**446**	**124**	446	124	446	124							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch Disabled
  Hardware Prefetcher Disabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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