



# SPEC® CINT2006 Result

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## Itaotec

## SPECint®\_rate2006 = Not Run

### Servidor Itaotec MX221 (Intel Xeon E5405)

## SPECint\_rate\_base2006 = 86.9

CPU2006 license: 9001

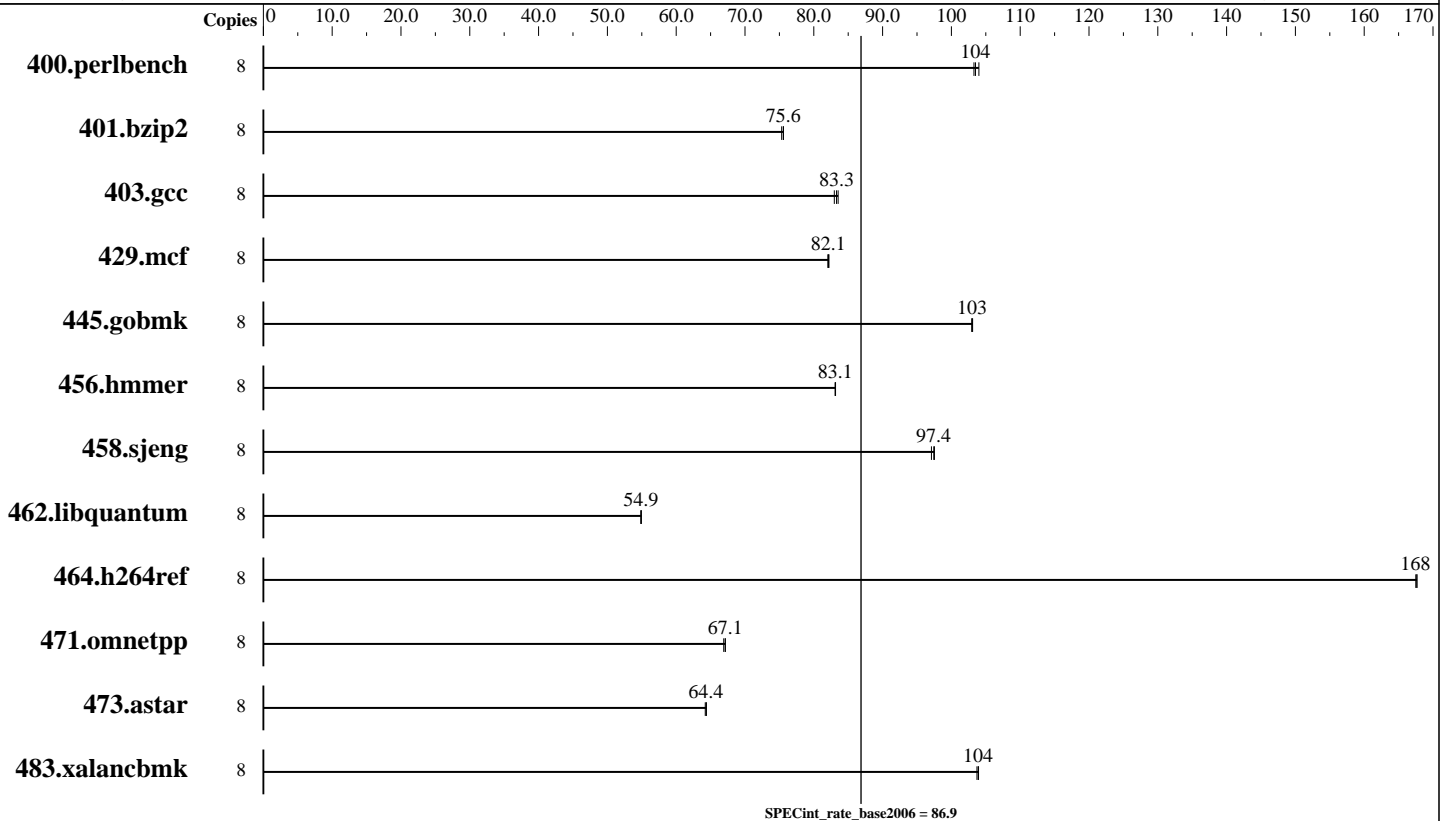
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Feb-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



### Hardware

CPU Name: Intel Xeon E5405  
 CPU Characteristics:  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2 GB DDR2 5300F, 2 rank, CL5 ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 15000RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-User, Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Binutils 2.17.10.50, MicroQuill SmartHeap 8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	752	104	757	103	<u>755</u>	<u>104</u>							
401.bzip2	8	1025	75.3	1021	75.6	<u>1021</u>	<u>75.6</u>							
403.gcc	8	776	83.0	<u>773</u>	<u>83.3</u>	771	83.5							
429.mcf	8	888	82.2	889	82.0	<u>888</u>	<u>82.1</u>							
445.gobmk	8	814	103	815	103	<u>815</u>	<u>103</u>							
456.hammer	8	898	83.2	<u>898</u>	<u>83.1</u>	898	83.1							
458.sjeng	8	997	97.1	<u>993</u>	<u>97.4</u>	992	97.5							
462.libquantum	8	3021	54.9	<u>3019</u>	<u>54.9</u>	3018	54.9							
464.h264ref	8	1057	167	1056	168	<u>1056</u>	<u>168</u>							
471.omnetpp	8	<u>745</u>	<u>67.1</u>	747	66.9	745	67.1							
473.astar	8	874	64.2	872	64.4	<u>873</u>	<u>64.4</u>							
483.xalancbmk	8	<u>531</u>	<u>104</u>	532	104	531	104							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS configuration:  
Hardware Prefetcher Disabled (default Enabled)

## General Notes

This result was measured on the Servidor Itautec MX201.  
The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.



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## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-alloc -opt-malloc-options=3  
  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs -L/CPU2006/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic10-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic10-ia32-intel64-linux-flags.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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