



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 119

PRIMERGY RX300 S4, Intel Xeon L5420, 2.50 GHz

SPECint_rate_base2006 = 96.8

CPU2006 license: 22

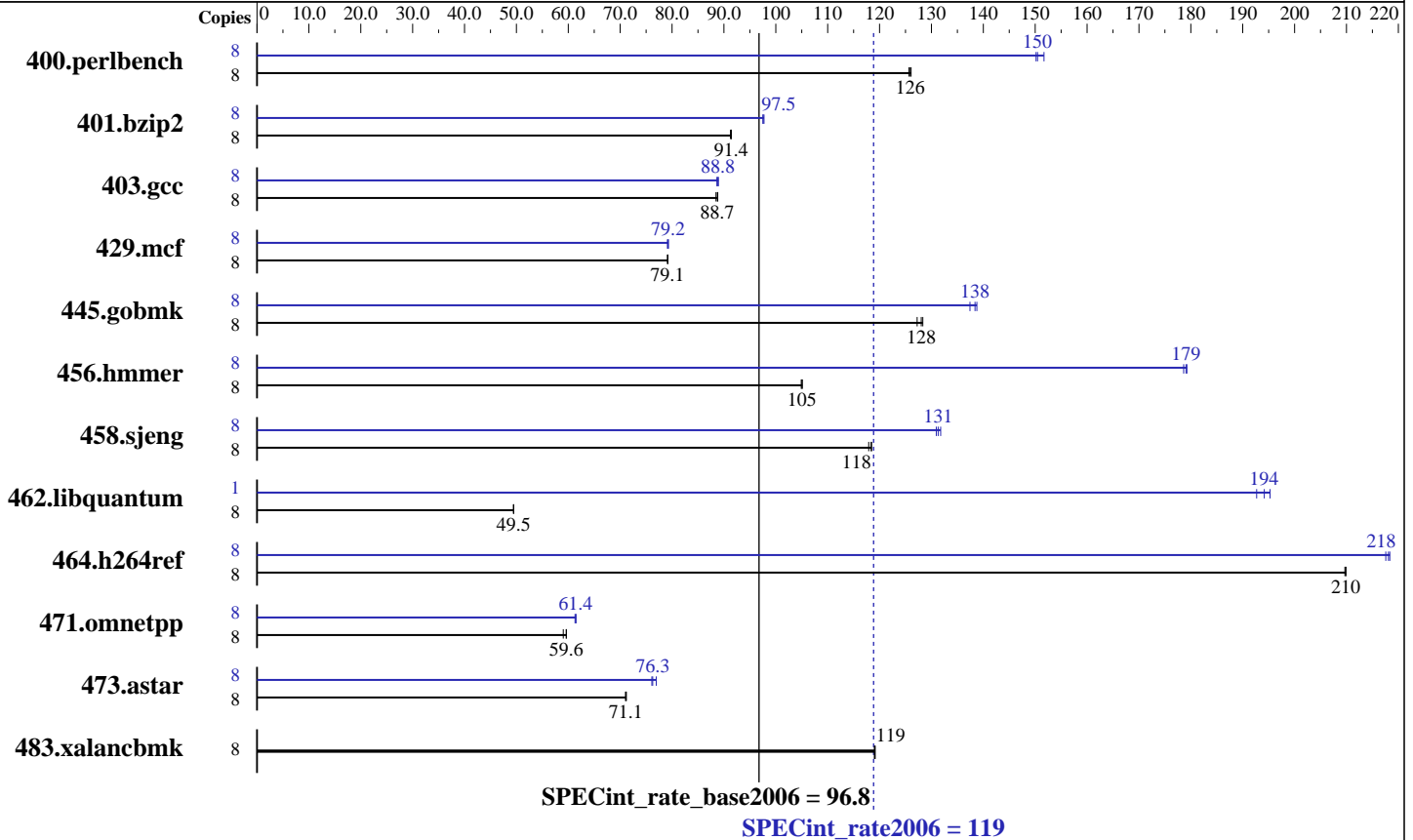
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon L5420
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
 Disk Subsystem: 1x SAS, 73 GB, 15000 rpm
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	620	126	<u>621</u>	<u>126</u>	622	126	8	515	152	<u>520</u>	<u>150</u>	520	150
401.bzip2	8	844	91.4	<u>845</u>	<u>91.4</u>	846	91.3	8	<u>791</u>	<u>97.5</u>	790	97.7	792	97.5
403.gcc	8	725	88.8	<u>726</u>	<u>88.7</u>	728	88.5	8	727	88.6	<u>725</u>	<u>88.8</u>	724	88.9
429.mcf	8	922	79.1	<u>922</u>	<u>79.1</u>	921	79.2	8	<u>921</u>	<u>79.2</u>	922	79.1	920	79.3
445.gobmk	8	654	128	<u>655</u>	<u>128</u>	660	127	8	<u>606</u>	<u>138</u>	611	137	605	139
456.hmmmer	8	712	105	710	105	<u>711</u>	<u>105</u>	8	418	179	416	179	<u>417</u>	<u>179</u>
458.sjeng	8	<u>818</u>	<u>118</u>	817	118	821	118	8	735	132	<u>737</u>	<u>131</u>	739	131
462.libquantum	8	<u>3351</u>	<u>49.5</u>	3351	49.5	3351	49.5	1	108	193	106	195	<u>107</u>	<u>194</u>
464.h264ref	8	844	210	844	210	<u>844</u>	<u>210</u>	8	814	218	811	218	<u>812</u>	<u>218</u>
471.omnetpp	8	847	59.0	<u>839</u>	<u>59.6</u>	839	59.6	8	816	61.3	<u>814</u>	<u>61.4</u>	813	61.5
473.astar	8	789	71.2	<u>790</u>	<u>71.1</u>	791	71.0	8	730	77.0	738	76.1	<u>736</u>	<u>76.3</u>
483.xalancbmk	8	463	119	464	119	<u>463</u>	<u>119</u>	8	463	119	464	119	<u>463</u>	<u>119</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

Platform Notes

BIOS configuration:
Adjacent Sector Prefetch = Enable

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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