



SPEC® CFP2006 Result

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NEC Corporation

Express5800/120Rh-1
(Intel Xeon X5260)

SPECfp®2006 = 23.6

SPECfp_base2006 = 20.1

CPU2006 license: 9006

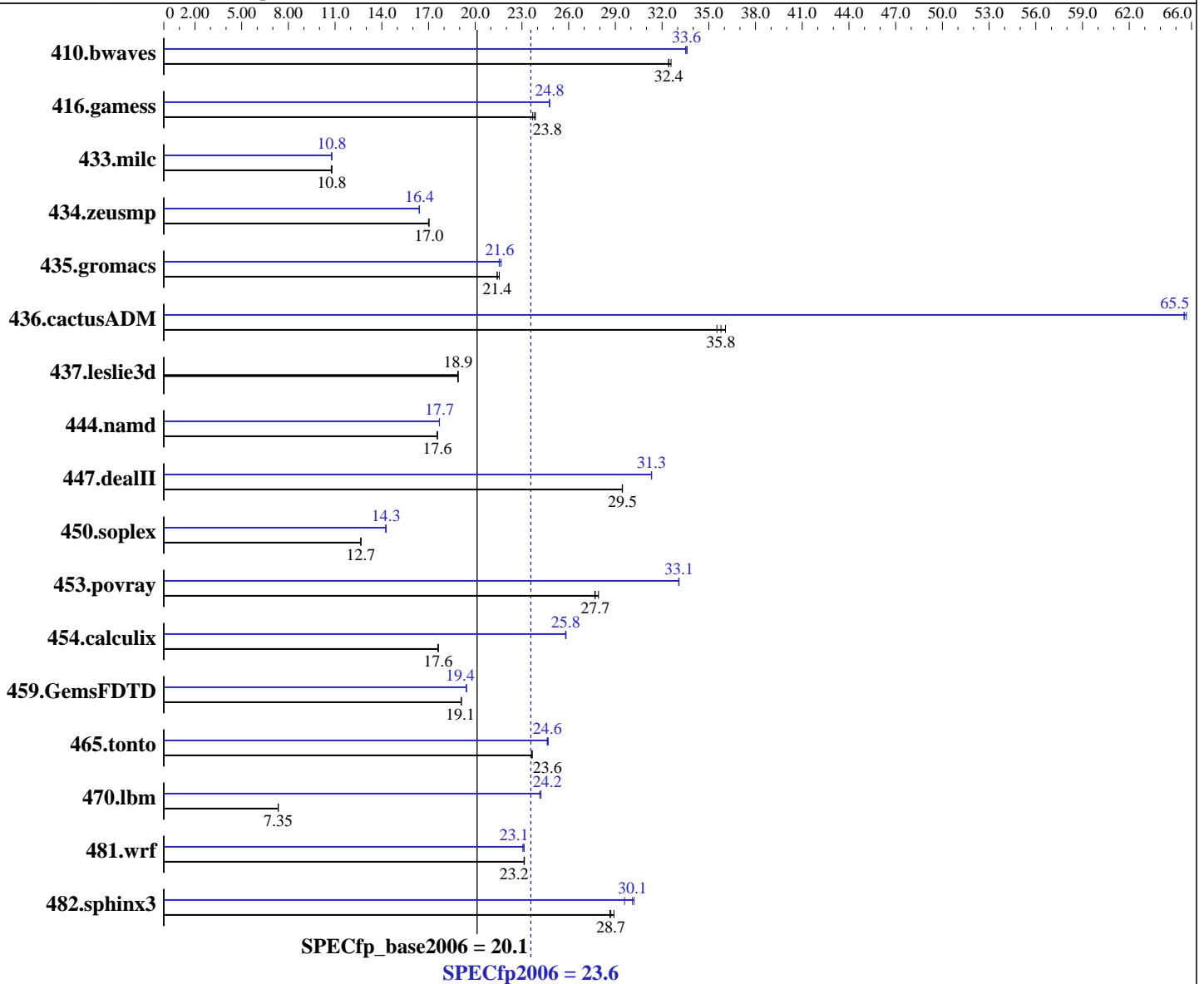
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Feb-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 3.33 GHz, 6 MB L2, 1333 MHz bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1,
Kernel 2.6.16.46-0.12-smpp
 Compiler: Intel C++ and Fortran Compiler for Linux32 and
Linux64
 version 10.1 Build 20070913 Package ID:
l_cc_p_10.1.008,
l_fc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2

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L3 Cache: None
Other Cache: None
Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x146.5 GB SAS, 15000RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: binutils-2.17.tar.gz, Version 2.17

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	419	32.4	417	32.6	419	32.4	406	33.5	405	33.6	404	33.6
416.gamess	820	23.9	827	23.7	822	23.8	791	24.8	790	24.8	791	24.8
433.milc	851	10.8	850	10.8	854	10.8	852	10.8	850	10.8	852	10.8
434.zeusmp	534	17.0	535	17.0	534	17.0	555	16.4	554	16.4	554	16.4
435.gromacs	333	21.4	333	21.4	331	21.5	331	21.6	332	21.5	329	21.7
436.cactusADM	331	36.1	336	35.5	334	35.8	182	65.5	182	65.7	182	65.5
437.leslie3d	497	18.9	497	18.9	498	18.9	497	18.9	497	18.9	498	18.9
444.namd	456	17.6	457	17.6	457	17.5	453	17.7	453	17.7	453	17.7
447.dealII	388	29.5	388	29.5	389	29.4	365	31.3	365	31.3	365	31.3
450.soplex	659	12.7	659	12.7	659	12.7	584	14.3	584	14.3	586	14.2
453.povray	192	27.7	192	27.7	191	27.9	161	33.1	161	33.1	161	33.1
454.calculix	468	17.6	469	17.6	468	17.6	319	25.8	320	25.8	320	25.8
459.GemsFDTD	555	19.1	555	19.1	556	19.1	546	19.4	546	19.4	546	19.4
465.tonto	416	23.7	416	23.6	417	23.6	399	24.7	400	24.6	399	24.6
470.lbm	1868	7.36	1870	7.35	1871	7.34	568	24.2	567	24.2	569	24.1
481.wrf	482	23.2	483	23.1	482	23.2	483	23.1	484	23.1	484	23.1
482.sphinx3	680	28.7	674	28.9	679	28.7	645	30.2	648	30.1	659	29.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Intel SpeedStep Technology: Disabled

General Notes

All benchmarks compiled in 64-bit mode except 450.soplex,
470.lbm and 482.sphinx3, for peak, are compiled in 32-bit mode

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General Notes (Continued)

The NEC Express5800/120Rh-1(Intel Xeon Processor X5260), the NEC Express5800/120Rj-2(Intel Xeon Processor X5260), the Bull NovaScale R440 E1 (Intel Xeon X5260,3.33GHz) and the Bull NovaScale R460 E1 (Intel Xeon X5260,3.33GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/120Rj-2(Intel Xeon Processor X5260) model.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast -parallel

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Base Optimization Flags (Continued)

C++ benchmarks:

-fast -parallel

Fortran benchmarks:

-fast -parallel

Benchmarks using both Fortran and C:

-fast -parallel

Peak Compiler Invocation

C benchmarks (except as noted below):

/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

433.milc: icc

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

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