SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

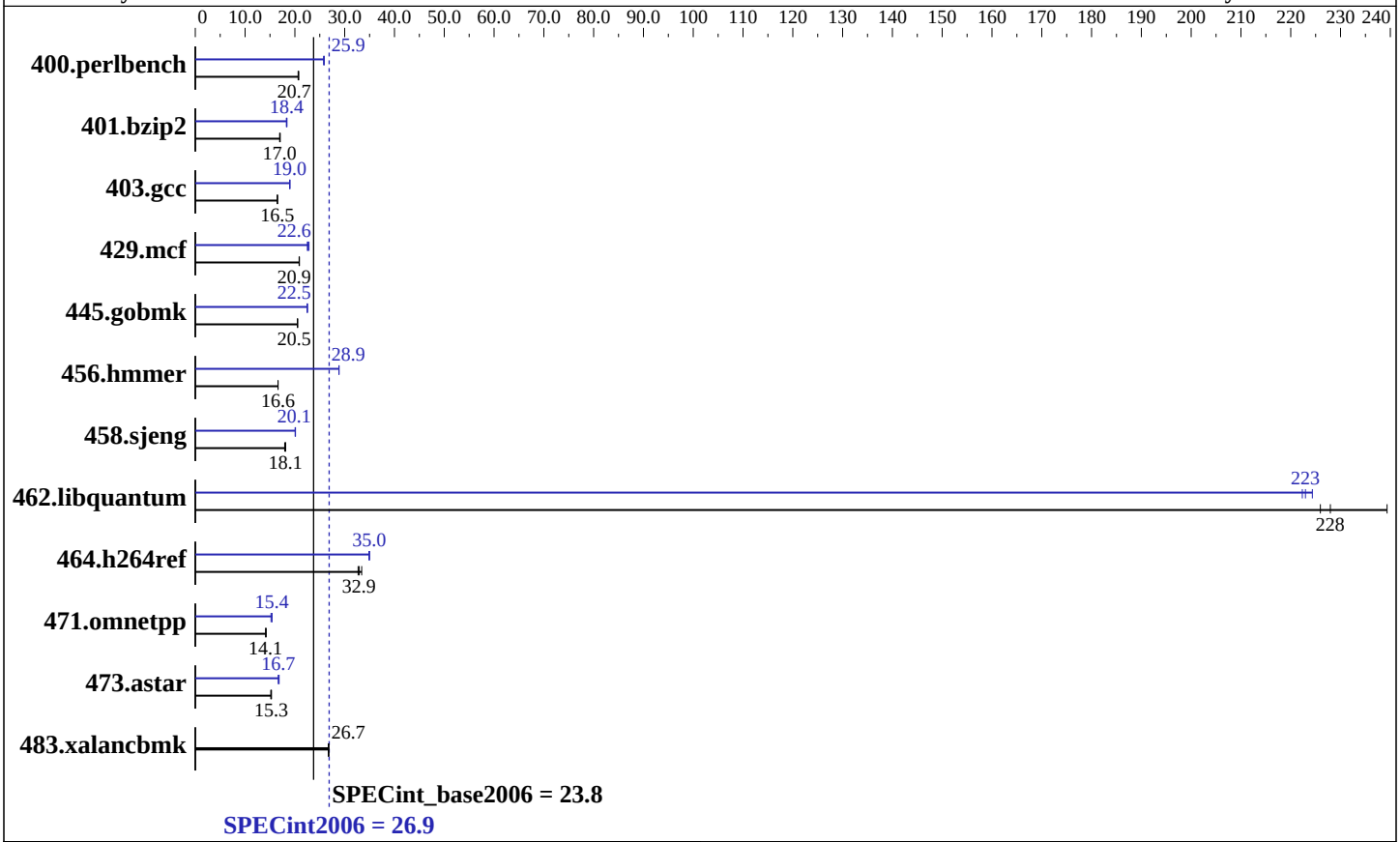
BladeSymphony BS1000 (Intel Xeon X5460)

SPECint®2006 = 26.9

SPECint_base2006 = 23.8

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Feb-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5460
CPU Characteristics:	1333MHz system bus
CPU MHz:	3160
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB(8 x 2 GB PC2-5300F CAS 5-5-5)
Disk Subsystem:	1 x 73 GB 10000 rpm SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 5.1 (Tikanga) Kernel 2.6.18-53.el5 on an x86_64
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

BladeSymphony BS1000 (Intel Xeon X5460)

SPECint2006 = 26.9

SPECint_base2006 = 23.8

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Feb-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	470	20.8	**473**	**20.7**	473	20.7	**378**	**25.9**	379	25.8	377	25.9
401.bzip2	**569**	**17.0**	569	17.0	566	17.1	525	18.4	528	18.3	**525**	**18.4**
403.gcc	**488**	**16.5**	488	16.5	489	16.5	423	19.0	**425**	**19.0**	425	18.9
429.mcf	437	20.8	436	20.9	**437**	**20.9**	**403**	**22.6**	399	22.8	405	22.5
445.gobmk	511	20.5	**511**	**20.5**	511	20.5	466	22.5	**466**	**22.5**	467	22.4
456.hmmer	**562**	**16.6**	562	16.6	561	16.6	324	28.8	323	28.9	**323**	**28.9**
458.sjeng	674	18.0	**668**	**18.1**	667	18.1	603	20.1	601	20.1	**603**	**20.1**
462.libquantum	86.6	239	91.7	226	**90.9**	**228**	**92.9**	**223**	92.4	224	93.2	222
464.h264ref	677	32.7	662	33.4	**673**	**32.9**	**633**	**35.0**	633	35.0	635	34.9
471.omnetpp	**442**	**14.1**	439	14.2	442	14.1	411	15.2	406	15.4	**406**	**15.4**
473.astar	**460**	**15.3**	459	15.3	463	15.2	421	16.7	417	16.8	**420**	**16.7**
483.xalancbmk	**258**	**26.7**	258	26.7	257	26.8	**258**	**26.7**	258	26.7	257	26.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -vec-guard-write -parallel -par-runtime-control`

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/bsc/smartheap/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/bsc/smartheap/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/bsc/smartheap/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.01.xml

Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

BladeSymphony BS1000 (Intel Xeon X5460)

SPECint2006 = 26.9

SPECint_base2006 = 23.8

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Feb-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 15:41:51 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 March 2008.