SPEC® CINT2006 Result
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Sun Microsystems
Sun Ultra 24

SPECint®_rate2006 = 65.3
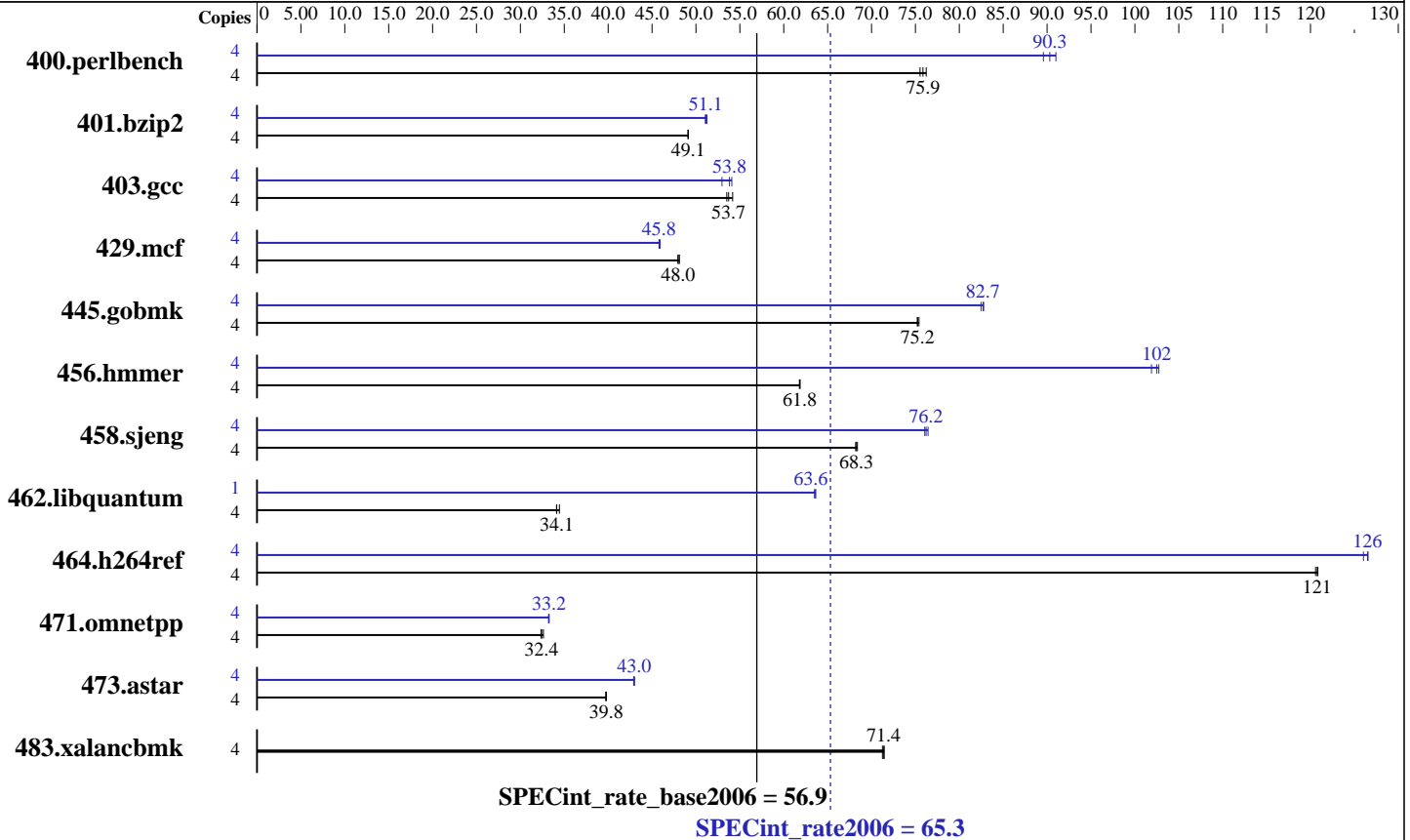
SPECint_rate_base2006 = 56.9

CPU2006 license: 6   **Test date:** Dec-2007

Test sponsor: Sun Microsystems   **Hardware Availability:** Oct-2007

Tested by: Sun Microsystems   **Software Availability:** Nov-2007

Benchmark	Copies	Peak	Base
400.perlbench	4	90.3	75.9
401.bzip2	4	51.1	49.1
403.gcc	4	53.8	53.7
429.mcf	4	45.8	48.0
445.gobmk	4	82.7	75.2
456.hmmer	4	102	61.8
458.sjeng	4	76.2	68.3
462.libquantum	1 / 4	63.6	34.1
464.h264ref	4	126	121
471.omnetpp	4	33.2	32.4
473.astar	4	43.0	39.8
483.xalancbmk	4		71.4

SPECint_rate_base2006 = 56.9

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Hardware

CPU Name:	Intel Core 2 Extreme QX6850
CPU Characteristics:	3.00 GHz 1333 MHz FSB
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 (order by number of chips)
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2GB DDR2 PC2-5300 2rank CAS 5-5-5 with ECC)
Disk Subsystem:	SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 SP1 for x86_64
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070913
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	513	76.2	517	75.5	**515**	**75.9**	4	436	89.6	429	91.0	**433**	**90.3**
401.bzip2	4	786	49.1	**786**	**49.1**	786	49.1	4	756	51.1	**755**	**51.1**	753	51.2
403.gcc	4	594	54.2	**599**	**53.7**	602	53.5	4	595	54.1	**598**	**53.8**	608	53.0
429.mcf	4	758	48.1	**760**	**48.0**	761	47.9	4	794	45.9	797	45.8	**796**	**45.8**
445.gobmk	4	**558**	**75.2**	556	75.4	558	75.2	4	509	82.5	**507**	**82.7**	507	82.8
456.hmmer	4	603	61.8	604	61.8	**603**	**61.8**	4	363	103	**364**	**102**	366	102
458.sjeng	4	**709**	**68.3**	708	68.4	710	68.2	4	**635**	**76.2**	636	76.1	633	76.4
462.libquantum	4	2406	34.4	**2427**	**34.1**	2429	34.1	1	**326**	**63.6**	326	63.6	326	63.5
464.h264ref	4	734	121	**733**	**121**	733	121	4	**700**	**126**	699	127	702	126
471.omnetpp	4	766	32.6	**771**	**32.4**	773	32.3	4	751	33.3	**752**	**33.2**	753	33.2
473.astar	4	**706**	**39.8**	706	39.8	707	39.7	4	653	43.0	655	42.9	**653**	**43.0**
483.xalancbmk	4	386	71.4	**387**	**71.4**	387	71.2	4	386	71.4	**387**	**71.4**	387	71.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited
OMP_NUM_THREADS set to 4
KMP_STACKSIZE set to 64M
KMP_AFFINITY set to physical,0
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch = Disable; Adjacent Sector Prefetch = Disable
```

General Notes

```
All benchmarks were compiled in 32-bit mode except 401.bzip2
and 456.hmmer for peak were compiled in 64-bit mode
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
    -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
    -L/data1/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/cce/10.1.008-64bit/bin/icc
            -L/opt/intel/cce/10.1.008-64bit/lib
            -I/opt/intel/cce/10.1.008-64bit/include

    456.hmmer: /opt/intel/cce/10.1.008-64bit/bin/icc
            -L/opt/intel/cce/10.1.008-64bit/lib
            -I/opt/intel/cce/10.1.008-64bit/include

C++ benchmarks:
    icpc

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

    483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias`
                 `-prefetch`

      401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

        403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

        429.mcf: `-fast -prefetch`

      445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo`
                 `-no-prec-div -ansi-alias`

      456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

      458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

    462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
                 `-opt-streaming-stores always -vec-guard-write`
                 `-opt-malloc-options=3 -parallel -par-runtime-control`

      464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2`
                 `-ansi-alias`

C++ benchmarks:

      471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
                 `-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
                 `-Wl,-z,muldefs -L/data1/cpu2006.1.0/lib -lsmartheap`

        473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
                 `-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
                 `-Wl,-z,muldefs -L/data1/cpu2006.1.0/lib -lsmartheap`

    483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.27.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.27.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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