



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

SPECint®\_rate2006 = 42.8

ProLiant DL380 G5  
(3.33 GHz, Intel Xeon X5260)

SPECint\_rate\_base2006 = 37.2

CPU2006 license: 3

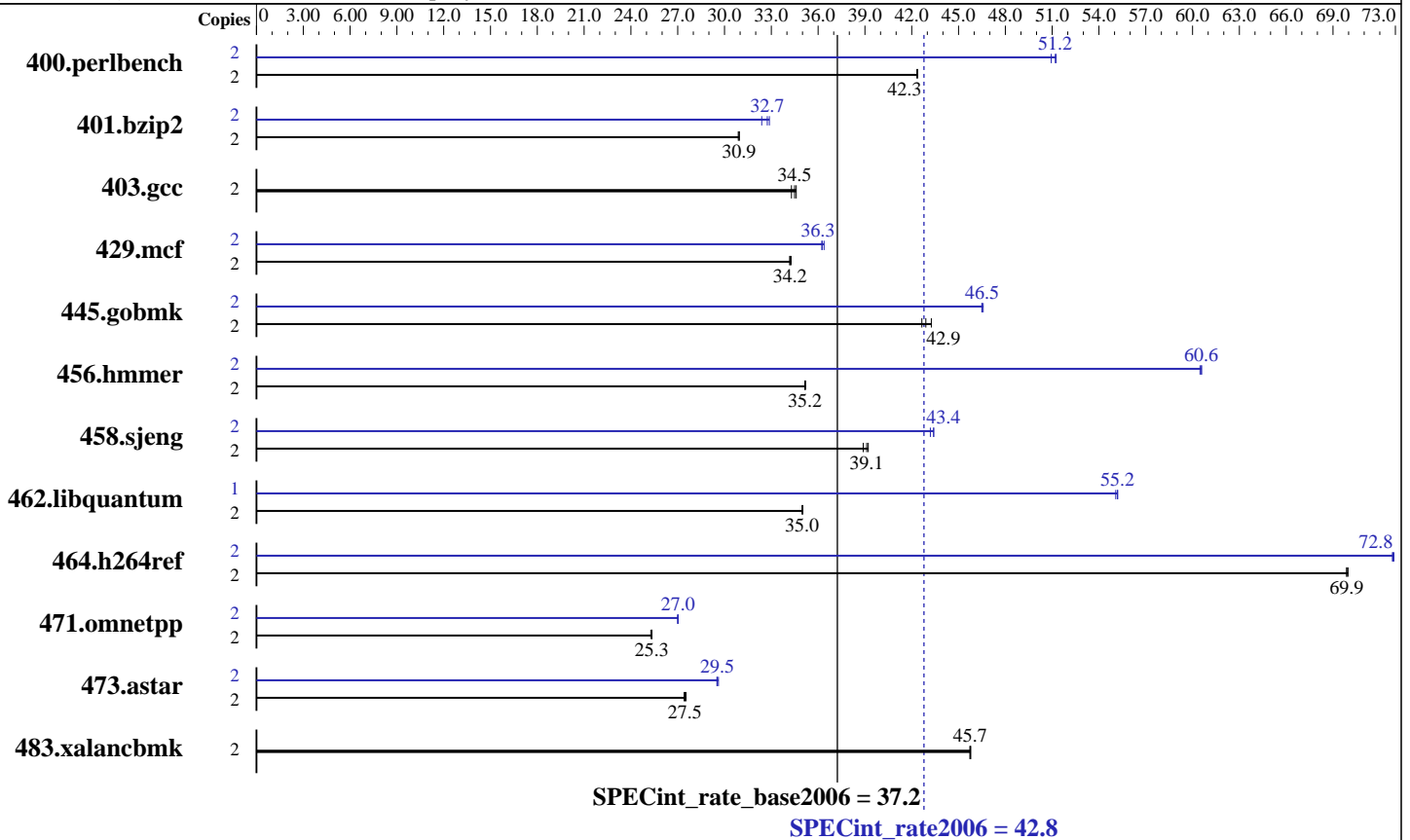
Test date: Feb-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics: 3.33 GHz, 6 MB L2 shared, 1333 MHz system bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F CL5)  
 Disk Subsystem: 1x72 GB 15 K SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	461	42.4	<b>461</b>	<b>42.3</b>	462	42.3	2	384	50.9	<b>382</b>	<b>51.2</b>	381	51.3
401.bzip2	2	625	30.9	624	30.9	<b>624</b>	<b>30.9</b>	2	596	32.4	587	32.9	<b>589</b>	<b>32.7</b>
403.gcc	2	465	34.6	<b>467</b>	<b>34.5</b>	470	34.3	2	465	34.6	<b>467</b>	<b>34.5</b>	470	34.3
429.mcf	2	<b>533</b>	<b>34.2</b>	534	34.2	532	34.3	2	503	36.2	501	36.4	<b>503</b>	<b>36.3</b>
445.gobmk	2	492	42.6	<b>489</b>	<b>42.9</b>	485	43.3	2	451	46.5	450	46.6	<b>451</b>	<b>46.5</b>
456.hammer	2	531	35.1	530	35.2	<b>531</b>	<b>35.2</b>	2	<b>308</b>	<b>60.6</b>	308	60.6	309	60.5
458.sjeng	2	617	39.2	<b>619</b>	<b>39.1</b>	622	38.9	2	560	43.2	<b>558</b>	<b>43.4</b>	558	43.4
462.libquantum	2	1183	35.0	<b>1185</b>	<b>35.0</b>	1185	35.0	1	<b>375</b>	<b>55.2</b>	376	55.1	375	55.2
464.h264ref	2	<b>633</b>	<b>69.9</b>	633	70.0	633	69.9	2	608	72.8	607	72.9	<b>608</b>	<b>72.8</b>
471.omnetpp	2	<b>493</b>	<b>25.3</b>	495	25.3	493	25.3	2	463	27.0	<b>463</b>	<b>27.0</b>	463	27.0
473.astar	2	510	27.5	<b>511</b>	<b>27.5</b>	512	27.4	2	<b>475</b>	<b>29.5</b>	476	29.5	474	29.6
483.xalancbmk	2	<b>302</b>	<b>45.7</b>	301	45.8	302	45.7	2	<b>302</b>	<b>45.7</b>	301	45.8	302	45.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
```

## Platform Notes

BIOS configuration:  
Power Regulator set to Static High Performance Mode

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/cpu2006/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/cpu2006/SmartHeap\_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/cpu2006/SmartHeap\_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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