SPEC® CINT2006 Result

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Fujitsu Siemens Computers

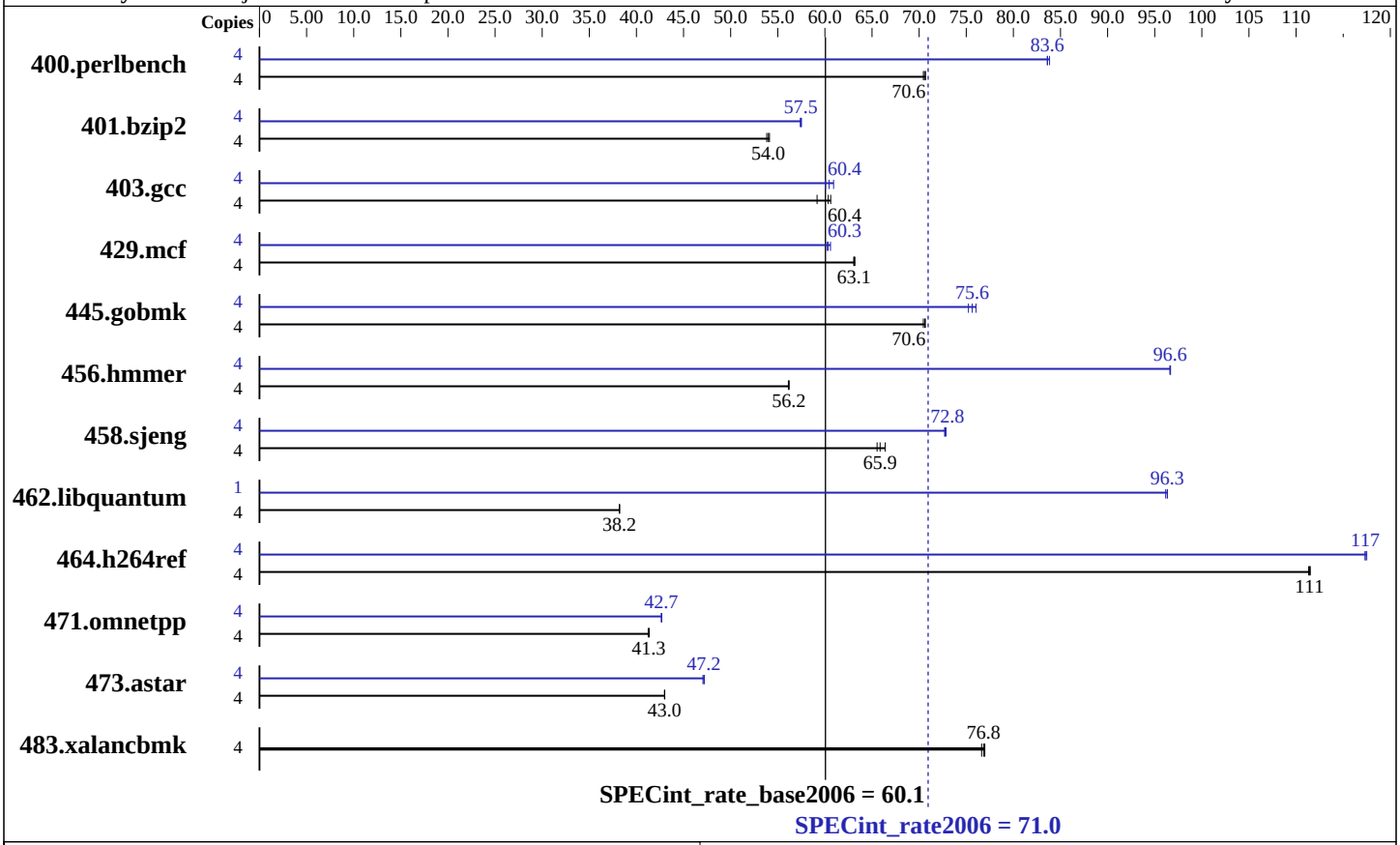
PRIMERGY TX150 S6, Intel Xeon X3350, 2.66 GHz

SPECint®_rate2006 = 71.0

SPECint_rate_base2006 = 60.1

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jan-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X3350
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1 GB PC2-6400E, 2 rank, CAS 6-6-6, with ECC)
Disk Subsystem:	Fujitsu MAY2036RC (SAS, 36GB, 10000rpm)
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel:	Yes
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	553	70.7	555	70.4	**554**	**70.6**	4	**467**	**83.6**	468	83.6	466	83.8
401.bzip2	4	**715**	**54.0**	713	54.1	717	53.8	4	671	57.5	673	57.4	**672**	**57.5**
403.gcc	4	544	59.2	**534**	**60.4**	531	60.6	4	**533**	**60.4**	529	60.9	536	60.1
429.mcf	4	577	63.2	578	63.1	**578**	**63.1**	4	606	60.2	**605**	**60.3**	602	60.6
445.gobmk	4	596	70.4	594	70.7	**594**	**70.6**	4	558	75.2	**555**	**75.6**	552	76.0
456.hmmer	4	665	56.1	**664**	**56.2**	664	56.2	4	**386**	**96.6**	386	96.7	386	96.6
458.sjeng	4	729	66.4	**735**	**65.9**	738	65.5	4	664	72.9	666	72.7	**665**	**72.8**
462.libquantum	4	**2170**	**38.2**	2170	38.2	2169	38.2	1	215	96.2	**215**	**96.3**	215	96.3
464.h264ref	4	794	112	795	111	**794**	**111**	4	753	117	**754**	**117**	754	117
471.omnetpp	4	604	41.4	**605**	**41.3**	606	41.2	4	586	42.7	586	42.6	**586**	**42.7**
473.astar	4	653	43.0	653	43.0	**653**	**43.0**	4	597	47.1	595	47.2	**596**	**47.2**
483.xalancbmk	4	360	76.6	359	77.0	**359**	**76.8**	4	360	76.6	359	77.0	**359**	**76.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'OMP_NUM_THREADS' set to number of cores (default)
```

General Notes

```
This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.xml`

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For other inquiries, please contact webmaster@spec.org.

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