SPEC® CINT2006 Result

Fujitsu Siemens Computers

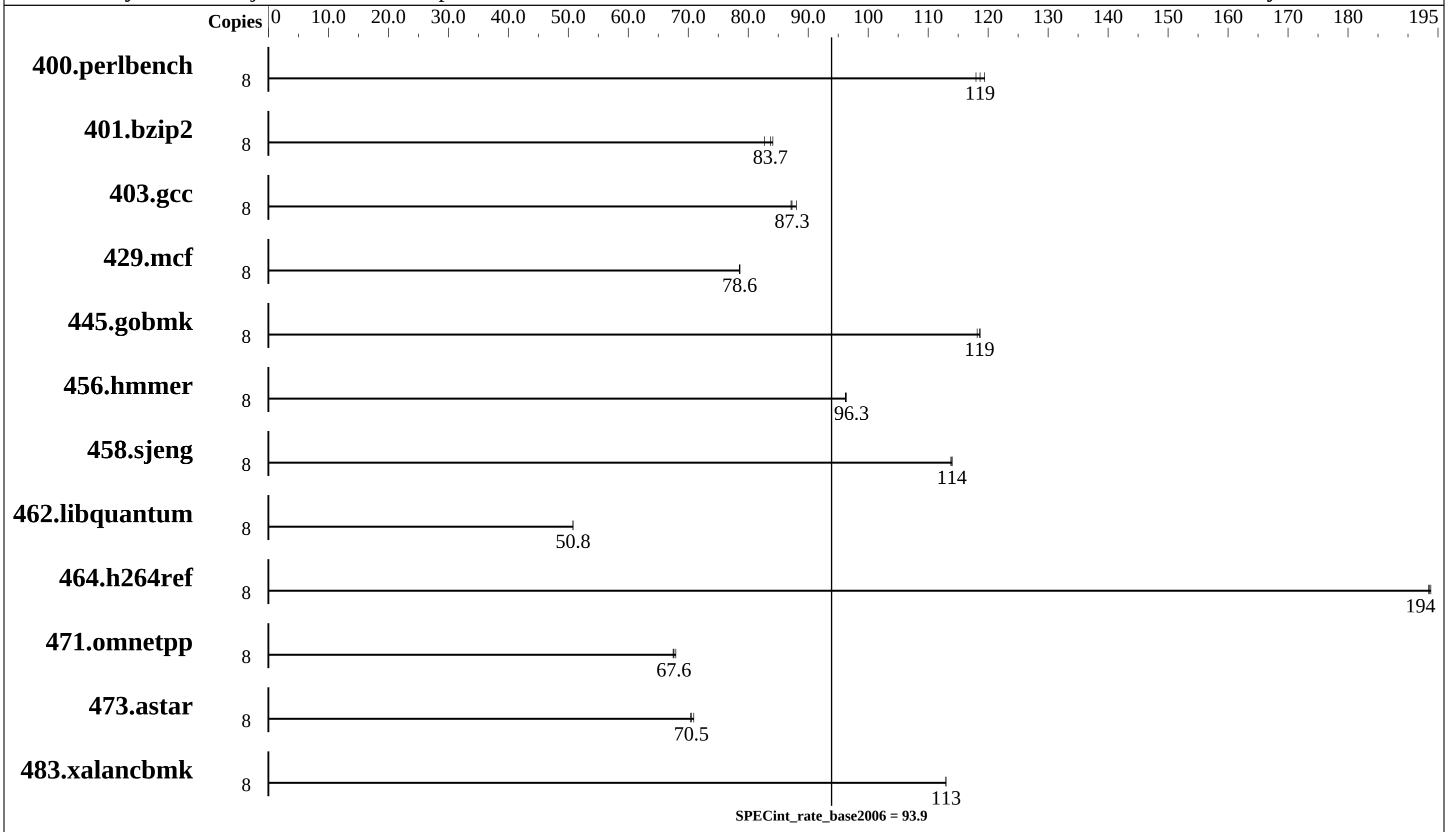
PRIMERGY RX200 S4, Intel Xeon L5410, 2.33 GHz

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 93.9

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jan-2008
Hardware Availability: Mar-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon L5410
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	8	663	118	**659**	**119**	655	119							
401.bzip2	8	933	82.7	**922**	**83.7**	918	84.1							
403.gcc	8	731	88.0	739	87.1	**738**	**87.3**							
429.mcf	8	**928**	**78.6**	928	78.6	929	78.5							
445.gobmk	8	710	118	**708**	**119**	707	119							
456.hmmer	8	775	96.4	776	96.2	**775**	**96.3**							
458.sjeng	8	851	114	**849**	**114**	849	114							
462.libquantum	8	3263	50.8	3264	50.8	**3264**	**50.8**							
464.h264ref	8	915	193	**914**	**194**	913	194							
471.omnetpp	8	736	67.9	741	67.5	**739**	**67.6**							
473.astar	8	**796**	**70.5**	798	70.4	792	70.9							
483.xalancbmk	8	489	113	489	113	**489**	**113**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:57:20 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 February 2008.