# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

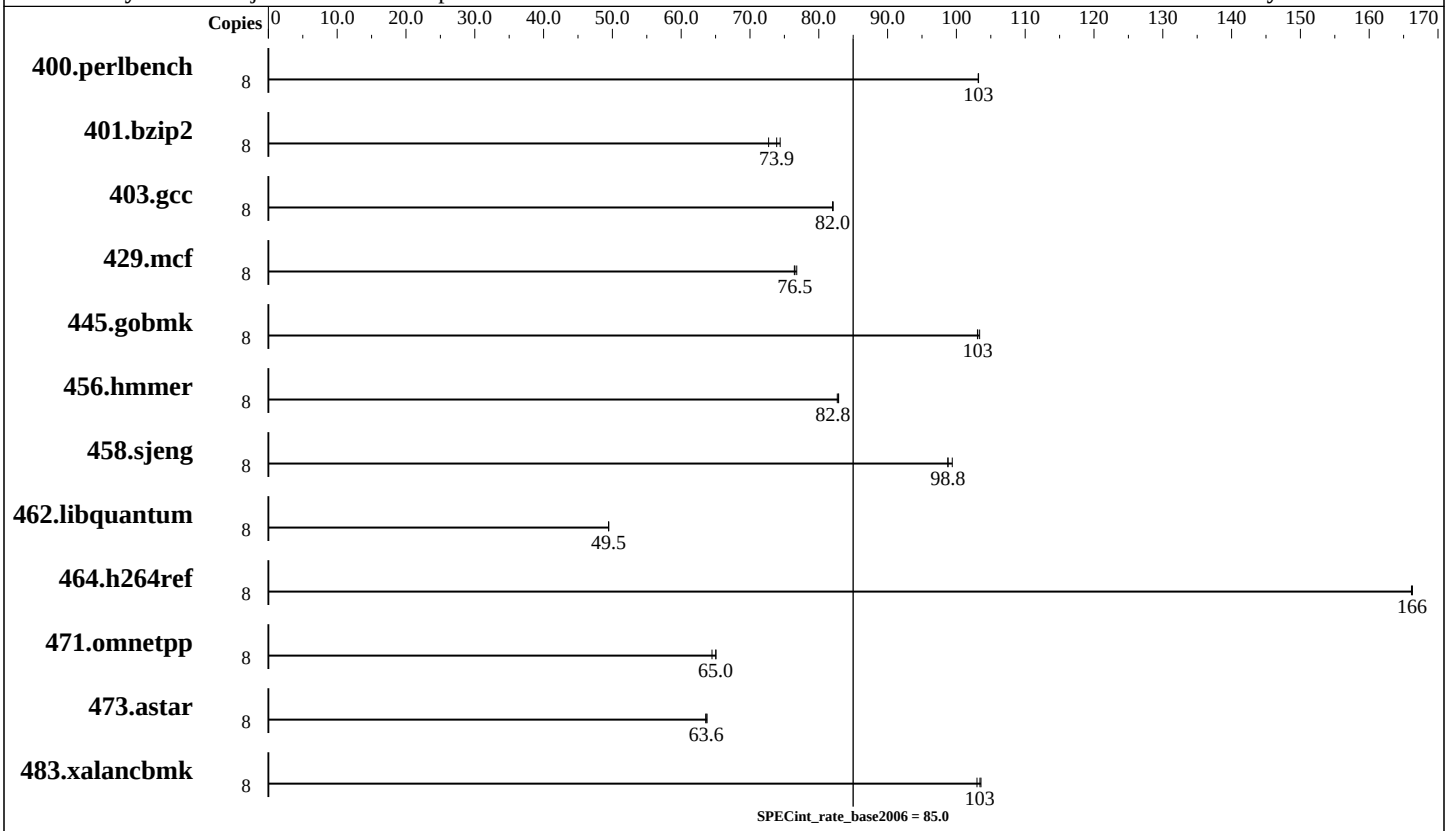
PRIMERGY TX300 S4, Intel Xeon E5405, 2.0 GHz

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 85.0

**CPU2006 license:** 22
**Test sponsor:** Fujitsu Siemens Computers
**Tested by:** Fujitsu Siemens Computers

**Test date:** Jan-2008
**Hardware Availability:** Dec-2007
**Software Availability:** Nov-2007




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5405 |
| CPU Characteristics: | 1333 MHz system bus |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 12 MB I+D on chip per chip, 6 MB shared / 2 cores |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC) |
| Disk Subsystem: | Seagate ST973451SS (SAS, 73GB, 15000rpm) |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp |
| Compiler: | Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725 |
| Auto Parallel: | No |
| File System: | ext2 |
| System State: | Multiuser, Runlevel 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | Not Applicable |
| Other Software: | MicroQuill SmartHeap Library, Version 8.1<br>binutils-2.17.tar.gz, Version 2.17 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 757 | 103 | **757** | **103** | 758 | 103 | | | | | | | |
| 401.bzip2 | 8 | 1038 | 74.4 | **1045** | **73.9** | 1062 | 72.7 | | | | | | | |
| 403.gcc | 8 | 785 | 82.0 | **785** | **82.0** | 785 | 82.1 | | | | | | | |
| 429.mcf | 8 | 950 | 76.8 | **953** | **76.5** | 954 | 76.4 | | | | | | | |
| 445.gobmk | 8 | 812 | 103 | 814 | 103 | **814** | **103** | | | | | | | |
| 456.hmmer | 8 | 903 | 82.7 | **901** | **82.8** | 901 | 82.9 | | | | | | | |
| 458.sjeng | 8 | 974 | 99.4 | 980 | 98.8 | **980** | **98.8** | | | | | | | |
| 462.libquantum | 8 | 3352 | 49.5 | 3351 | 49.5 | **3352** | **49.5** | | | | | | | |
| 464.h264ref | 8 | **1065** | **166** | 1066 | 166 | 1065 | 166 | | | | | | | |
| 471.omnetpp | 8 | **769** | **65.0** | 769 | 65.0 | 775 | 64.5 | | | | | | | |
| 473.astar | 8 | 881 | 63.8 | 884 | 63.6 | **882** | **63.6** | | | | | | | |
| 483.xalancbmk | 8 | **534** | **103** | 533 | 104 | 536 | 103 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

## Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.xml`


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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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