SPEC® CINT2006 Result

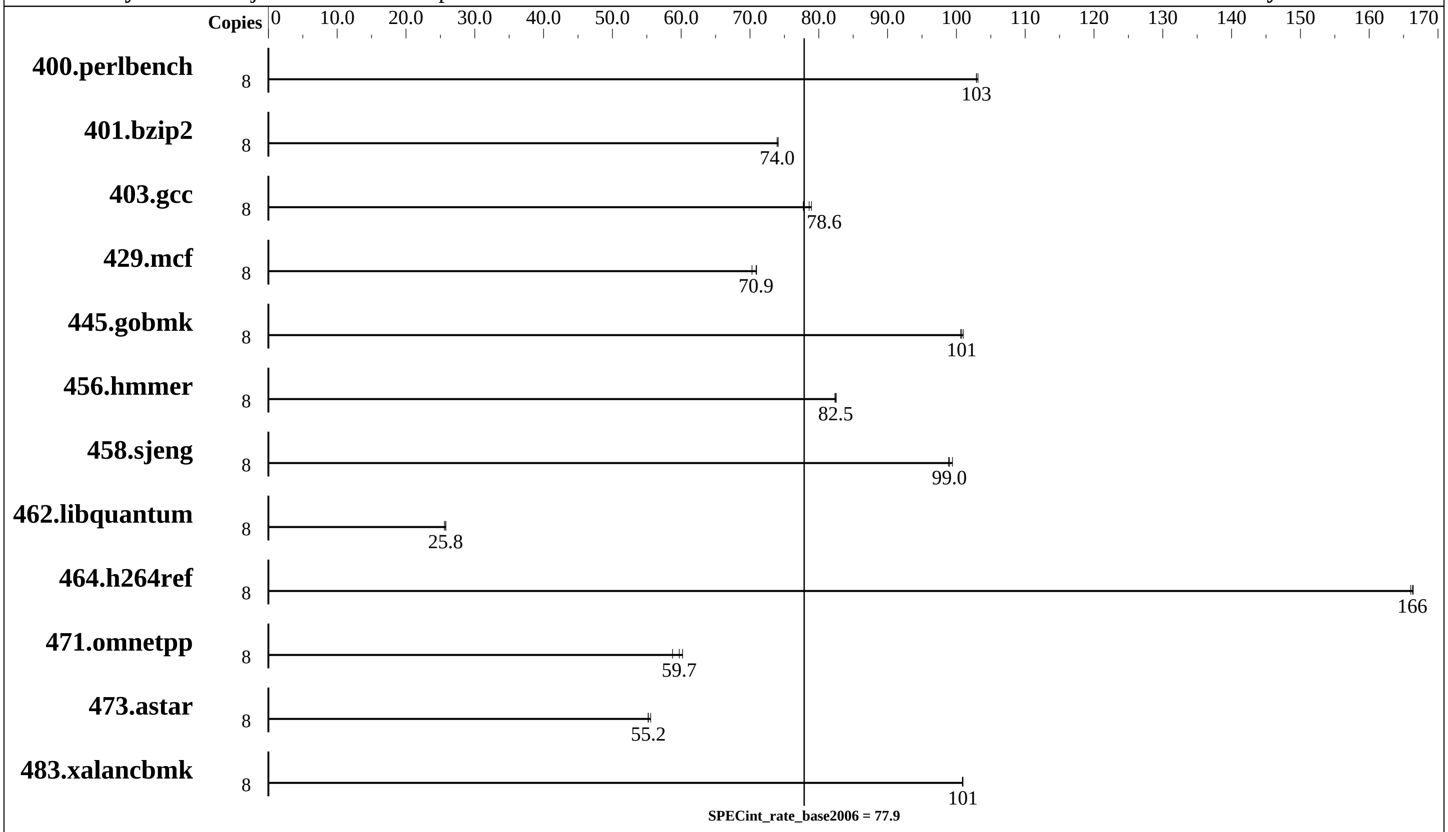
Fujitsu Siemens Computers

PRIMERGY TX200 S4, Intel Xeon E5405, 2.0 GHz

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 77.9

CPU2006 license: 22		**Test date:**	Oct-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Dec-2007
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Nov-2007



Hardware

CPU Name:	Intel Xeon E5405
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Fujitsu MAX3036RC (SAS, 36GB, 10000rpm)
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	758	103	**759**	**103**	760	103							
401.bzip2	8	**1044**	**74.0**	1044	74.0	1042	74.1							
403.gcc	8	828	77.7	816	78.9	**819**	**78.6**							
429.mcf	8	1038	70.3	**1029**	**70.9**	1028	71.0							
445.gobmk	8	831	101	**833**	**101**	834	101							
456.hmmer	8	907	82.3	904	82.5	**905**	**82.5**							
458.sjeng	8	979	98.9	973	99.4	**978**	**99.0**							
462.libquantum	8	6481	25.6	**6435**	**25.8**	6425	25.8							
464.h264ref	8	1066	166	1064	166	**1065**	**166**							
471.omnetpp	8	851	58.8	**837**	**59.7**	830	60.2							
473.astar	8	1018	55.2	1011	55.6	**1017**	**55.2**							
483.xalancbmk	8	547	101	547	101	**547**	**101**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

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Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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