SPEC® CINT2006 Result

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Dell Inc.

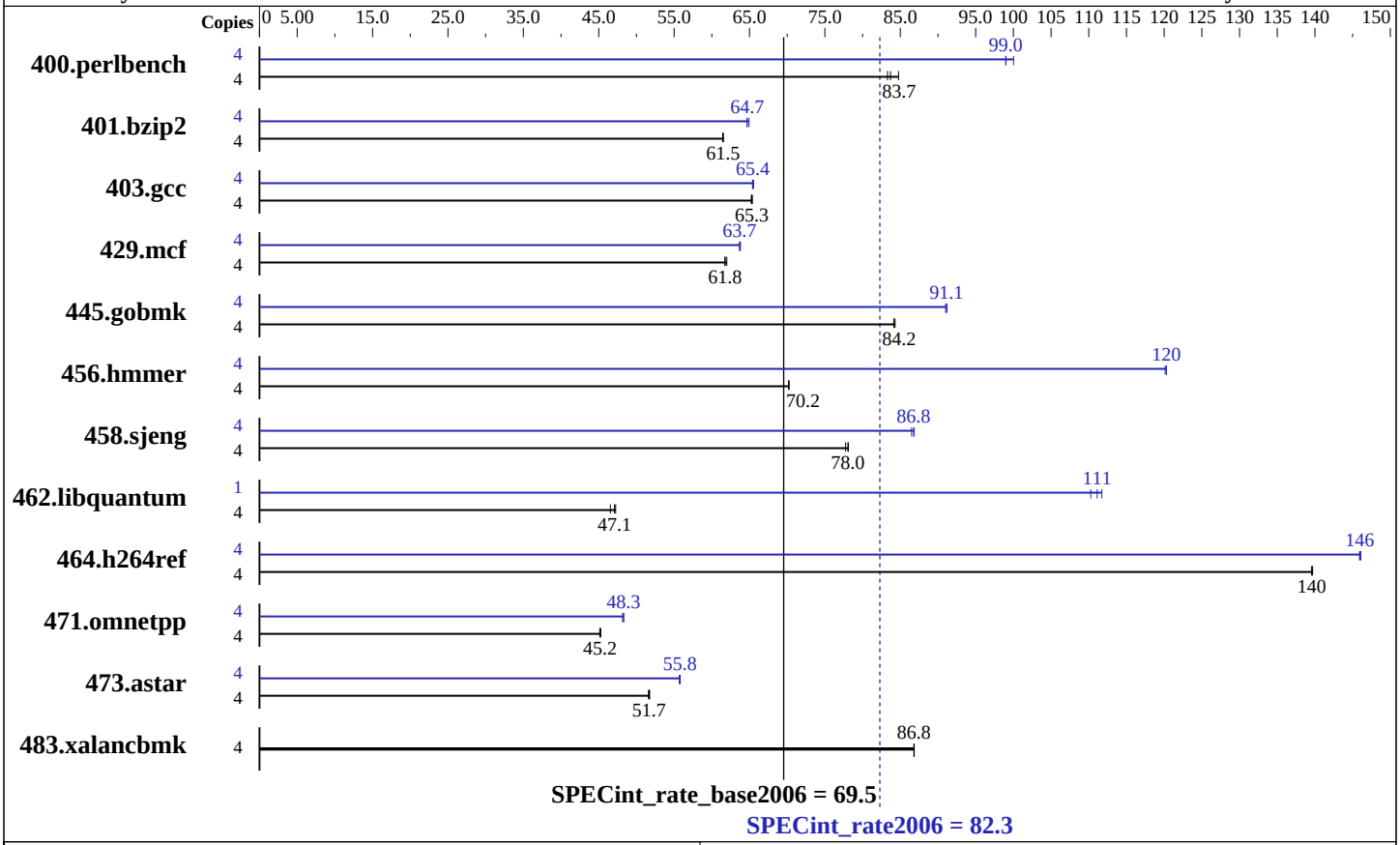
PowerEdge 1950 III (Intel Xeon X5260, 3.33 GHz)

SPECint®_rate2006 = 82.3

SPECint_rate_base2006 = 69.5

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jan-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2-667 ECC CL5 FB-DIMM)
Disk Subsystem:	1 x 73 GB SAS 15k RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**467**	**83.7**	461	84.8	469	83.3	4	**395**	**99.0**	391	100	395	99.0
401.bzip2	4	628	61.4	627	61.5	**628**	**61.5**	4	**597**	**64.7**	594	64.9	597	64.7
403.gcc	4	493	65.4	**493**	**65.3**	494	65.2	4	**492**	**65.4**	491	65.5	492	65.4
429.mcf	4	**591**	**61.8**	591	61.7	589	62.0	4	572	63.8	**573**	**63.7**	573	63.7
445.gobmk	4	498	84.3	499	84.1	**499**	**84.2**	4	461	91.0	460	91.2	**460**	**91.1**
456.hmmer	4	531	70.2	532	70.2	**532**	**70.2**	4	310	120	**310**	**120**	311	120
458.sjeng	4	620	78.1	623	77.7	**620**	**78.0**	4	**558**	**86.8**	557	86.8	560	86.5
462.libquantum	4	1781	46.5	1754	47.2	**1760**	**47.1**	1	**186**	**111**	188	110	185	112
464.h264ref	4	634	140	634	140	**634**	**140**	4	**606**	**146**	607	146	606	146
471.omnetpp	4	554	45.1	**553**	**45.2**	552	45.3	4	519	48.2	517	48.3	**518**	**48.3**
473.astar	4	**543**	**51.7**	544	51.6	543	51.8	4	**503**	**55.8**	503	55.8	504	55.7
483.xalancbmk	4	318	86.8	318	86.8	**318**	**86.8**	4	318	86.8	318	86.8	**318**	**86.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.1.008/bin/icc`
`-L/opt/intel/cce/10.1.008/lib`
`-I/opt/intel/cce/10.1.008/include`

456.hmmer: `/opt/intel/cce/10.1.008/bin/icc`
`-L/opt/intel/cce/10.1.008/lib`
`-I/opt/intel/cce/10.1.008/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.12.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.12.xml`

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