



SPEC[®] CFP2006 Result

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IBM Corporation

SPECfp[®]_rate2006 = Not Run

IBM BladeCenter HS21 (Intel Xeon E5450)

SPECfp_rate_base2006 = 67.8

CPU2006 license: 11

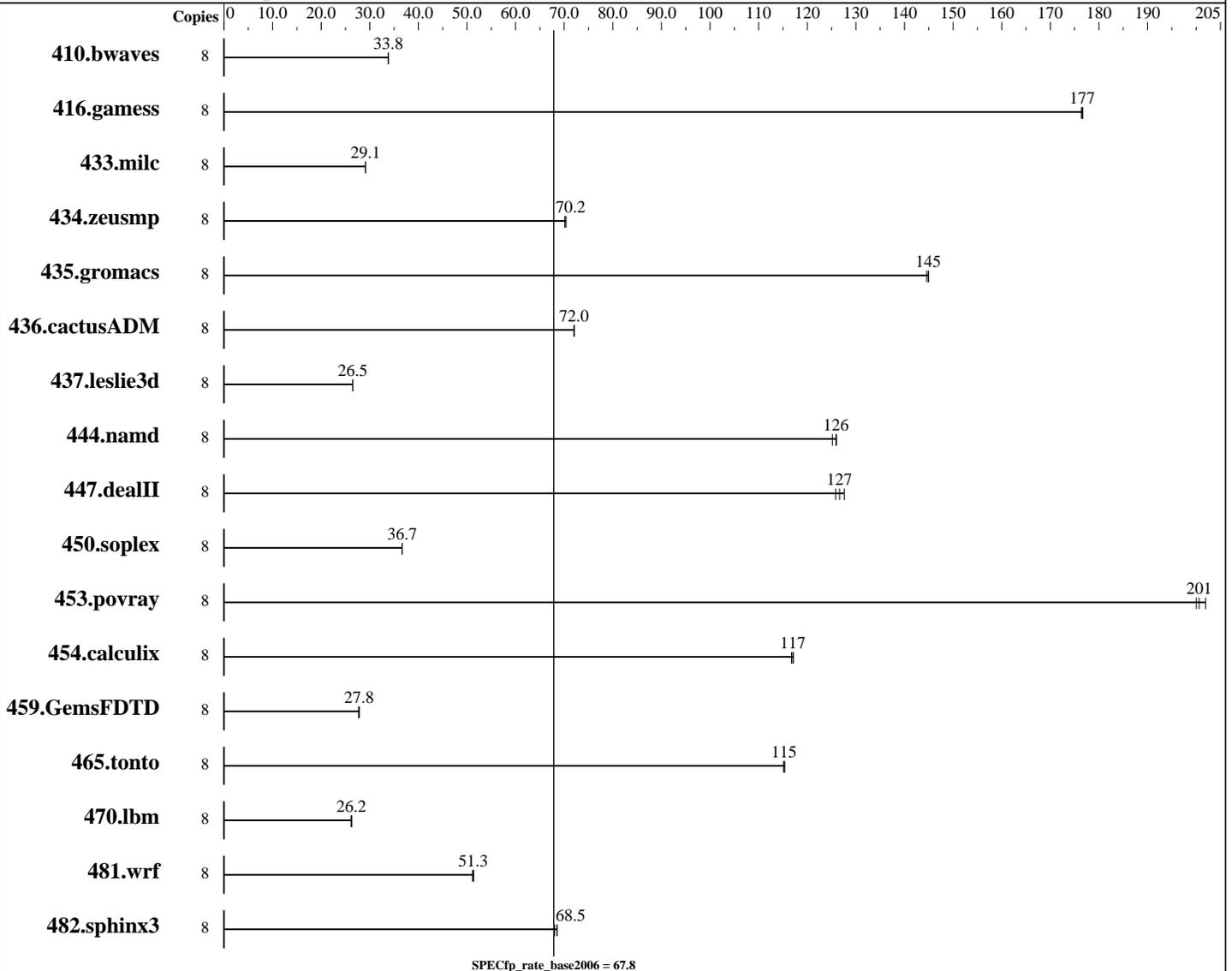
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Dec-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5450
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 36 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: Binutils 2.17.50.0.15

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|--------------------|--------------------|--------------------|--------------------|--------------------|--------------------|--------|---------|-------|---------|-------|---------|-------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 410.bwaves | 8 | 3216 | 33.8 | <u>3214</u> | <u>33.8</u> | 3214 | 33.8 | | | | | | | | | |
| 416.gamess | 8 | 888 | 176 | <u>887</u> | <u>177</u> | 886 | 177 | | | | | | | | | |
| 433.milc | 8 | 2525 | 29.1 | 2520 | 29.1 | <u>2521</u> | <u>29.1</u> | | | | | | | | | |
| 434.zeusmp | 8 | 1034 | 70.4 | <u>1038</u> | <u>70.2</u> | 1038 | 70.1 | | | | | | | | | |
| 435.gromacs | 8 | 394 | 145 | <u>394</u> | <u>145</u> | 395 | 145 | | | | | | | | | |
| 436.cactusADM | 8 | <u>1327</u> | <u>72.0</u> | 1328 | 72.0 | 1326 | 72.1 | | | | | | | | | |
| 437.leslie3d | 8 | 2837 | 26.5 | 2841 | 26.5 | <u>2837</u> | <u>26.5</u> | | | | | | | | | |
| 444.namd | 8 | 513 | 125 | 509 | 126 | <u>510</u> | <u>126</u> | | | | | | | | | |
| 447.dealII | 8 | 717 | 128 | 727 | 126 | <u>723</u> | <u>127</u> | | | | | | | | | |
| 450.soplex | 8 | <u>1820</u> | <u>36.7</u> | 1822 | 36.6 | 1819 | 36.7 | | | | | | | | | |
| 453.povray | 8 | <u>212</u> | <u>201</u> | 213 | 200 | 211 | 202 | | | | | | | | | |
| 454.calculix | 8 | 563 | 117 | 565 | 117 | <u>564</u> | <u>117</u> | | | | | | | | | |
| 459.GemsFDTD | 8 | 3053 | 27.8 | 3067 | 27.7 | <u>3053</u> | <u>27.8</u> | | | | | | | | | |
| 465.tonto | 8 | 682 | 115 | <u>683</u> | <u>115</u> | 684 | 115 | | | | | | | | | |
| 470.lbm | 8 | <u>4188</u> | <u>26.2</u> | 4201 | 26.2 | 4185 | 26.3 | | | | | | | | | |
| 481.wrf | 8 | 1748 | 51.1 | <u>1743</u> | <u>51.3</u> | 1740 | 51.4 | | | | | | | | | |
| 482.sphinx3 | 8 | 2292 | 68.0 | <u>2277</u> | <u>68.5</u> | 2274 | 68.6 | | | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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