SPEC® CFP2006 Result

Intel Corporation

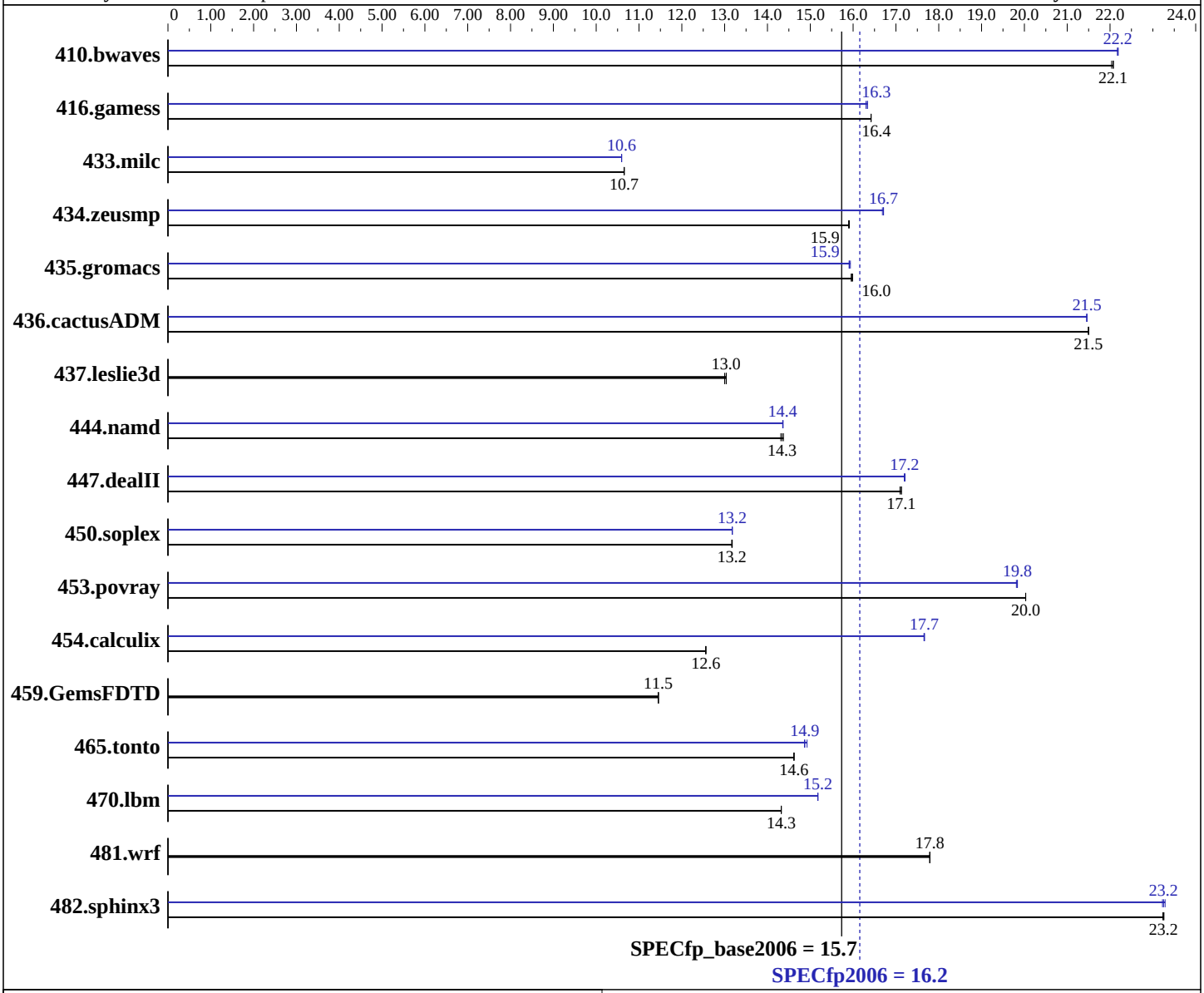
Lenovo ThinkPad T61 (Intel Core 2 Duo T7800)

SPECfp®2006 = 16.2

SPECfp_base2006 = 15.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo T7800
CPU Characteristics:	
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Ultimate (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Intel Fortran Compiler for IA32 version 10.1 Build 20070913 Package ID: w_fc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Hynix DDR2-667 CL5)
Disk Subsystem: Hitachi 100 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None
SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	617	22.0	**616**	**22.1**	616	22.1	612	22.2	**613**	**22.2**	613	22.2
416.gamess	**1193**	**16.4**	1192	16.4	1193	16.4	**1199**	**16.3**	1199	16.3	1201	16.3
433.milc	861	10.7	**862**	**10.7**	862	10.7	867	10.6	866	10.6	**867**	**10.6**
434.zeusmp	572	15.9	572	15.9	**572**	**15.9**	**545**	**16.7**	545	16.7	545	16.7
435.gromacs	447	16.0	**447**	**16.0**	448	16.0	**449**	**15.9**	449	15.9	448	15.9
436.cactusADM	556	21.5	556	21.5	**556**	**21.5**	557	21.5	557	21.5	**557**	**21.5**
437.leslie3d	723	13.0	721	13.0	**721**	**13.0**	723	13.0	721	13.0	**721**	**13.0**
444.namd	**559**	**14.3**	558	14.4	560	14.3	558	14.4	559	14.4	**559**	**14.4**
447.dealII	**668**	**17.1**	668	17.1	669	17.1	665	17.2	665	17.2	**665**	**17.2**
450.soplex	**633**	**13.2**	633	13.2	633	13.2	633	13.2	**633**	**13.2**	633	13.2
453.povray	266	20.0	266	20.0	**266**	**20.0**	268	19.8	269	19.8	**268**	**19.8**
454.calculix	657	12.6	657	12.6	**657**	**12.6**	**467**	**17.7**	467	17.7	467	17.7
459.GemsFDTD	926	11.5	**926**	**11.5**	927	11.4	926	11.5	**926**	**11.5**	927	11.4
465.tonto	673	14.6	**673**	**14.6**	673	14.6	662	14.9	**662**	**14.9**	659	14.9
470.lbm	959	14.3	959	14.3	**959**	**14.3**	905	15.2	905	15.2	**905**	**15.2**
481.wrf	628	17.8	628	17.8	**628**	**17.8**	628	17.8	628	17.8	**628**	**17.8**
482.sphinx3	839	23.2	838	23.3	**838**	**23.2**	839	23.2	**838**	**23.2**	837	23.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The system bus runs at 800 MHz
Binaries were built on Windows Vista (32-bit)
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast -Qparallel /F1000000000 libguide40.lib

C++ benchmarks:
-fast -Qparallel -Qcxx_features /F1000000000 shlw32m.lib
libguide40.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast -Qparallel /F1000000000 libguide40.lib

Benchmarks using both Fortran and C:
-fast -Qparallel /F1000000000 libguide40.lib

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -fast -Qunroll2 -Oa /F1000000000 libguide40.lib

470.lbm: -fast -Qunroll2 -Qscalar-rep- -Qprefetch /F1000000000 libguide40.lib

482.sphinx3: -fast -Qunroll2 /F1000000000 libguide40.lib

C++ benchmarks:

444.namd: -fast -Oa -Qcxx_features /F1000000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

447.dealII: -fast -Qunroll2 -Qprefetch -Qcxx_features /F1000000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

450.soplex: -fast -Qparallel -Qcxx_features /F1000000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

453.povray: -fast -Qunroll4 -Qcxx_features /F1000000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -fast -Qparallel -Qprefetch /F1000000000 libguide40.lib

416.gamess: -fast -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000 libguide40.lib

434.zeusmp: -QxT -O2 -Qprec-div- -Qunroll0 -Qscalar-rep- /F1000000000 libguide40.lib

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Peak Optimization Flags (Continued)

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-fast -Qunroll4 -Qauto /F1000000000 libguide40.lib`

Benchmarks using both Fortran and C:

435.gromacs: `-fast -Oa -Qprefetch /F1000000000 libguide40.lib`

436.cactusADM: `-fast -Qunroll2 -Qparallel -Qprefetch /F1000000000 libguide40.lib`

454.calculix: `-fast -Qunroll-aggressive /F1000000000 libguide40.lib`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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