SPEC® CFP2006 Result

Hewlett-Packard Company

ProLiant ML350 G5
(3.16 GHz, Intel Xeon processor X5460)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.
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Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'./usr/bin/taskset' used to bind processes to CPUs
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Non-compliant
SPEC CFP2006 Result

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Platform Notes
BIOS configuration:
- Power Regulator set to Static High Performance Mode
- Adjacent Sector Prefetch Disabled
- Hardware Prefetcher Disabled

Base Compiler Invocation

C benchmarks:
- icc

C++ benchmarks:
- icpc

Fortran benchmarks:
- ifort

Benchmarks using both Fortran and C:
- icc ifort

Base Portability Flags
410.bwaves: -DSPEC_CPU_LP64
441.ice: -DSPEC_CPU_LP64
451.milc: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lbm: -DSPEC_CPU_LP64
434.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64 -nofor_main
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

Non-Compliant
Hewlett-Packard Company
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<table>
<thead>
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### Base Portability Flags (Continued)

<table>
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<th>Benchmark</th>
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<td>465.tonto</td>
<td>-DSPEC_CPU_LP64</td>
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<tr>
<td>470.lbm</td>
<td>-DSPEC_CPU_LP64</td>
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<tr>
<td>481.wrf</td>
<td>-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX</td>
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<tr>
<td>482.sphinx3</td>
<td>-DSPEC_CPU_LP64</td>
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### Base Optimization Flags

<table>
<thead>
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<th>Category</th>
<th>Flags</th>
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<tr>
<td>C benchmarks</td>
<td>-fast</td>
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<tr>
<td>C++ benchmarks</td>
<td>-fast</td>
</tr>
<tr>
<td>Fortran benchmarks</td>
<td>-fast</td>
</tr>
<tr>
<td>Benchmarks using both Fortran and C</td>
<td>-fast</td>
</tr>
</tbody>
</table>

### Peak Compiler Invocation

```
C benchmarks (except as noted below):
  /opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
  -I/opt/intel/cc/10.1.008/include

  433.milc: icc

C++ benchmarks (except as noted below):
  icpc

  450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
    -I/opt/intel/cc/10.1.008/include
```

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Peak Compiler Invocation (Continued)

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: /opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib
-I/opt/intel/fc/10.1.008/include

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
454.flecom: -DSPEC_CPU_LP64 -nofor_main
459.GemsF ColliD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

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Peak Optimization Flags (Continued)

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -scalar-rep -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto
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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) fast -prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) fast -unroll2 -prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggresive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at  

You can also download the XML flags source by saving the following link:  

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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