SPEC® CINT2006 Result

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Hewlett-Packard Company

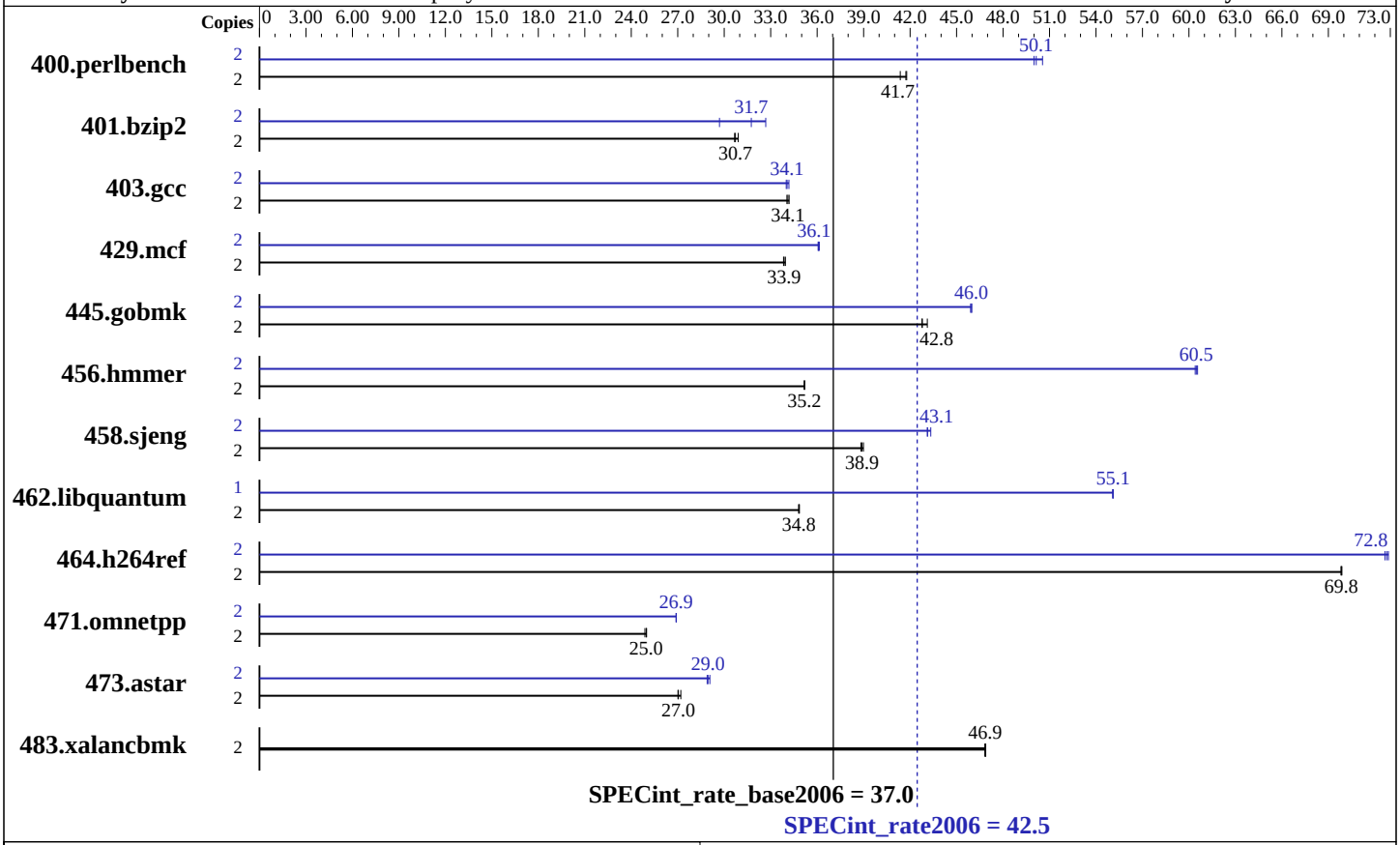
ProLiant ML370 G5
(3.33 GHz, Intel Xeon processor X5260)

SPECint®_rate2006 = 42.5

SPECint_rate_base2006 = 37.0

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jan-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	3.33 GHz, 6 MB L2 shared, 1333 MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem:	1x72 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1 Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for applications running on IA-32 and Intel 64, Version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 binutils-2.17.50

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**468**	**41.7**	468	41.8	472	41.4	2	387	50.5	**390**	**50.1**	391	50.0
401.bzip2	2	625	30.9	**628**	**30.7**	629	30.7	2	650	29.7	591	32.7	**608**	**31.7**
403.gcc	2	473	34.0	**472**	**34.1**	471	34.2	2	**473**	**34.1**	473	34.0	471	34.2
429.mcf	2	**539**	**33.9**	539	33.8	537	34.0	2	**505**	**36.1**	506	36.1	505	36.1
445.gobmk	2	487	43.1	491	42.8	**490**	**42.8**	2	457	45.9	456	46.0	**456**	**46.0**
456.hmmer	2	530	35.2	531	35.2	**530**	**35.2**	2	**309**	**60.5**	308	60.6	309	60.4
458.sjeng	2	623	38.8	621	39.0	**622**	**38.9**	2	558	43.3	561	43.1	**561**	**43.1**
462.libquantum	2	1191	34.8	1189	34.9	**1190**	**34.8**	1	376	55.1	**376**	**55.1**	376	55.1
464.h264ref	2	**634**	**69.8**	634	69.8	633	69.9	2	607	72.9	609	72.7	**608**	**72.8**
471.omnetpp	2	502	24.9	500	25.0	**501**	**25.0**	2	465	26.9	465	26.9	**465**	**26.9**
473.astar	2	520	27.0	516	27.2	**519**	**27.0**	2	486	28.9	**485**	**29.0**	483	29.1
483.xalancbmk	2	**295**	**46.9**	295	46.8	294	46.9	2	**295**	**46.9**	295	46.8	294	46.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
```

Platform Notes

```
BIOS configuration:
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

Continued on next page

Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:06:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 January 2008.