SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL360 G5
(3.33 GHz, Intel Xeon processor X5260)

SPECint®_rate2006 =   42.7

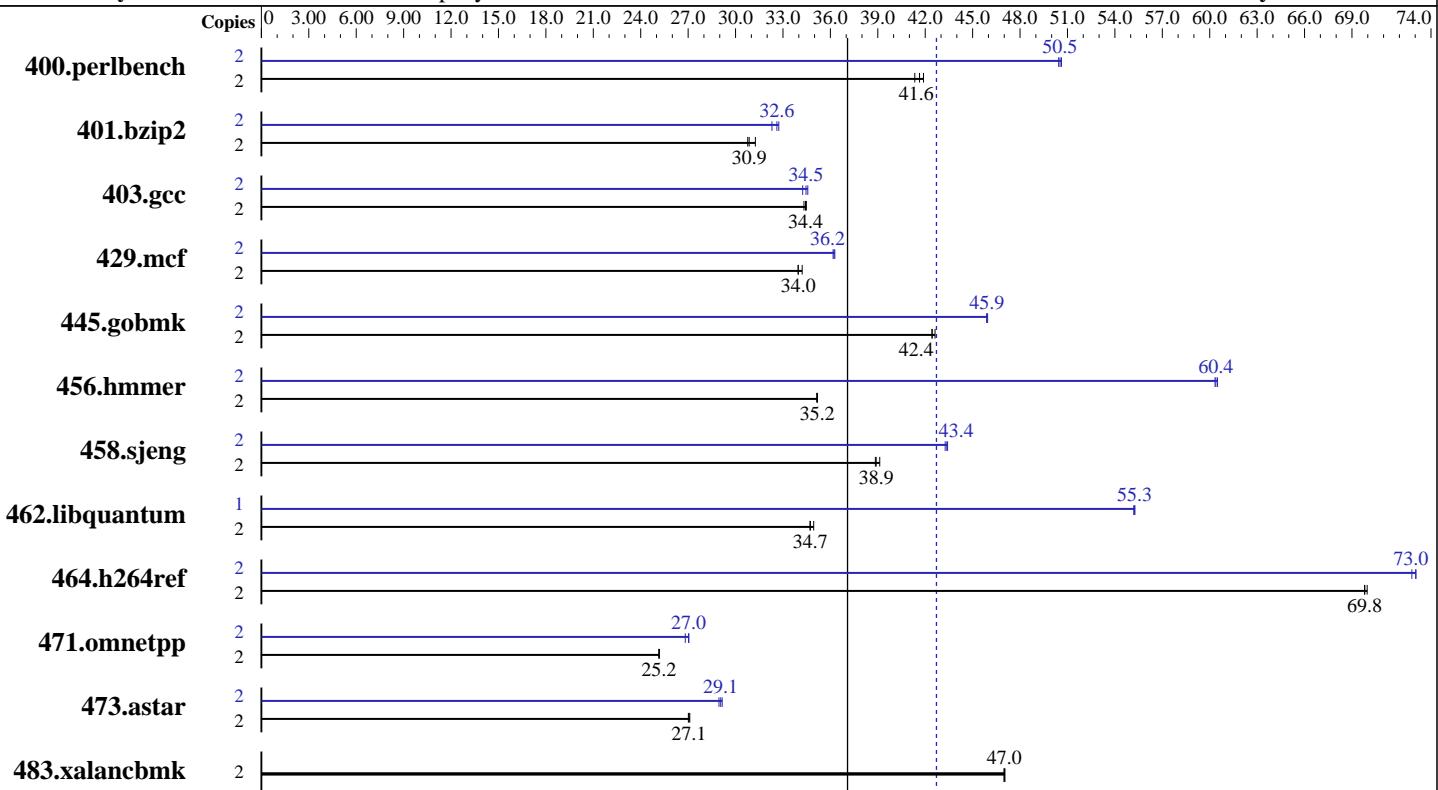SPECint_rate_base2006 = 37.1

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Dec-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007

Copies 0 3.00 6.00 9.00 12.0 15.0 18.0 21.0 24.0 27.0 30.0 33.0 36.0 39.0 42.0 45.0 48.0 51.0 54.0 57.0 60.0 63.0 66.0 69.0   74.0

400.perlbench   2 .... 50.5
                2 .... 41.6
401.bzip2   2 .... 32.6
                2 .... 30.9
403.gcc   2 .... 34.5
                2 .... 34.4
429.mcf   2 .... 36.2
                2 .... 34.0
445.gobmk   2 .... 45.9
                2 .... 42.4
456.hmmer   2 .... 60.4
                2 .... 35.2
458.sjeng   2 .... 43.4
                2 .... 38.9
462.libquantum 1 .... 55.3
                2 .... 34.7
464.h264ref   2 .... 73.0
                2 .... 69.8
471.omnetpp   2 .... 27.0
                2 .... 25.2
473.astar   2 .... 29.1
                2 .... 27.1
483.xalancbmk   2 .... 47.0

SPECint_rate_base2006 = 37.1

SPECint_rate2006 = 42.7

Hardware

CPU Name: Intel Xeon X5260

CPU Characteristics: 3.33 GHz, 6 MB L2 shared, 1333 MHz system bus

CPU MHz: 3333

FPU: Integrated

CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 6 MB I+D on chip per chip

L3 Cache: None

Other Cache: None

Memory: 16 GB (8x2 GB PC2-5300F CL5)

Disk Subsystem: 1x72 GB 15 K SAS

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1
                     Kernel 2.6.16.46-0.12-smp

Compiler: Intel C++ Compiler for applications running on
                     IA-32 and Intel 64, Version 10.1
                     Build 20070913 Package ID: l_cc_p_10.1.008

Auto Parallel: Yes

File System: ext2

System State: Multi-user run level 3

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: MicroQuill SmartHeap Library 8.1
                     binutils-2.17.50

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL360 G5
(3.33 GHz, Intel Xeon processor X5260)

SPECint_rate2006 = 42.7

SPECint_rate_base2006 = 37.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**<u>469</u>**	**<u>41.6</u>**	473	41.3	466	41.9	2	387	50.4	386	50.6	**<u>387</u>**	**<u>50.5</u>**
401.bzip2	2	**<u>625</u>**	**<u>30.9</u>**	618	31.3	627	30.8	2	597	32.3	590	32.7	**<u>592</u>**	**<u>32.6</u>**
403.gcc	2	**<u>468</u>**	**<u>34.4</u>**	467	34.5	469	34.3	2	470	34.2	**<u>467</u>**	**<u>34.5</u>**	466	34.6
429.mcf	2	**<u>537</u>**	**<u>34.0</u>**	537	34.0	533	34.2	2	503	36.3	504	36.2	**<u>503</u>**	**<u>36.2</u>**
445.gobmk	2	492	42.6	494	42.4	**<u>494</u>**	**<u>42.4</u>**	2	**<u>457</u>**	**<u>45.9</u>**	457	45.9	457	45.9
456.hmmer	2	530	35.2	531	35.1	**<u>531</u>**	**<u>35.2</u>**	2	308	60.5	**<u>309</u>**	**<u>60.4</u>**	309	60.3
458.sjeng	2	619	39.1	**<u>622</u>**	**<u>38.9</u>**	623	38.8	2	**<u>558</u>**	**<u>43.4</u>**	559	43.3	557	43.4
462.libquantum	2	1186	34.9	**<u>1193</u>**	**<u>34.7</u>**	1194	34.7	1	**<u>375</u>**	**<u>55.3</u>**	375	55.2	375	55.3
464.h264ref	2	**<u>634</u>**	**<u>69.8</u>**	634	69.8	633	70.0	2	**<u>606</u>**	**<u>73.0</u>**	608	72.8	606	73.0
471.omnetpp	2	497	25.2	496	25.2	**<u>497</u>**	**<u>25.2</u>**	2	462	27.0	466	26.8	**<u>463</u>**	**<u>27.0</u>**
473.astar	2	518	27.1	520	27.0	**<u>519</u>**	**<u>27.1</u>**	2	485	29.0	482	29.2	**<u>483</u>**	**<u>29.1</u>**
483.xalancbmk	2	293	47.0	294	47.0	**<u>294</u>**	**<u>47.0</u>**	2	293	47.0	294	47.0	**<u>294</u>**	**<u>47.0</u>**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
```

Platform Notes

```
BIOS configuration:
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

Continued on next page

Hewlett-Packard Company

ProLiant DL360 G5
(3.33 GHz, Intel Xeon processor X5260)

SPECint_rate2006 =   42.7

SPECint_rate_base2006 = 37.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Base Portability Flags (Continued)

462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
       -L/opt/intel/cce/10.1.008/lib
       -I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
       -L/opt/intel/cce/10.1.008/lib
       -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Hewlett-Packard Company

ProLiant DL360 G5
(3.33 GHz, Intel Xeon processor X5260)

SPECint_rate2006 =　42.7

SPECint_rate_base2006 =　37.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch`

    401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

    403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

    429.mcf: `-fast -prefetch`

    445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias`

    456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

    458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

    462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

    464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias`

C++ benchmarks:

    471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap`

    473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap`

    483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

    403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL360 G5
(3.33 GHz, Intel Xeon processor X5260)

SPECint_rate2006 = 42.7

SPECint_rate_base2006 = 37.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:11:58 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 January 2008.