SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

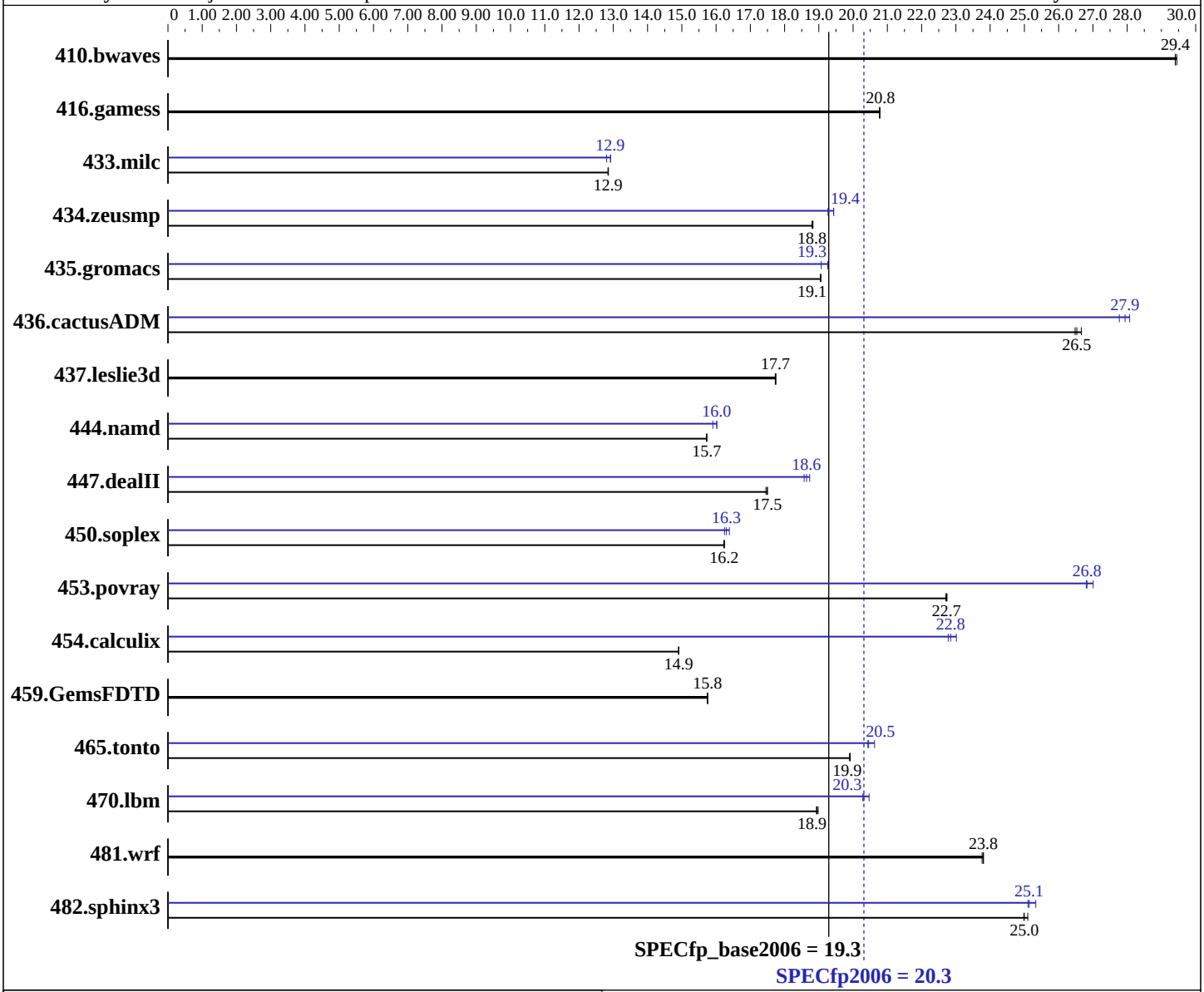
CELSIUS M460, Intel Core 2 Duo E6850 processor

SPECfp®2006 = 20.3

SPECfp_base2006 = 19.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Dec-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo E6850
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

Continued on next page

Software

Operating System:	Microsoft Windows Vista Ultimate (x64)
Compiler:	Intel C++ and Fortran Compilers for Intel64, Version 10.1, Build 20070913 Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

CELSIUS M460, Intel Core 2 Duo E6850 processor

SPECfp2006 = 20.3

SPECfp_base2006 = 19.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Dec-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007

L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem: 1 x 400 GB SATA II 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: MicroQuill SmartHeap Library, Version 8.0 (64 bit)

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	461	29.4	**462**	**29.4**	462	29.4	461	29.4	**462**	**29.4**	462	29.4
416.gamess	942	20.8	**942**	**20.8**	943	20.8	942	20.8	**942**	**20.8**	943	20.8
433.milc	714	12.9	**714**	**12.9**	715	12.8	**711**	**12.9**	710	12.9	717	12.8
434.zeusmp	483	18.8	**484**	**18.8**	484	18.8	**468**	**19.4**	468	19.4	472	19.3
435.gromacs	375	19.0	**375**	**19.1**	375	19.1	371	19.3	**371**	**19.3**	374	19.1
436.cactusADM	448	26.7	451	26.5	**450**	**26.5**	426	28.1	**428**	**27.9**	430	27.8
437.leslie3d	530	17.7	530	17.7	**530**	**17.7**	530	17.7	530	17.7	**530**	**17.7**
444.namd	510	15.7	510	15.7	**510**	**15.7**	**501**	**16.0**	501	16.0	504	15.9
447.dealII	655	17.5	**654**	**17.5**	654	17.5	611	18.7	**614**	**18.6**	616	18.6
450.soplex	513	16.2	514	16.2	**514**	**16.2**	509	16.4	**512**	**16.3**	513	16.2
453.povray	234	22.7	**234**	**22.7**	234	22.7	197	27.0	**198**	**26.8**	198	26.8
454.calculix	553	14.9	553	14.9	**553**	**14.9**	358	23.0	**361**	**22.8**	362	22.8
459.GemsFDTD	673	15.8	**674**	**15.8**	674	15.7	673	15.8	**674**	**15.8**	674	15.7
465.tonto	494	19.9	**494**	**19.9**	495	19.9	477	20.6	**481**	**20.5**	482	20.4
470.lbm	726	18.9	724	19.0	**725**	**18.9**	671	20.5	**676**	**20.3**	677	20.3
481.wrf	470	23.8	469	23.8	**469**	**23.8**	470	23.8	469	23.8	**469**	**23.8**
482.sphinx3	780	25.0	**780**	**25.0**	777	25.1	769	25.3	**775**	**25.1**	776	25.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
OMP_NUM_THREADS set to number of cores (default).
```

Platform Notes

```
BIOS default settings have been used.
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc8 -Qc99 ifort
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
```
-fast -Qparallel -F1000000000 libguide40.lib
```

C++ benchmarks:
```
-fast -Qparallel -Qcxx-features -F1000000000 libguide40.lib
shlW64M.lib -link -FORCE:MULTIPLE
```

Fortran benchmarks:
```
-fast -Qparallel -F1000000000 libguide40.lib
```

Benchmarks using both Fortran and C:
```
-fast -Qparallel -F1000000000 libguide40.lib
```

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F1000000000 libguide40.lib

470.lbm: -fast -Qunroll2 -Qscalar-rep- -Qprefetch -F1000000000 libguide40.lib

482.sphinx3: -fast -Qunroll2 -F1000000000 libguide40.lib

C++ benchmarks:

444.namd: -fast -Qcxx-features -Oa -F1000000000 libguide40.lib shlW64M.lib -link -FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -F1000000000 libguide40.lib shlW64M.lib -link -FORCE:MULTIPLE

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

416.gamess: basepeak = yes

434.zeusmp: -O2 -Qunroll0 -QxT -Qscalar-rep- -Qprec-div- -F1000000000 libguide40.lib

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -fast -Qunroll4 -Qauto -F1000000000 libguide40.lib

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F1000000000 libguide40.lib

436.cactusADM: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel -Qprefetch -Qunroll2 -F1000000000 libguide40.lib

454.calculix: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll-aggressive -F1000000000 libguide40.lib

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.02.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:16:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 January 2008.