SPEC® CINT2006 Result

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Fujitsu Siemens Computers

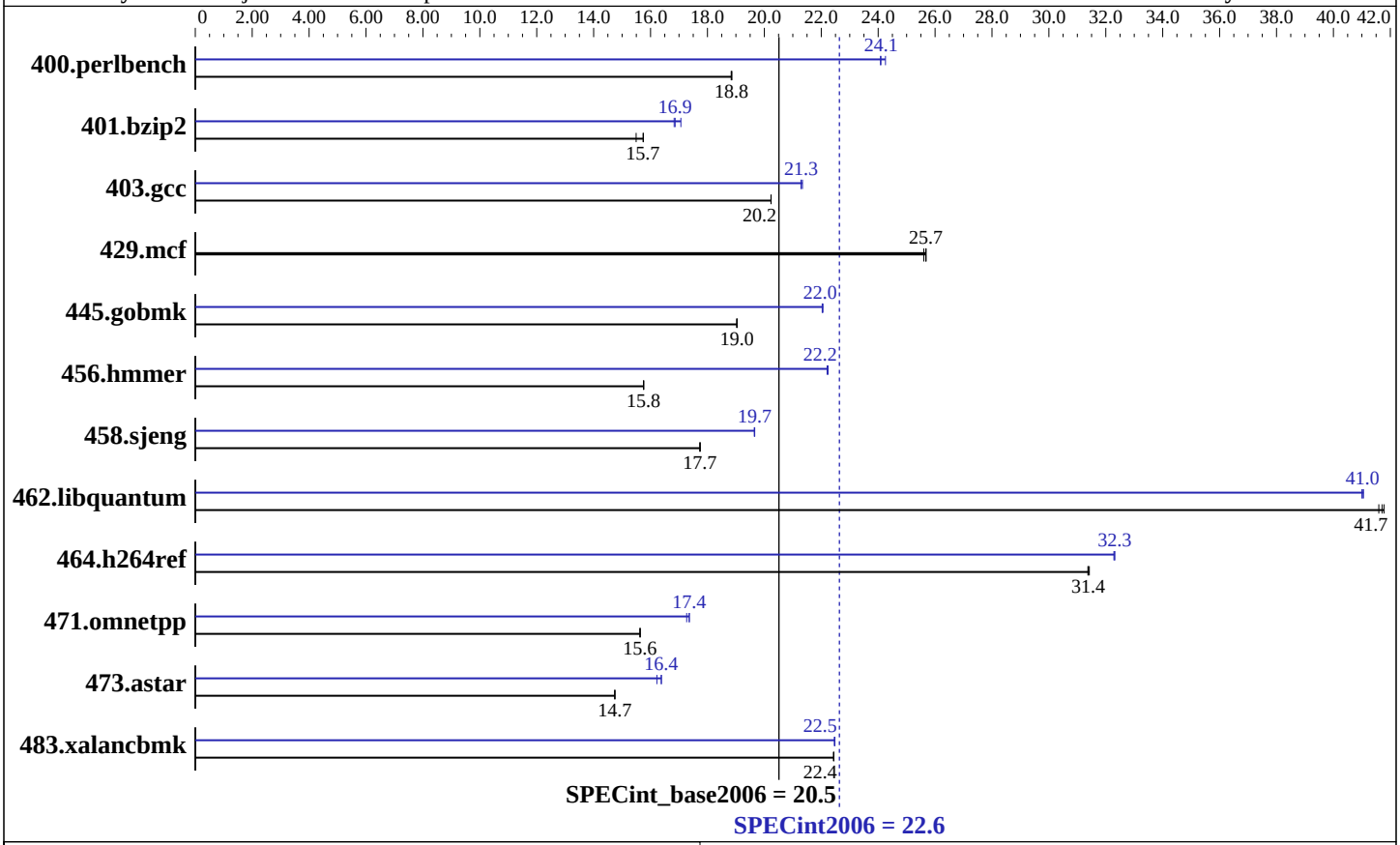
CELSIUS M460, Intel Core 2 Duo E6850 processor

SPECint®2006 = 22.6

SPECint_base2006 = 20.5

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Dec-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo E6850
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem:	1 x 400 GB SATA II 7200 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Vista Ultimate (x64)
Compiler:	Intel C++ Compilers for IA-32 and for Intel64, Version 10.1, Build 20070913 Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library, Version 8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	519	18.8	518	18.9	**518**	**18.8**	403	24.3	**405**	**24.1**	406	24.1
401.bzip2	**613**	**15.7**	613	15.8	623	15.5	**572**	**16.9**	573	16.8	565	17.1
403.gcc	398	20.2	**398**	**20.2**	398	20.2	377	21.3	378	21.3	**378**	**21.3**
429.mcf	356	25.6	**355**	**25.7**	355	25.7	356	25.6	**355**	**25.7**	355	25.7
445.gobmk	551	19.0	551	19.0	**551**	**19.0**	475	22.1	476	22.0	**476**	**22.0**
456.hmmer	592	15.8	592	15.8	**592**	**15.8**	420	22.2	**420**	**22.2**	420	22.2
458.sjeng	682	17.7	**682**	**17.7**	682	17.7	616	19.6	615	19.7	**615**	**19.7**
462.libquantum	496	41.8	**497**	**41.7**	498	41.6	**505**	**41.0**	505	41.1	505	41.0
464.h264ref	705	31.4	704	31.4	**705**	**31.4**	685	32.3	**685**	**32.3**	685	32.3
471.omnetpp	400	15.6	400	15.6	**400**	**15.6**	362	17.3	360	17.4	**360**	**17.4**
473.astar	**476**	**14.7**	476	14.7	476	14.7	433	16.2	**429**	**16.4**	429	16.4
483.xalancbmk	**308**	**22.4**	308	22.4	307	22.4	307	22.5	**307**	**22.5**	307	22.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
OMP_NUM_THREADS set to number of cores (default).
```

Platform Notes

```
BIOS default settings have been used.
```

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.


For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -Qparallel -Qvec-guard-write -Qpar-runtime-control -F512000000
libguide40.lib

C++ benchmarks:
-fast -Qcxx-features -F512000000 libguide40.lib shlW32M.lib
-link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icl -Qvc8 -Qc99

401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

456.hmmer: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

C++ benchmarks:
icl -Qvc8

Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_WIN32
456.hmmer: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel -Qpar-runtime-control -Qansi-alias -Qprefetch -F512000000 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch -F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000 libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -O2 -Qipo -QxT -Qprec-div- -Qansi-alias -F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive -F512000000 libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 -F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Qparallel -Qpar-runtime-control -Qprefetch -Qopt-streaming-stores:always -Ob0 -F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -Qansi-alias -Qopt-ra-region-strategy=block -F512000000 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -Qansi-alias -Qopt-ra-region-strategy=routine -F512000000 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx-features -Qansi-alias -F512000000 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.01.xml`

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